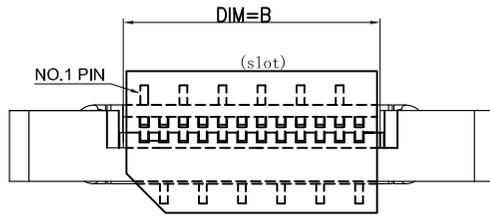
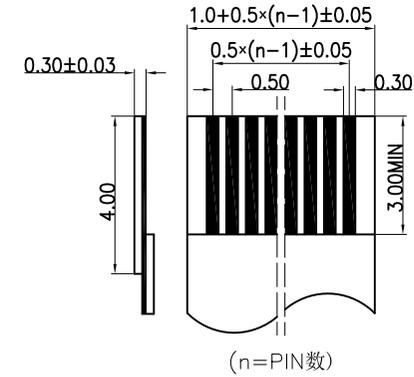
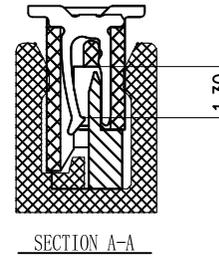
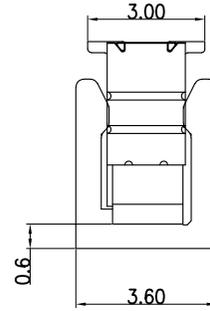
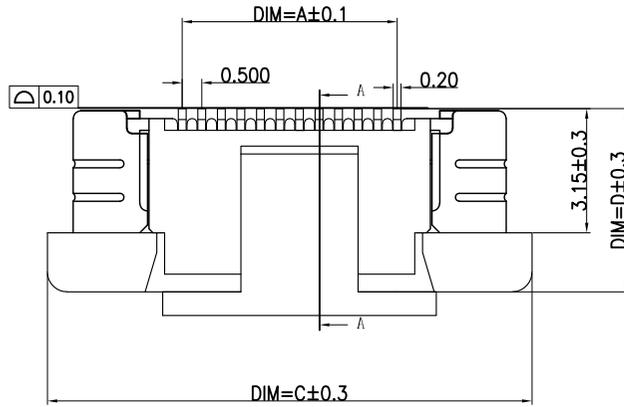


# GP Component

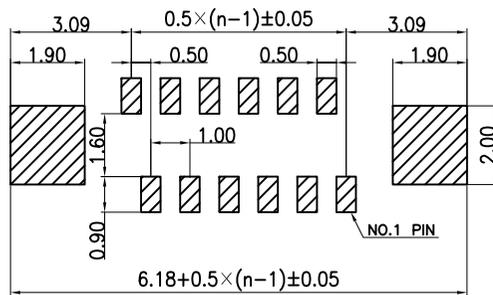
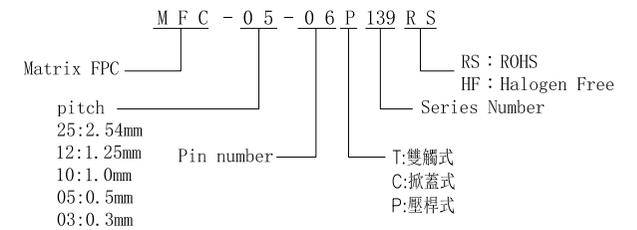
REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2024/03/26	Ken Lin



Electrical:  
 Voltage: 50 V AC(rms)/DC  
 Current: 0.5 A AC(rms)/DC  
 Contact Resistance: 40 mΩ Max(initial)  
 Dielectric Withstanding Voltage: 200 V AC/rms  
 Insulation Resistance: 500MΩ min  
 Operating Temp: -40°C~+85°C

PIN数	A	B	C	D
06	2.50	3.57	9.40	4.65

MATRIX PART NO:



RECOMMENDED P. C. B LAYOUT (COMPONENT SIDE)  
 (TOLERANCE=±0.05mm) WITHOUT LOCKING FUNCTION

(n=PIN数)



Matrix Electronics Co., Ltd

<b>TOLERANCE:</b> X:X ±0.40 X:XX ±0.30 X:XXX ±0.20 X:XXX ±0.10 ANGLE: ±3°	<b>DESIGN BY :</b> Ken Lin	<b>DATE :</b> 2024/03/26	<b>PART NAME:</b> FPC 0.5PITCH 立贴全包反位 CONNECTORS	
 <b>UNIT: mm [inch]</b> <b>SCALE:1:1 SIZE:A4</b>	<b>CHECKED BY:</b> Hanson Huang	<b>DATE :</b> 2024/03/26	<b>PART NO.</b>	MFC-05-06P139RS
	<b>APPROVED BY1:</b> Richard Hsieh	<b>DATE :</b> 2024/03/26	<b>MOLD NO.</b>	NA
	<b>APPROVED BY2:</b> Richard Hsieh	<b>DATE :</b> 2024/03/26	<b>DRAW NO.</b>	
			<b>SHEET NO.</b>	1 OF 1