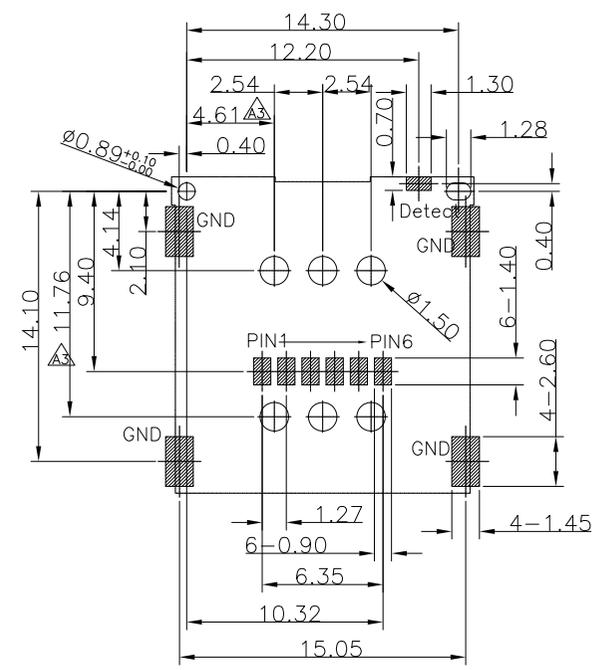
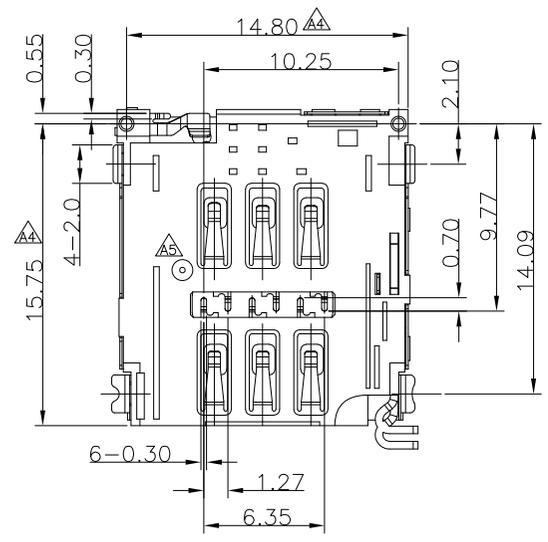
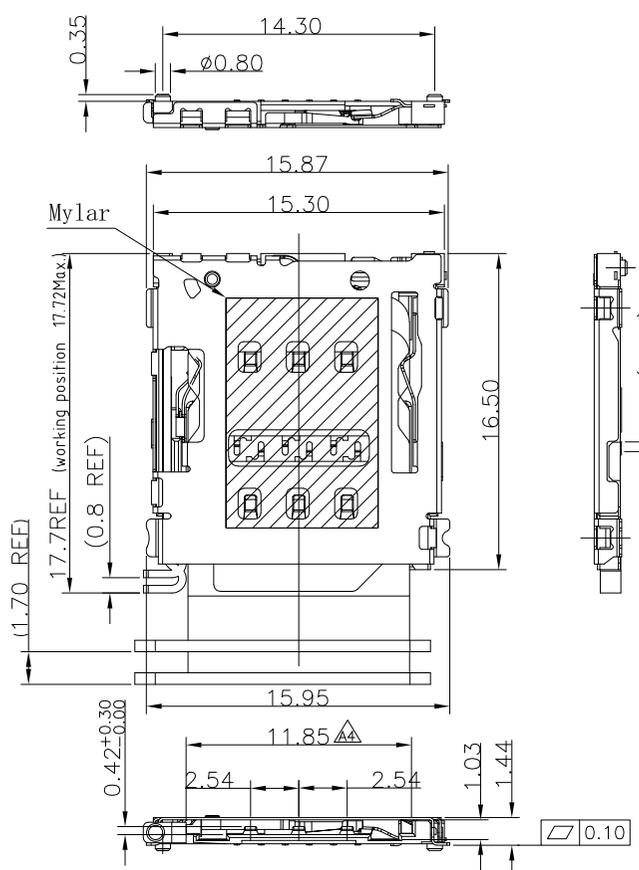


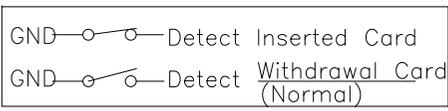
GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2018/11/26	Phebe Su
A1			Change	2019/04/21	Hanson
A2			Add dimension	2020/06/11	Hanson
A3/A4			Add dimension	2020/06/12	Hanson
A5			Add gate	2020/08/06	Hanson

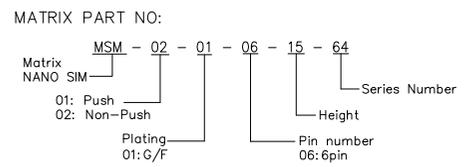


Recommended PCB Layout
Tolerance: ±0.05

- NOTE:
- Material & Finish See Table;
 - Circuit Diagram For Switch



- Characteristic:
 - Current Rating :1A max.
 - Contact Resistance:50mOhms max
 - Insulation Resistance:100MOhms min./500VDC
 - Dielectric Withstanding Voltage:500VAC/1minute
 - Mating Force: 10N Max, Withdrawal Force: 1N Min.;
 - Durability: 1500 Cycles
 - Operating Temperature:-40°C to+85°C



PIN #	Function
PIN 1#	C1: VCC
PIN 2#	C5: Grand
PIN 3#	C2: Reset
PIN 4#	C6: VPP
PIN 5#	C3: Clock
PIN 6#	C7: I/O

Item	Part Name	Material	Description
6	Shell	SUS304	Ni 30u" MIN. Plated Overall.
5	Lever	SUS304	No plating
4	Cam	SUS304	No plating
3	Housing	LCP	Black , UL94V-0
2	Detect	C5191	G/F Plated on Solder Tails and Contact Area; Ni 30u" MIN. Under Plated Overall.
1	Terminal	C5191	G/F Plated on Solder Tails and Contact Area; Ni 30u" MIN. Under Plated Overall.



Matrix Electronics Co.,Ltd

TOLERANCE:	DESIGN BY :	DATE :	PART NAME:
X.X ±0.38	Hanson Huang	2020/08/06	Nano SIM 带卡托
X.XX ±0.25	CHECKED BY:	DATE :	PART NO.
X.XXX ±0.15	Janice Liu	2020/08/06	MSM-02-01-06-15-64
ANGLE: ±5°	APPROVED BY1:	DATE :	MOLD NO.
	Richard Hsieh	2020/08/06	NA
	APPROVED BY2:	DATE :	DRAW NO.
	Richard Hsieh	2020/08/06	SHEET NO.
SCALE:1:1	SIZE:A4		1 OF 1