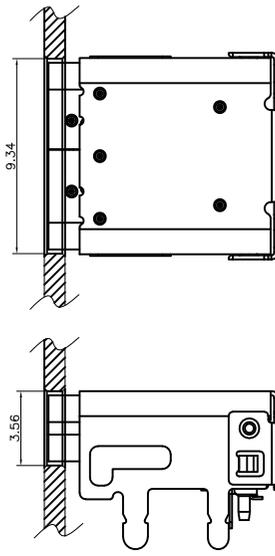
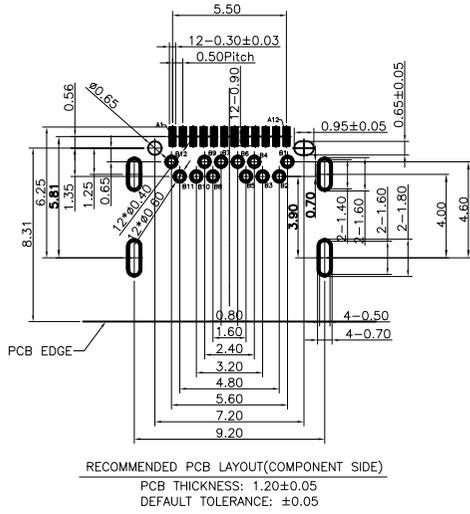
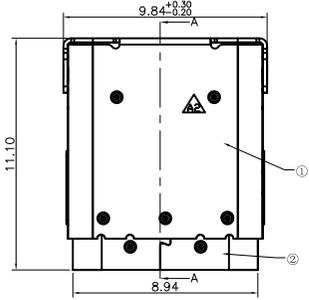
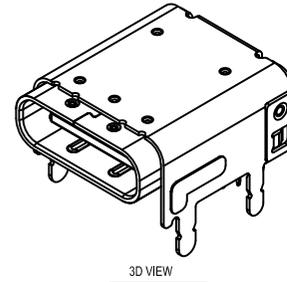
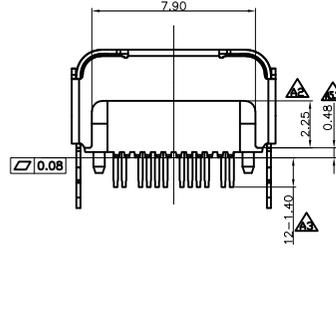
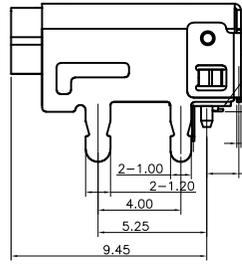
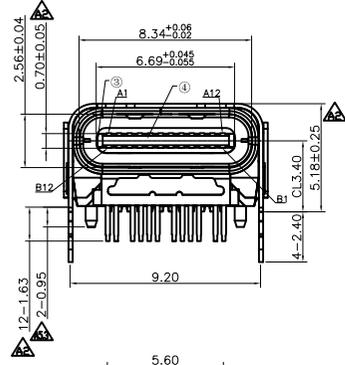


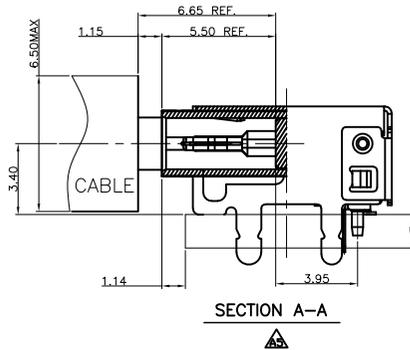
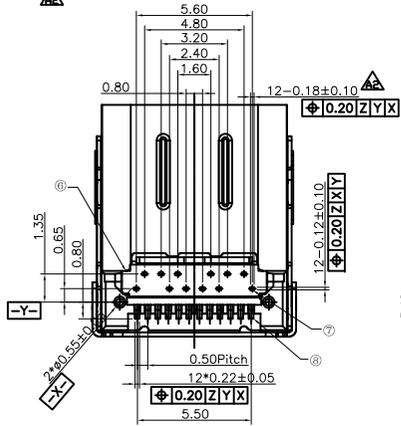
GP Component



RECOMMENDED PANEL CUTOUT



3D VIEW



SECTION A-A

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2019/11/03	Hanson
A1			Change Notes	2020/07/10	Hanson
A2			修改焊點	2020/08/13	Ginny Liao
A3			Change dimension	2020/09/15	Hanson
A4			Modify PIN tail	2020/12/17	Ginny Liao
A5			Add dimension	2021/05/22	Ken Lin
A5.1			Change VOLTAGE	2021/11/28	Ken Lin
A5.2			Add Drawing	2023/02/27	Ken Lin
A5.3			Add Drawing	2024/07/08	Ken Lin

NOTES:

- MECHANICAL:
 - INSERTION: 5~20N.
 - EXTRACTION: 8~20N.
 - DURABILITY: 10000 CYCLES
- ELECTRICAL:
 - CURRENT: 5A MIN FOR VBUS;
 - VOLTAGE: 20VAC MAX
 - WITHSTANDING VOLTAGE: 100V AC.
 - CONTACT RESISTANCE: 40mΩ MAX.
 - INSULATION RESISTANCE: 100MΩ MIN.
- ENVIRONMENTAL
 - TEMPERATURE RANGE -55°C ~ +85°C

MATRIX PART NO:

MUSB12-03-395

MATRIX TYPE-C

Series number

Plating
01:Gold Flash
03:3u"
15:15u"
30:30u"

USB TYPE-C FULL-FEATURED RECEPTACLE INTERFACE PIN ASSIGNMENTS

PIN	Signal Name	Description	PIN	Signal Name	Description
A1	GND	Ground return	B12	GND	Ground return
A2	SSTxp1	Positive half of first SuperSpeed TX differential pair	B11	SSRxp1	Positive half of first SuperSpeed RX differential pair
A3	SSTxn1	Negative half of first SuperSpeed TX differential pair	B10	SSRxn1	Negative half of first SuperSpeed RX differential pair
A4	Vbus	Bus Power	B9	Vbus	Bus Power
A5	CC1	Configuration Channel	B8	SBU2	Sideband Use (SBU)
A6	Dp1	Positive half of the USB 2.0 differential pair-Position 1	B7	Dn2	Negative half of the USB 2.0 differential pair-Position 2
A7	Dn1	Negative half of the USB 2.0 differential pair-Position 1	B6	Dp2	Positive half of the USB 2.0 differential pair-Position 2
A8	SBU1	Sideband Use (SBU)	B5	CC2	Configuration Channel
A9	Vbus	Bus Power	B4	Vbus	Bus Power
A10	SSRxp2	Negative half of second SuperSpeed RX differential pair	B3	SSTXn2	Negative half of second SuperSpeed TX differential pair
A11	SSRxp2	Positive half of second SuperSpeed RX differential pair	B2	SSTXp2	Positive half of second SuperSpeed TX differential pair
A12	GND	Ground return	B1	GND	Ground return

NO.	DESCRIPTION	QTY	MATERIAL	SPECIFICATION
8	UPPER CONTACT	12	COPPER ALLOY	Ni 50μ"Min AT TAIL, Tin AT SOLDER
7	LOWER CONTACT	12	COPPER ALLOY	AU 3μ" Min AT CONNECT AREA Ni 80μ"Min.
6	LOWER Insert Molding	1	THERMOPLASTIC UL 94V-0	COLOR: BLACK
5	UPPER Insert Molding	1	THERMOPLASTIC UL 94V-0	COLOR: BLACK
4	OVERMOLD	1	THERMOPLASTIC UL 94V-0	COLOR: BLACK
3	SHIELD PLATE	1	STAINLESS STEEL	NICKEL PLATING AT OVER ALL
2	INNER SHELL	1	STAINLESS STEEL	CLEAN
1	OUTER SHELL	1	STAINLESS STEEL	NICKEL PLATING ONLY AT TAIL,AU AT SOLDER TAIL



Matrix Electronics Co.,Ltd

TOLERANCE: X.X ±0.25 X.XX ±0.15 X.XXX ±0.05 ANGLE: ±2'	DESIGN BY : Ken Lin	DATE : 2024/07/08	PART NAME: USB 3.1 Gen 2 Type C Female R/A, Pad type, CL 3.4 后壳有缺口	
	CHECKED BY: Hanson Huang	DATE : 2024/07/08	PART NO.	MUSB12-03-395
	APPROVED BY1: Richard Hsieh	DATE : 2024/07/08	MOLD NO.	NA
	APPROVED BY2: Richard Hsieh	DATE : 2024/07/08	DRAW NO.	
SCALE:1:1	SIZE:A4		SHEET NO.	1 OF 1