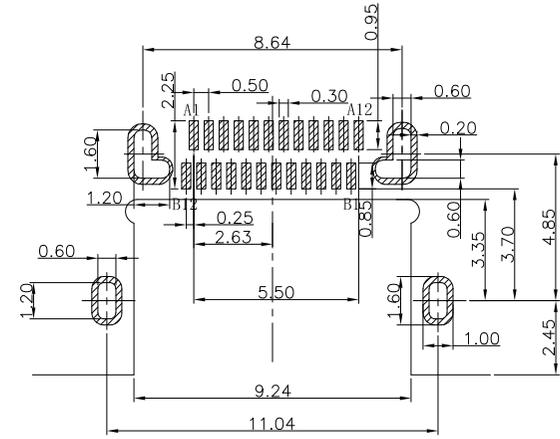
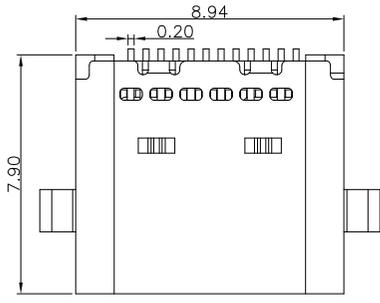
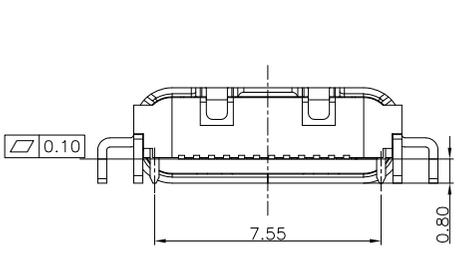
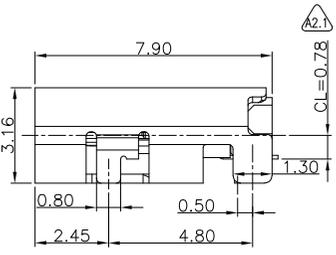
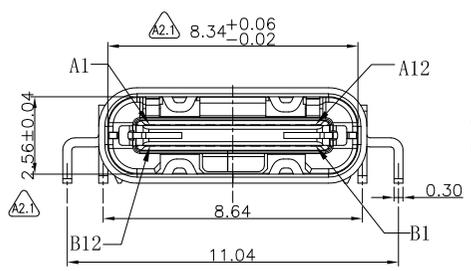


GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2017/05/03	Phebe Su
A1			Change Shell Type	2017/08/02	Summer Wang
A2			Change tolerance	2019/02/20	Phebe Su
A2.1			Change DIM.	2023/01/10	Ken Lin



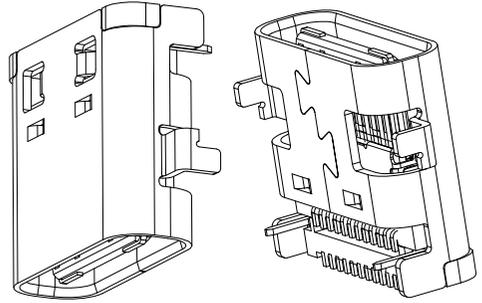
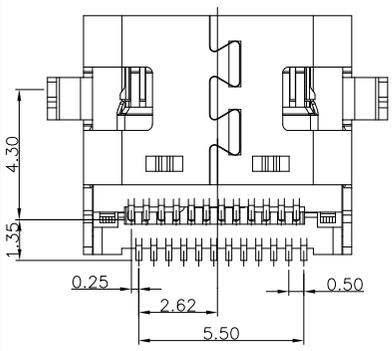
PCB LAYOUT (RECOMMENDED)
(Tol:±0.05)



- NOTES:
- MATERIAL:
 - MOLDING: LCP BLACK UL94 V-0
 - CONTACT: COPPER ALLOY.
 - G/F PLATED ON CONTACT AREA, 100u"
 - Min TIN (LEAD FREE) ON SOLDER AREA, 50u"
 - NICKEL PLATING OVER ALL.
 - SHILD: SUS304-H,T=0.12±0.03mm
 - MECHANICAL:
 - INSERTION: 5~20N.
 - EXTRACTION: 8~20N AFTER TEST.
 - DURABILITY: 10000 CYCLES
 - ELECTRICAL:
 - CURRENT: 5A FOR VBUS;
 - 0.25A FOR OTHER.
 - VOLTAGE: 20 V MAX
 - WITHSTANDING VOLTAGE: 100V AC R.M.S.
 - CONTACT RESISTANCE: 40mΩ MAX.
 - INSULATION RESISTANCE: 100MΩ MIN.
 - ENVIRONMENTAL
 - TEMPERATURE RANGE -40°C ~ +85°C

MATRIX PART NO:
MUSB12-01-157
 MATRIX USB ——— Series number

Plating
 01:Gold Flash
 15:15u"
 30:30u"



USB TYPE-C FULL-FEATURED RECEPTACLE INTERFACE PIN ASSIGNMENTS

PIN	Signal Name	Description	PIN	Signal Name	Description
A1	GND	Ground return	B12	GND	Ground return
A2	SSTXp1	Positive half of first SuperSpeed TX differential pair	B11	SSRXp1	Positive half of first SuperSpeed RX differential pair
A3	SSTXn1	Negative half of first SuperSpeed TX differential pair	B10	SSRXn1	Negative half of first SuperSpeed RX differential pair
A4	Vbs	Bus Power	B9	Vbs	Bus Power
A5	CC1	Configuration Channel	B8	SBI2	Sideband Use (SBU)
A6	Dp1	Positive half of the USB 2.0 differential pair-Position 1	B7	Dn2	Negative half of the USB 2.0 differential pair-Position 2
A7	Dn1	Negative half of the USB 2.0 differential pair-Position 1	B6	Dp2	Positive half of the USB 2.0 differential pair-Position 2
A8	SBU1	Sideband Use (SBU)	B5	CC2	Configuration Channel
A9	Vbs	Bus Power	B4	Vbs	Bus Power
A10	SSRXn2	Negative half of second SuperSpeed RX differential pair	B3	SSTXn2	Negative half of second SuperSpeed TX differential pair
A11	SSRXp2	Positive half of second SuperSpeed RX differential pair	B2	SSTXp2	Positive half of second SuperSpeed TX differential pair
A12	GND	Ground return	B1	GND	Ground return



Matrix Electronics Co.,Ltd

TOLERANCE: X.X ±0.25 X.XX ±0.15 X.XXX ±0.10 ANGLE: ±3°	DESIGN BY :	DATE :	PART NAME:
	Ken Lin	2023/01/10	USB 3.1 Gen 2 Type C Female R/A, Offset type, CL 0.78
UNIT: mm [inch] SCALE: 1:1 SIZE: A4	CHECKED BY:	DATE :	PART NO.
	Vicky Hsieh	2023/01/10	MUSB12-01-157
APPROVED BY1: APPROVED BY2:	DATE :	MOLD NO.	NA
	2023/01/10	2023/01/10	DRAW NO.
APPROVED BY: Richard Hsieh		DATE :	SHEET NO.
2023/01/10		2023/01/10	1 OF 1