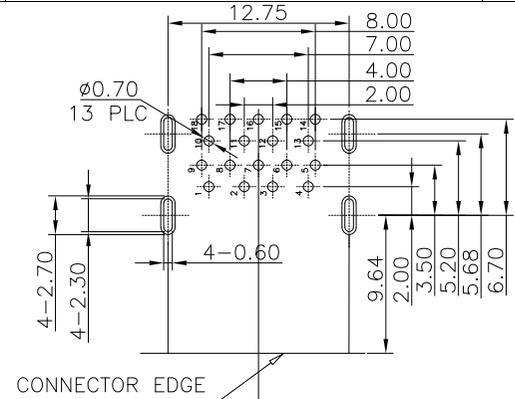
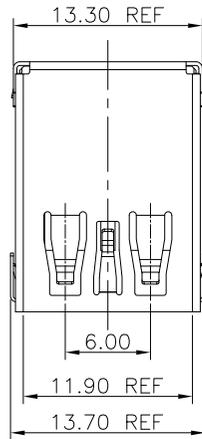


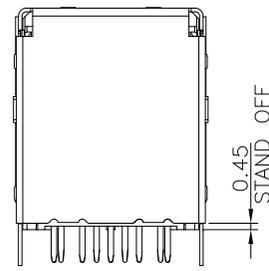
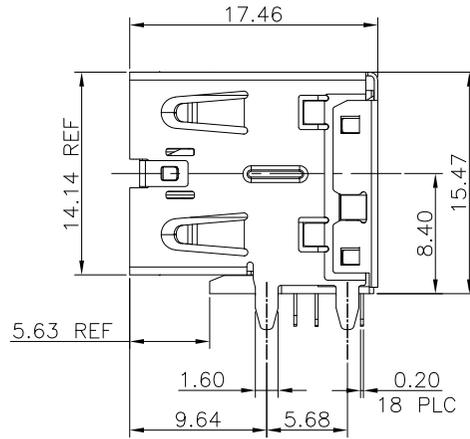
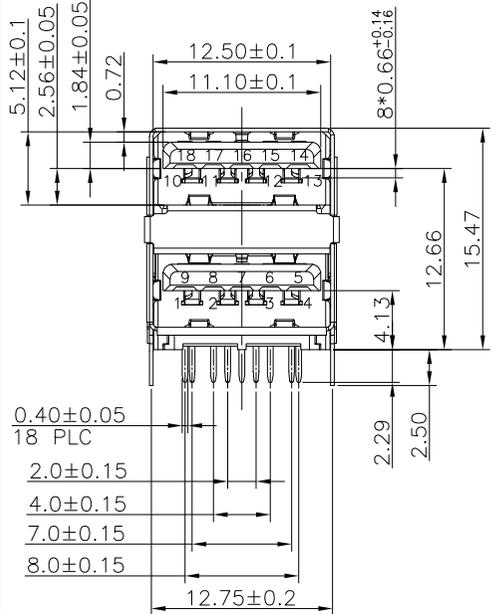
# GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2019/12/05	Phebe Su
A1			Modify	2020/03/06	Phebe Su
A2			Initial	2020/03/10	Phebe Su



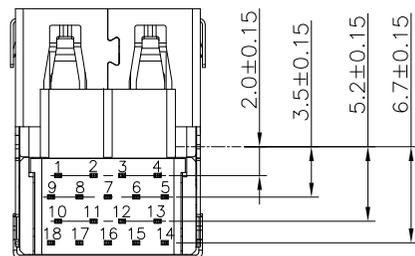
P.C.B. LAYOUT (COMPONENT SIDE)  
DEFAULT TOLERANCE: ±0.05  
(TOP VIEW)

- NOTES:
- MATERIAL:
    - HOUSING: LCP UL 94V-0, COLOR: BLACK.
    - 2.0 CONTACT: C2680R-EH T=0.20mm
    - 3.0 CONTACT: C2680R-EH T=0.20mm
    - SHELL: SUS201 T=0.25mm
    - MIDDLE SHELL: SUS201 T=0.20mm
  - FINISH:
    - CONTACT: GOLD FLASH ON CONTACT AREA, 100u" TIN PLATED ON SOLDER TALLS, 50u" MIN. NICKEL UNDERPLATING OVER ALL.
    - SHELL: 80u" MIN. NICKEL UNDERPLATING OVER ALL.
  - ELECTRICAL:
    - CONTACT RESISTANCE: 30 mΩ (MAX) INITIAL FOR VBUS AND GND CONTACTS 50 mΩ (MAX) INITIAL FOR OTHER CONTACTS
    - CURRENT RATING: 1.5Amps MAX PER PIN
    - DIELECTRIC WITHSTANDING VOLTAGE: 100V AC FOR 1 MINUTE.
    - INSULATION RESISTANCE: 1000 MΩ MIN



MATRIX PART NO:  
MUSB 18 - 01 - 376

Matrix-USB      Pin number      Gold Plating:  
01=Gold Flash  
15=15u"  
30=30u"



USB 2.0 CONTACT PINS		USB 3.0 CONTACT PINS	
Pin NUMBER	SIGNAL NAME	Pin NUMBER	SIGNAL NAME
1	10 VBUS	5	14 StdA_SSRX-
2	11 D-	6	15 StdA_SSRX+
3	12 D+	7	16 GND_DRAIN
4	13 GND	8	17 StdA_SSTX-
Shell	Shield	9	18 StdA_SSTX+

**Matrix Electronics Co., Ltd**  
深圳市越洋達科技有限公司

TOLERANCE: X.X ±0.30 X.XX ±0.20 X.XXX ±0.05 ANGLE: ±3°	DESIGN BY : Phebe Su	DATE : 2020/03/10	PART NAME: USB 3.0 Double Stacked H=15.47mm CH=8.40mm
APPROVED BY1: Richard Hsieh	CHECKED BY: Janice Liu	DATE : 2020/03/10	PART NO. MUSB18-01-376
APPROVED BY2: Richard Hsieh	DATE : 2020/03/10	MOLD NO. NA	DRAW NO.
SCALE: 1:1    SIZE: A4	DATE : 2020/03/10	SHEET NO. 1 OF 1	