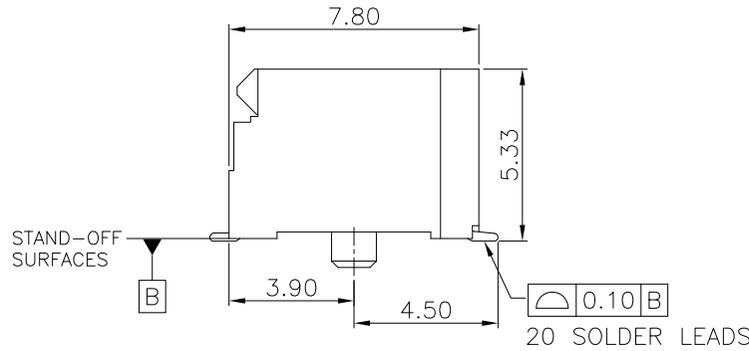
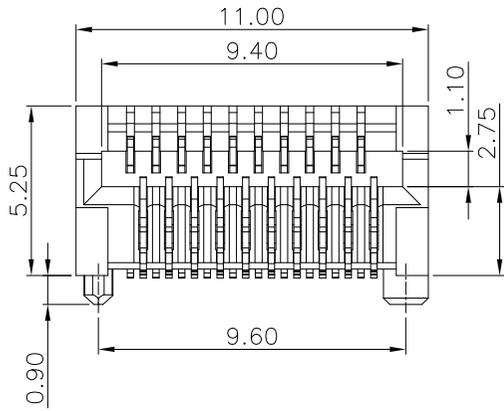
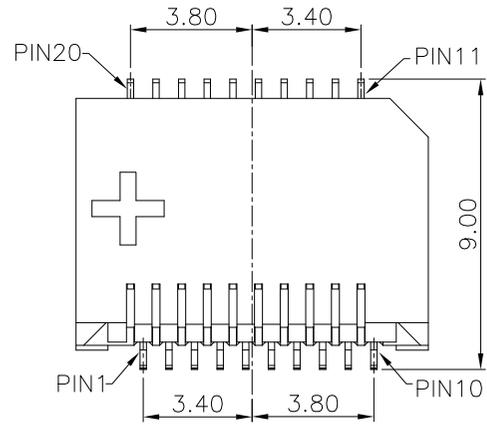


# GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2017/07/06	Phebe Su
A1			Modify Tolerance	2025/06/09	Vince



### NOTES:

#### 1.MATERIAL:

HOUSING: LCP UL94V-0, BLACK

CONTACT: C5210R-EH, t=0.20mm

#### 2.DURABILITY TEST:100 CYCLES MIN.

#### 3.SPECIFICATION:

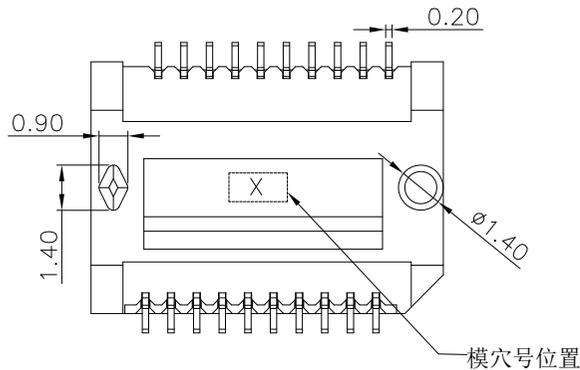
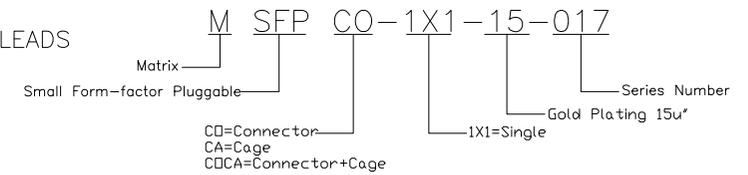
DIELECTRIC STRENGTH: AC 300V/MINUTE

INSULATOR RESISTANCE: 500 Mohm MIN

CONTACT RESISTANCE: 40 mohm Max.

OPERATING TEMPERATURE: -40°~+85°

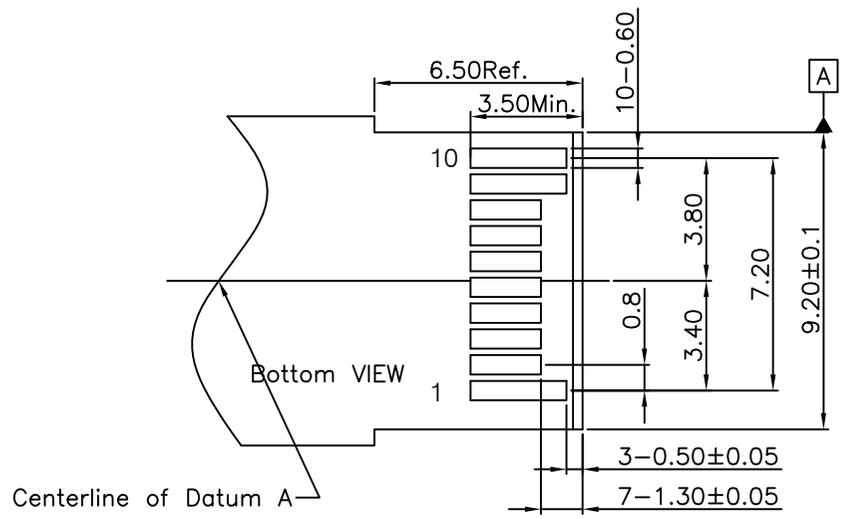
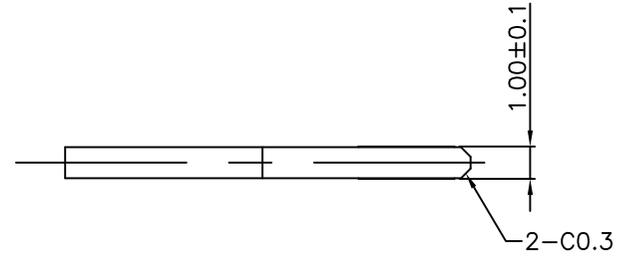
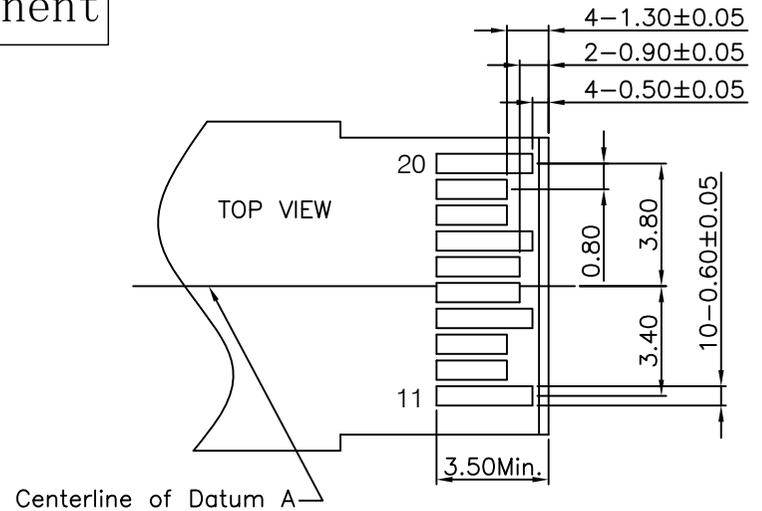
### MATRIX PART NO:



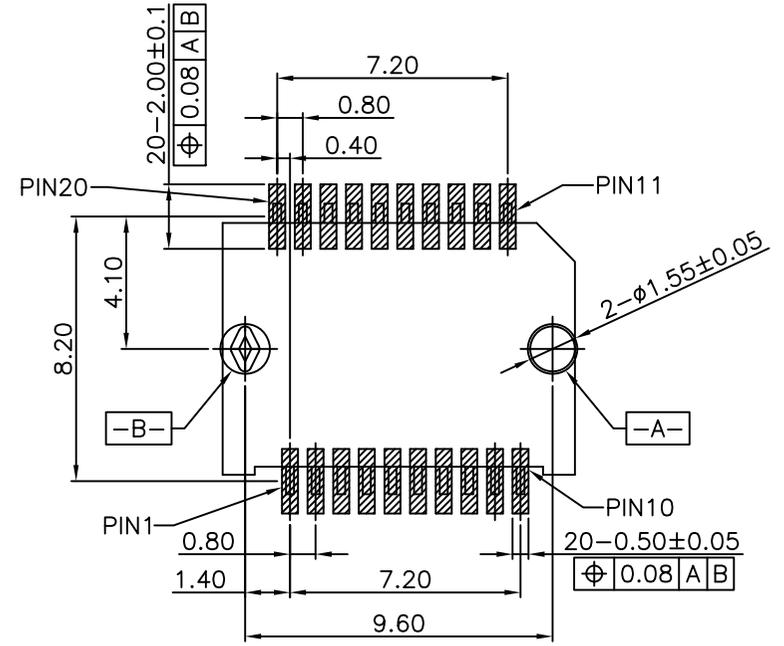
Matrix Electronics Co.,Ltd			
TOLERANCE: X:X ±0.35 X:XX ±0.25 X:XXX ±0.15 ANGLE: ±3°	DESIGN BY : Vince Chen	DATE : 2025/06/09	PART NAME: SFP+ 1X1 Conn
 UNIT: mm [inch] SCALE:1:1 SIZE:A4	CHECKED BY: Hanson Huang	DATE : 2025/06/09	PART NO. MSFFPCO-1X1-15-017
	APPROVED BY1: Richard Hsieh	DATE : 2025/06/09	MOLD NO. NA
	APPROVED BY2: Richard Hsieh	DATE : 2025/06/09	DRAW NO.
			SHEET NO. 1 OF 2

# GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2017/07/06	Phebe Su
A1			Modify Tolerance	2025/06/09	Vince



RECOMMENDED LAYOUT  
MATING PCB SIDE



RECOMMENDED PCB LAYOUT(TOP VIEW)  
(ALL TOLERANCES ARE ±0.05)



Matrix Electronics Co.,Ltd

TOLERANCE:	DESIGN BY :	DATE :	PART NAME:	
X:X ±0.35	Vince Chen	2025/06/09	SFP+ 1X1 Conn	
X:XX ±0.25	CHECKED BY:	DATE :	PART NO.	MSFPCO-1X1-15-017
X:XXX ±0.15	Hanson Huang	2025/06/09	MOLD NO.	NA
ANGLE: ±3°	APPROVED BY1:	DATE :	DRAW NO.	
	Richard Hsieh	2025/06/09	SHEET NO.	2 OF 2
UNIT: mm [inch]	APPROVED BY2:	DATE :		
SCALE:1:1	Richard Hsieh	2025/06/09		