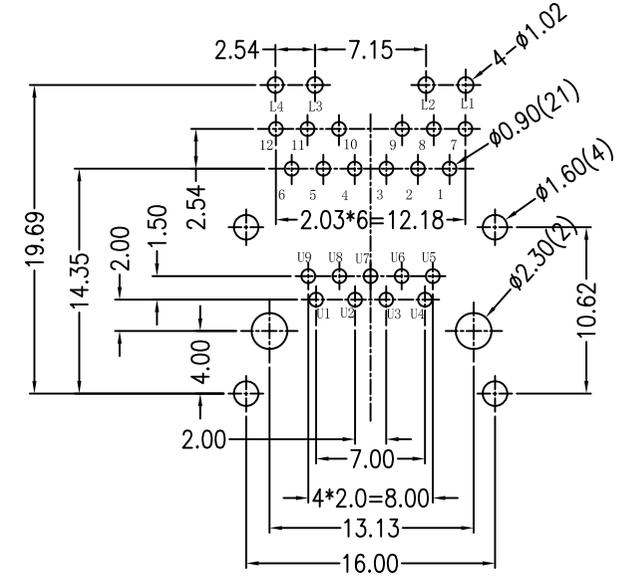
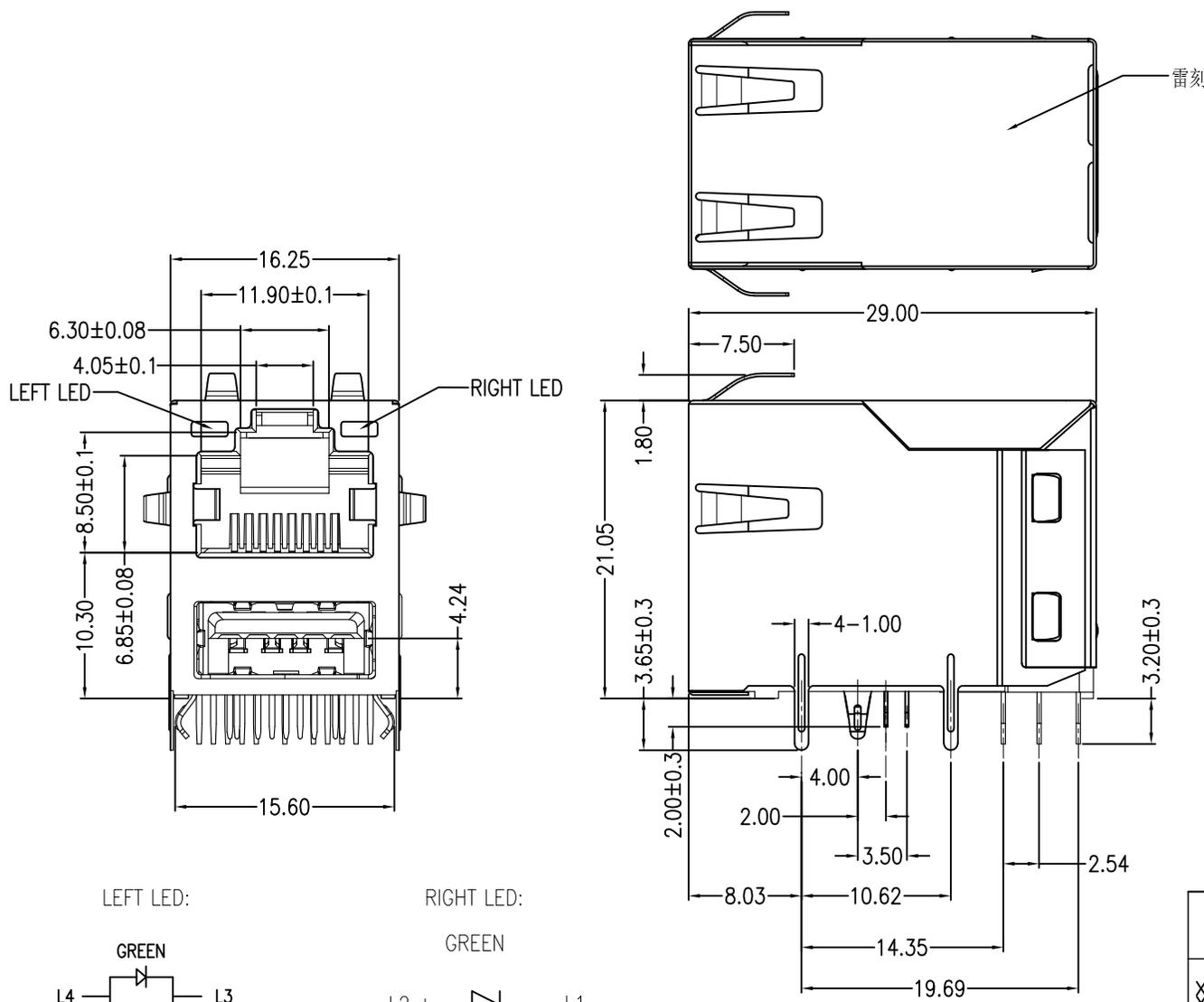


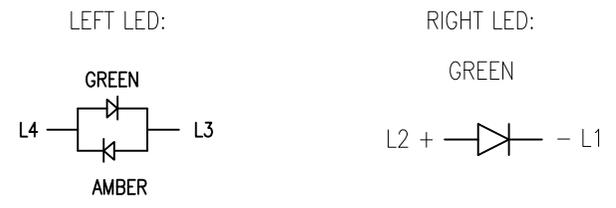
REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
AO			Initial	2022/08/14	Vince

1. MECHANICAL :



- NOTES:
- MATERIAL:
HOUSING:PBT BLACK(UL94V-0)
CONTACT:PHOSPHOR BORNZE
SHELL:STAINLESS STEEL T=0.20
 - FINISHED:
CONTACT:50u"MIN. NICKEL UNDERPLATING ALL AREA,100u"MIN. TIN ON SOLDER TAILS,SELECT G/F PLATING ON CONTACT AREA.
SHELL:30u"MIN. NICKEL PRE-PLATING ALL AREA
 - USB MECHANICAL:
HOUSING:PBT BLUE(UL94V-0)
CONTACT:PHOSPHOR BORNZE G/F PLATING ON CONTACT AREA
SHELL:SPCC
 - JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68, SUBPART F

RECOMMENED PCB LAYOUT
COMPONENT SIDE
TOLERANCE ±0.05



LED CONFIGURATION :

Matrix Electronics Co.,Ltd			
TOLERANCE: X.X ±0.38 X.XX ±0.25 X.XXX ±0.13 X.* ±2* X.X* ±1*	DESIGN BY : Vince Chen	DATE : 2022/08/14	PART NAME: RJ45+SINGLE PORT USB 3.1 GEN1 GIGABIT W/SHELL & LED
	CHECKED BY: Hanson Huang	DATE : 2022/08/14	PART NO. MRJG-1UT1665RS
	APPROVED BY1: Richard Hsieh	DATE : 2022/08/14	MOLD NO. NA
	APPROVED BY2: Richard Hsieh	DATE : 2022/08/14	DRAW NO.
UNIT: mm [inch] SCALE:1:1 SIZE:A4			SHEET NO. 1 OF 2

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
AO			Initial	2022/08/14	Vince

2. SCHEMATIC:

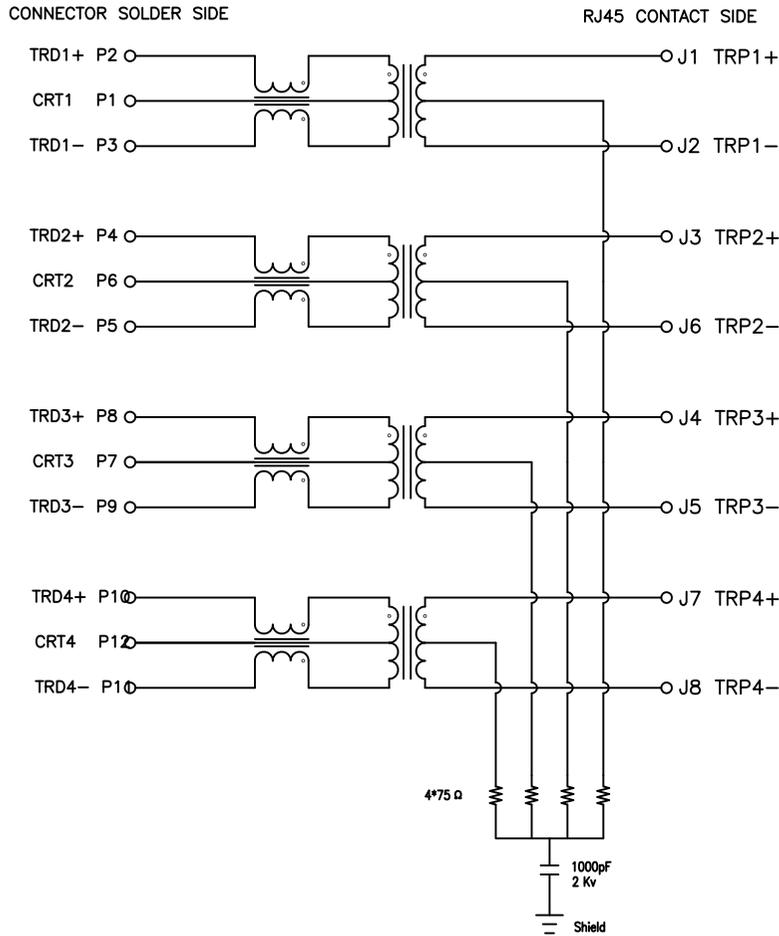
3. ELECTRICAL CHARACTERISTICS :

TEST NOTES:(25°±5°C)

- TR:(100KHz,0.1V); 100%
PINS:(P2,P3):(J1,J2)=1:1±3%;(P4,P5):(J3,J6)=1:1±3%
PINS:(P8,P9):(J4,J5)=1:1±3%;(P10,P11):(J7,J8)=1:1±3%
- LX:(100KHz,100mV,8mA, DC Bias) 100%
PINS:(J1,J2),(J3,J6)=350uH MINIMUM
PINS:(J4,J5),(J7,J8)=350uH MINIMUM
- DCR: 100%
PINS:(J1,J2),(J3,J6)=1.2 Ω MAXIMUM
PINS:(J4,J5),(J7,J8)=1.2 Ω MAXIMUM
- HIPOT: 100%
PINS(P2,P3)TO(J1,J2),(P4,P5)TO(J3,J6)=1500VAC FOR 60 SECONDS
PINS(P8,P9)TO(J4,J5),(P10,P11)TO(J7,J8)=1500VAC FOR 60 SECONDS
- INSERTION LOSS:
-1.0dB MAXIMUM AT 1MHz TO 100MHz;
- RETURN LOSS:
-18dB MINIMUM AT 1MHz TO 30MHz;
-16dB MINIMUM AT 30MHz TO 60MHz
-12dB MINIMUM AT 60MHz TO 80MHz
-10dB MINIMUM AT 80MHz TO 100MHz
- CROSS TALK:
-30dB MINIMUM AT 1MHz TO 100MHz
- COMMON TO COMMON MODE REJECTION:
-30dB MINIMUM AT 1MHz TO 100MHz

USB 3.1 Standard-A Type Pin Assignment:

Pin No.	Signal Name	Description
U1	VBUS	Power
U2	Data -	USB 2.0 Differential Pair
U3	Data +	USB 2.0 Differential Pair
U4	GND	Ground For Power Return
U5	Std A SSRX-	Super Speed Receiver Differential Pair
U6	Std A SSRX+	Super Speed Receiver Differential Pair
U7	GND Drain	Ground For Signal Return
U8	Std A SSTX-	Super Speed Receiver Differential Pair
U9	Std A SSTX+	Super Speed Receiver Differential Pair
SHELL	Shield	Connector Metal Shell



MATRIX PART NO:

M R J G - 1U T 1665 RS

RS: ROHS
HF: Halogen Free
Series Number

Matrix-RJ45

F: 10/100
G: 10/100/1000
N: Pure Connector

11:1X1 ; 21:2X1
12:1X2 ; 22:2X2
14:1X4 ; 24:2X4
16:1X6 ; 26:2X6
18:1X8 ; 28:2X8
1U:RJ+USB

S: SMD
T: THT

Printing:
MATRIX
MRJG-1UT1665RS
YYWW



Matrix Electronics Co.,Ltd

TOLERANCE: X.X ±0.38 X.XX ±0.25 X.XXX ±0.13 X.* ±2° X.X* ±1°	DESIGN BY : Vince Chen	DATE : 2022/08/14	PART NAME: RJ45+SINGLE PORT USB 3.1 GEN1 GIGABIT W/SHELL & LED	
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	APPROVED BY2: Richard Hsieh	DATE : 2022/08/14	DRAW NO.	
			SHEET NO.	2 OF 2