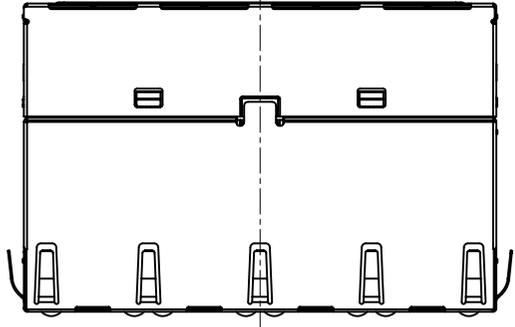


GP Component

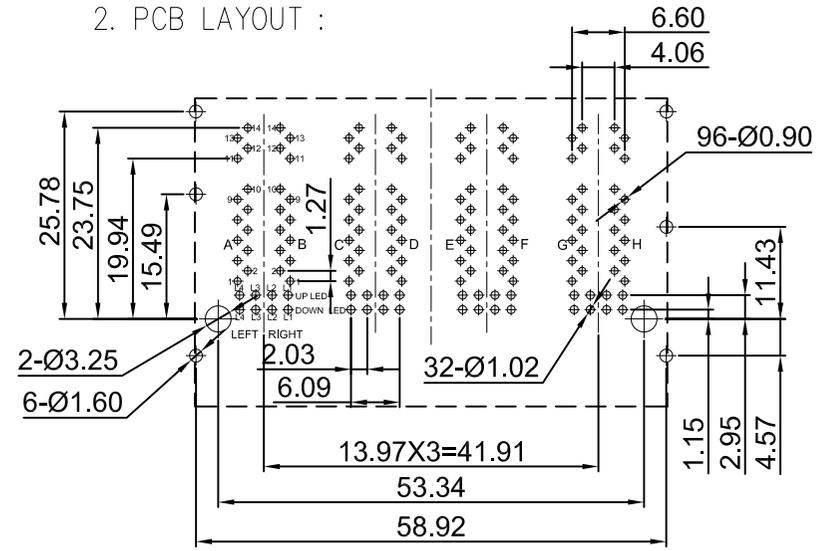
REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2022/12/06	Vince

1. MECHANICAL DIMENSIONS :



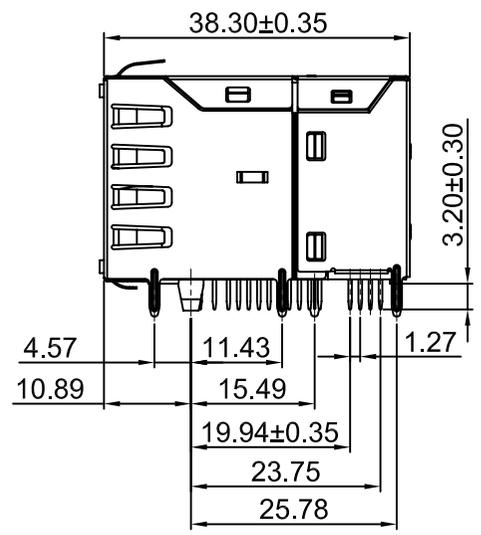
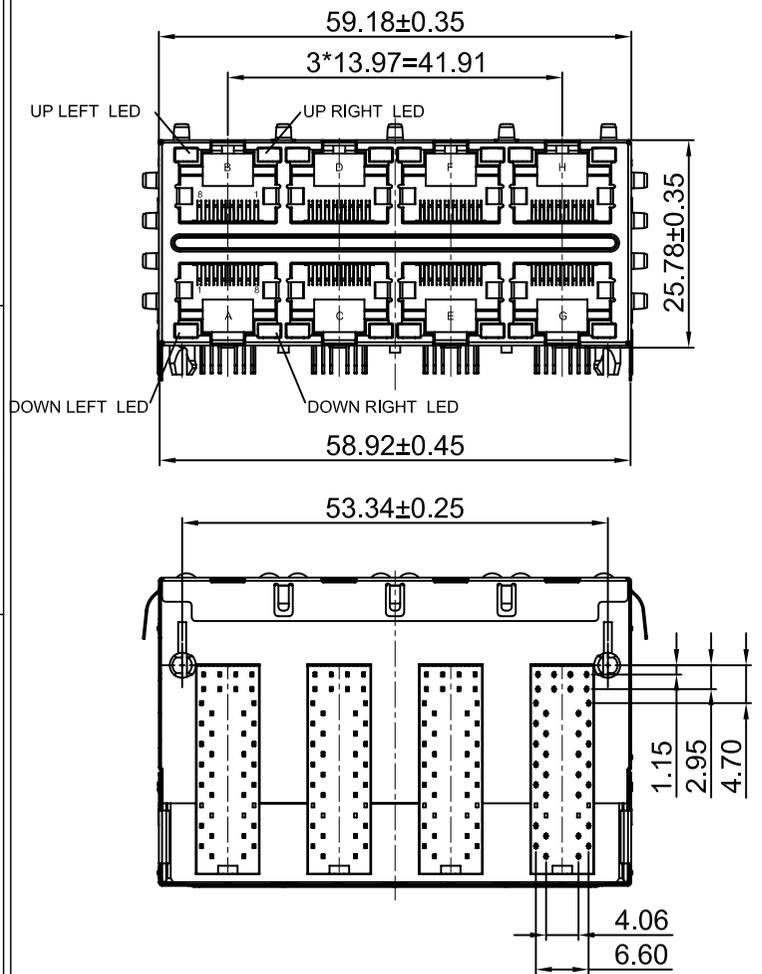
Printing:
MATRIX
MRJ2G-24T1741TRS
YYWW

2. PCB LAYOUT :



Suggested PCB Layout(Top View)
 (Tolerance:±0.05)

1. Material:
- 1.1 Housing: PA46 UL94V-0 Black.
 - 1.2 Insert core and Cover:PA46 UL94V-0 Black.
 - 1.3 Contact :Phosphor Bronze,Gold plated on contact area 30u",Tin plated on solder area 80u" min, overall nickel plating 30u" min.
 - 1.4 Input Terminal : Brass,Tin plated on solder area 80u" min, Ni underplated overall 30u" min.
 - 1.5 Metal shielding:Copper alloy with Nickel plated.
- 2.Environmental requirements:
- 2.1 Lead-free process,compliant RoHS.



MATRIX PART NO:
MRJ 2G-24 T 1741 T RS

Matrix-RJ45
 F:10/100
 G:10/100/1G/2.5G
 N:Pure Connector
 11:1X1 ; 21:2X1
 12:1X2 ; 22:2X2
 14:1X4 ; 24:2X4
 16:1X6 ; 26:2X6
 18:1X8 ; 28:2X8
 1U:RJ+USB

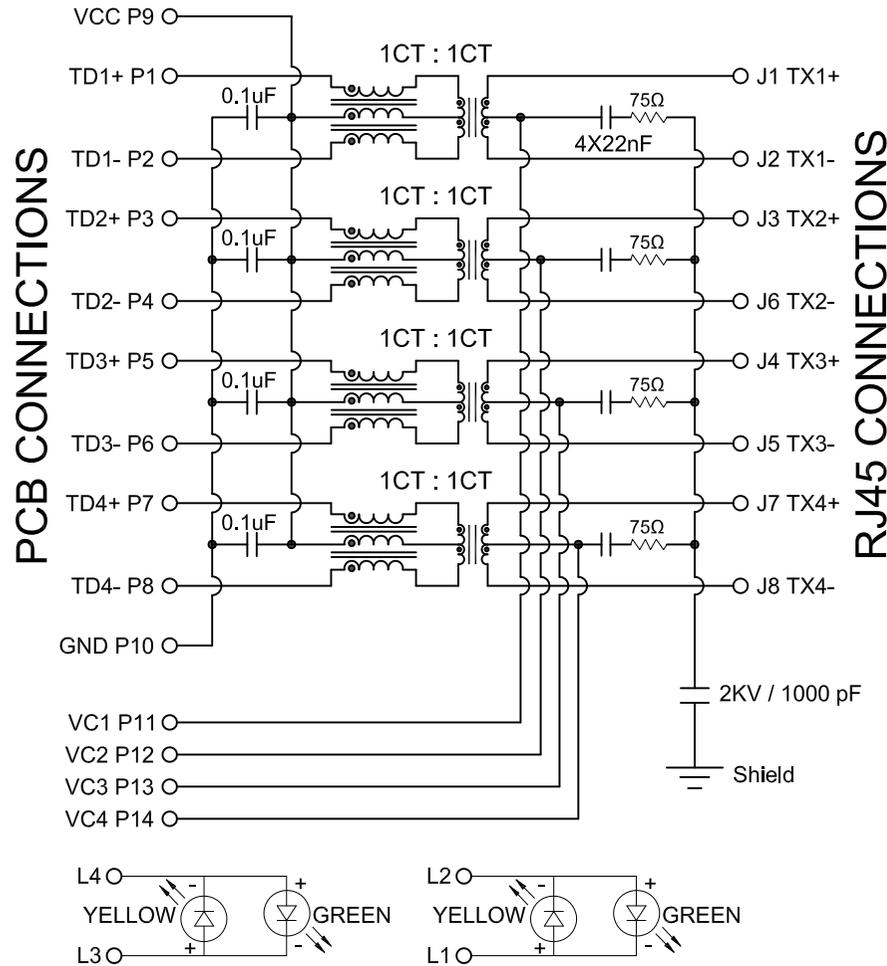
S:SMD
 T:THT

RS:ROHS
 HF:Halogen Free
 Industrial Temperature
 Series Number

Matrix Electronics Co., Ltd			
TOLERANCE: X: X X.X ±0.38 X.XX ±0.25 X.XXX ±0.13 ANGLE: ±3°	DESIGN BY : Vince Chen	DATE : 2022/12/06	PART NAME: RJ45 2x4 2.5G Base With POE DIP Double W/LED
	CHECKED BY: Hanson Huang	DATE : 2022/12/06	PART NO. MRJ2G-24T1741TRS
 UNIT: mm [inch] SCALE:1:1 SIZE:A4	APPROVED BY1: Richard Hsieh	DATE : 2022/12/06	MOLD NO. NA
	APPROVED BY2: Richard Hsieh	DATE : 2022/12/06	DRAW NO. SHEET NO. 1 OF 3

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2022/12/06	Vince

3. SCHEMATIC:



4. Electrical Specification @25°C:

- 4.1 Inductance: 180uH Min With 8mA DC Bias.
- 4.2 Hi-Pot Test: Input to Output 2250VDC/1mA.
- 4.3 Insertion loss :
 - @1-50MHZ -0.8dB max.
 - @50-125MHZ -1.2dB max.
 - @125-200MHZ -2.0dB max.
- 4.4 Return loss :
 - @1-40MHZ -18dB min.
 - @40-200MHZ -18+15*log(Freq MHz/40MHz) dB Min.
- 4.5 Common to Common Mode Rejection:
 - @1-200MHZ -30dB min.
- 4.6 Cross Talk:
 - @1-40MHZ -35dB min.
 - @40-125MHZ -35+15*log(Freq MHz/40MHz) dB Min.
- 5. Operating and Storage Temperature:
 - 5.1 Operating Temperature : -40°C to +85°C.
 - 5.2 Storage Temperature : -40°C to +85°C.
- 6. Wave soldering peak temperature: 260°C.
- 7. DC current: Meeting IEEE802.3bt Standards.
 - 1A MAX @57VDC Continuous.
 - Supports of 90W lines.

	LEFT LED		RIGHT LED
	Emitting Color	λ p(nm)	Vf@If=20mA
	GREEN	565	1.8-2.5V
	YELLOW	585	1.8-2.5V

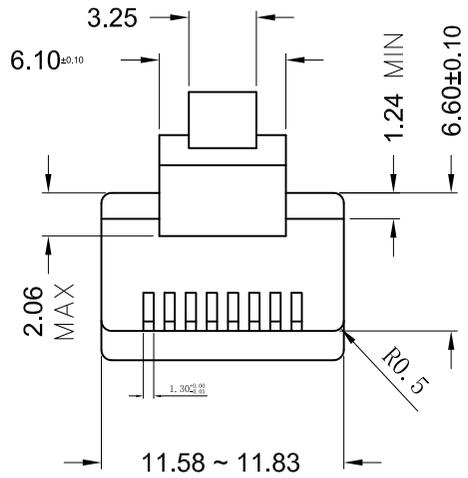


Matrix Electronics Co., Ltd

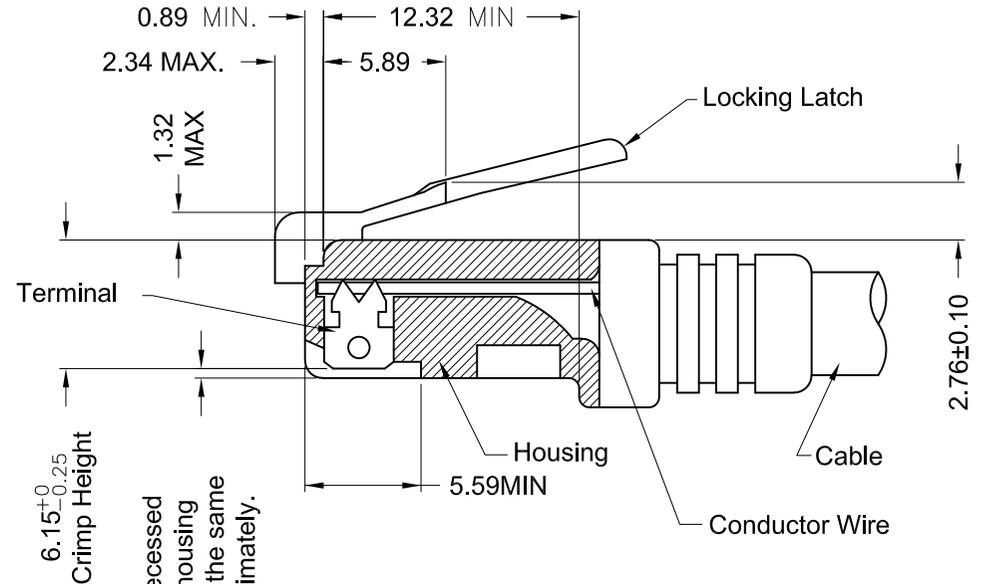
TOLERANCE: X: X X.X ±0.38 X.XX ±0.25 X.XXX ±0.13 ANGLE: ±3°	DESIGN BY :	DATE :	PART NAME:	
	Vince Chen	2022/12/06	RJ45 2x4 2.5G Base With POE DIP Double W/LED	
 UNIT: mm [inch] SCALE: 1:1 SIZE: A4	CHECKED BY:	DATE :	PART NO.	MRJ2G-24T1741TRS
	Hanson Huang	2022/12/06	MOLD NO.	NA
	APPROVED BY1:	DATE :	DRAW NO.	
	Richard Hsieh	2022/12/06	SHEET NO.	2 OF 3
	APPROVED BY2:	DATE :		
	Richard Hsieh	2022/12/06		

GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2022/12/06	Vince

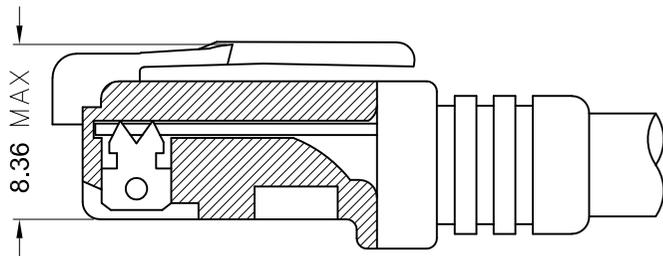


* There must be no damage to housing or locking latch. There must be no nicks or cuts in cable.
 * Durability : 750 cycles generally



All contacts recessed below top of housing and must be at the same height approximately.

FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F FIGURE 68.500 (C)(2)(i) AND IEC 603-7 FIGURE 23 & 24



FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F FIGURE 68.500 (C)(2)(ii)

STANDARD MODULAR PLUG ASSEMBLY

 Matrix Electronics Co., Ltd				
TOLERANCE: X ±0.38 X.X ±0.25 X.XX ±0.13 ANGLE: ±3°	DESIGN BY : Vince Chen	DATE : 2022/12/06	PART NAME: RJ45 2x4 2.5G Base With POE DIP Double W/LED	
	CHECKED BY: Hanson Huang	DATE : 2022/12/06	PART NO.	MRJ2G-24T1741TRS
  UNIT: mm [inch] SCALE:1:1 SIZE:A4	APPROVED BY1: Richard Hsieh	DATE : 2022/12/06	MOLD NO.	NA
	APPROVED BY2: Richard Hsieh	DATE : 2022/12/06	DRAW NO.	
			SHEET NO.	3 OF 3