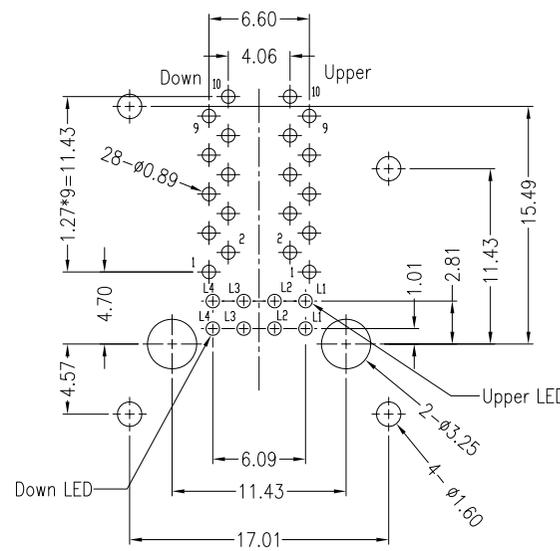
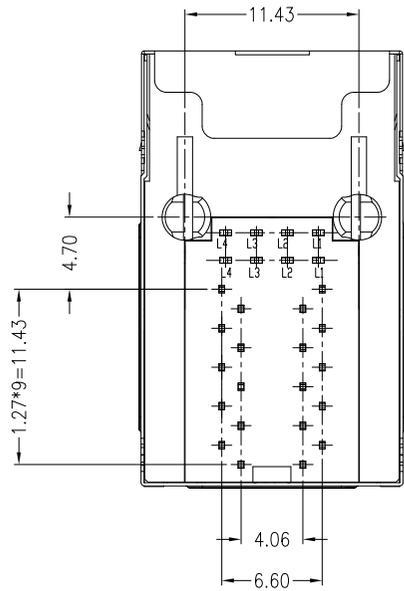
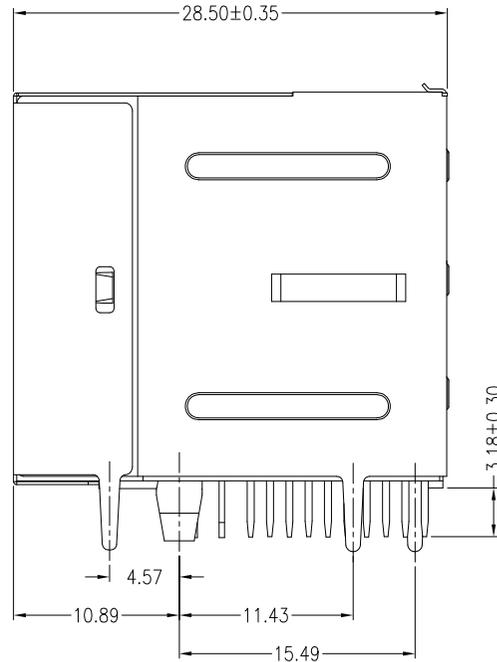
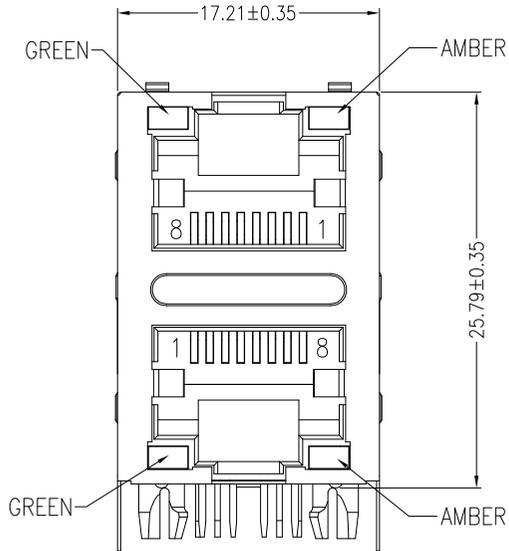


GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
AO			Initial	2020/02/17	Phebe Su

1. MECHANICAL :

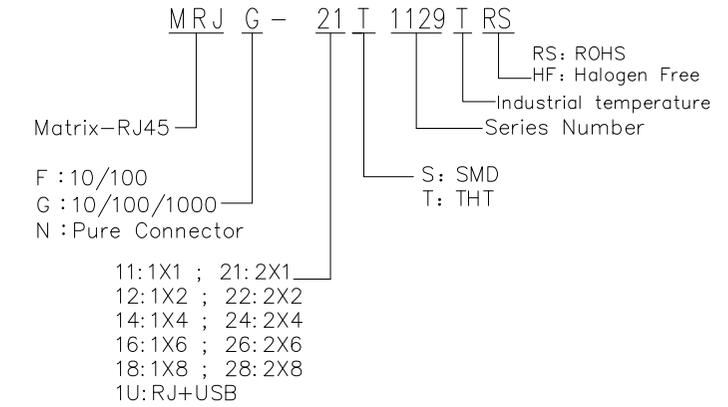


RECOMMENED PCB LAYOUT(COMPONENT SIDE)
TOLERANCE ±0.05

Printing:
MATRIX
MRJG-21T1129TRS
YYWW

- NOTES:
- MATERIAL:
HOUSING: PA66,UL94V-0,COLOR:BLACK;
SHELL: COPPER ALLOY T=0.20mm;
RJ45 TERMINAL: PHOSPHOR BRONZE T=0.30mm
PLATING: G/F PLATED ON CONTACT AREA,
100u" TIN PLATED ON SOLDER AREA,
50u"~80u" NI UNDERPLATED OVERALL;
TAIL TERMINAL: BRASS T=0.40mm
PLATING: 60u"~100u" TIN PLATED OVER,
30u"~50u" NI UNDERPLATED OVERALL.
 - Operating temperature: -40°C~85°C
 - JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68, SUBPART F

MATRIX PART NO:

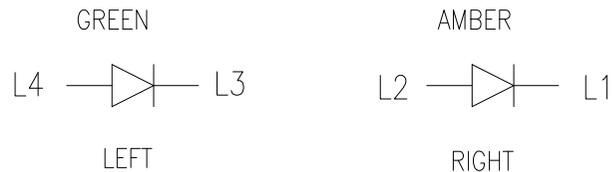
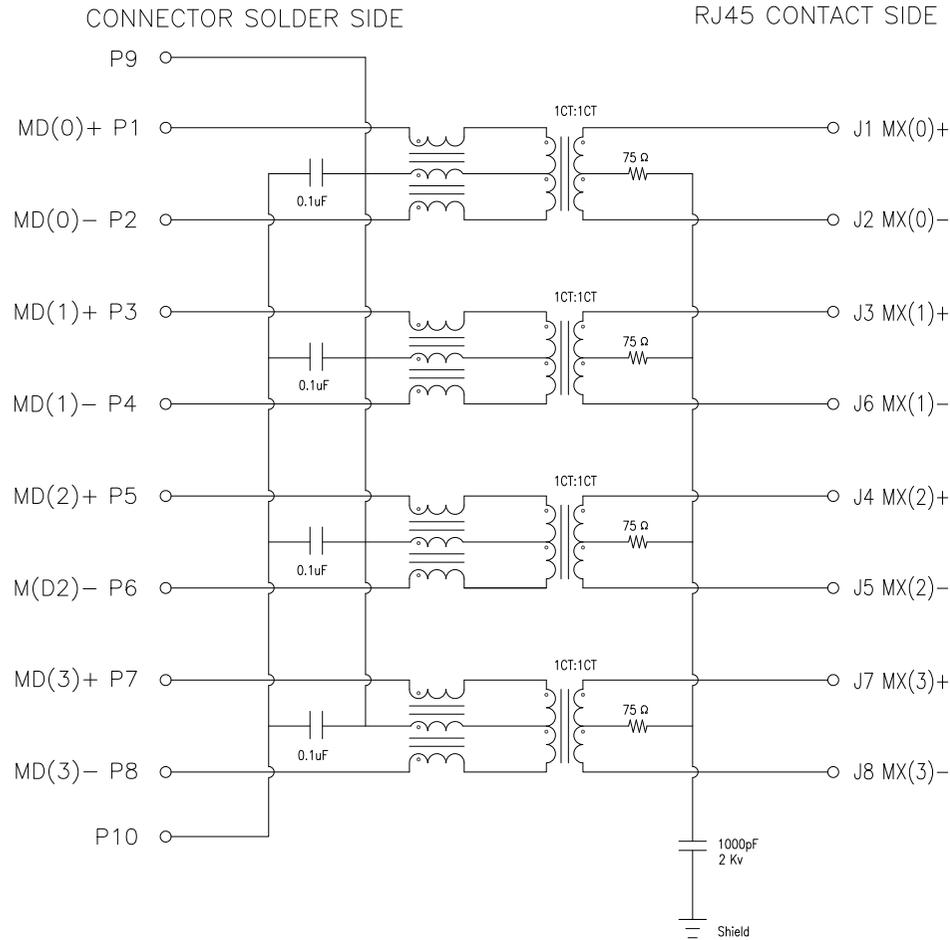


Matrix Electronics Co.,Ltd			
TOLERANCE: X.X X.XX ±0.38 X.XXX ±0.25 X.XXX ±0.13 ANGLE: ±3°	DESIGN BY : Phebe Su	DATE : 2020/02/17	PART NAME: RJ45 2X1 CONN,W/LED 10/100/1000,FILTER
	CHECKED BY: Janice Liu	DATE : 2020/02/17	PART NO. MRJG-21T1129TRS
	APPROVED BY1: Richard Hsieh	DATE : 2020/02/17	MOLD NO. NA
	APPROVED BY2: Richard Hsieh	DATE : 2020/02/17	DRAW NO.
SCALE:1:1 SIZE:A4			SHEET NO. 1 OF 2

GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
AO			Initial	2020/02/17	Phebe Su

2. SCHEMATIC:



3. ELECTRICAL CHARACTERISTICS(25±5°C)

- TR:(100KHz,0.1V); 100%
PINS:(P1,P2):(J1,J2)=1:1±3%;(P3,P4):(J3,J6)=1:1±3%
PINS:(P5,P6):(J4,J5)=1:1±3%;(P7,P8):(J7,J8)=1:1±3%
- LX:(100KHz,100mV,8mA, DC Bias) 100%
PINS:(J1,J2),(J3,J6)=350uH MINIMUM
PINS:(J4,J5),(J7,J8)=350uH MINIMUM
- DCR: 100%
PINS:(P1,P2),(P3,P4)=1.2 Ω MAXIMUM
PINS:(P5,P6),(P7,P8)=1.2 Ω MAXIMUM
- HIPOT: 100%
PINS(P1,P2)TO(J1,J2),(P3,P4)TO(J3,J6)=1500VAC FOR 60 SECONDS
PINS(P5,P6)TO(J4,J5),(P7,P8)TO(J7,J8)=1500VAC FOR 60 SECONDS
- INSERTION LOSS:
-1.0dB MAXIMUM AT 1MHz TO 100MHz;
- RETURN LOSS:
-18dB MINIMUM AT 1MHz TO 30MHz;
-16dB MINIMUM AT 30MHz TO 60MHz
-12dB MINIMUM AT 60MHz TO 80MHz
-10dB MINIMUM AT 80MHz TO 100MHz
- CROSS TALK:
-30dB MINIMUM AT 1MHz TO 100MHz
- COMMON TO COMMON MODE REJECTION:
-30dB MINIMUM AT 1MHz TO 100MHz



Matrix Electronics Co.,Ltd

TOLERANCE: X:X ±0.38 X:XX ±0.25 X:XXX ±0.13 ANGLE: ±3°	DESIGN BY : Phebe Su	DATE : 2020/02/17	PART NAME: RJ45 2X1 CONN,W/LED 10/100/1000,FILTER	
	CHECKED BY: Janice Liu	DATE : 2020/02/17	PART NO.	MRJG-21T1129TRS
 UNIT: mm [inch] SCALE:1:1 SIZE:A4	APPROVED BY1: Richard Hsieh	DATE : 2020/02/17	MOLD NO.	NA
	APPROVED BY2: Richard Hsieh	DATE : 2020/02/17	DRAW NO.	
			SHEET NO.	2 OF 2