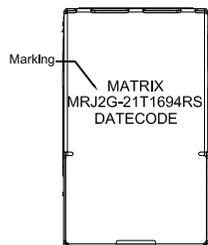


GP Component

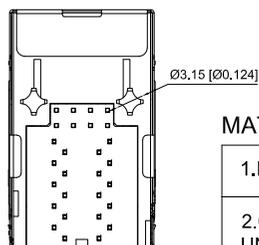
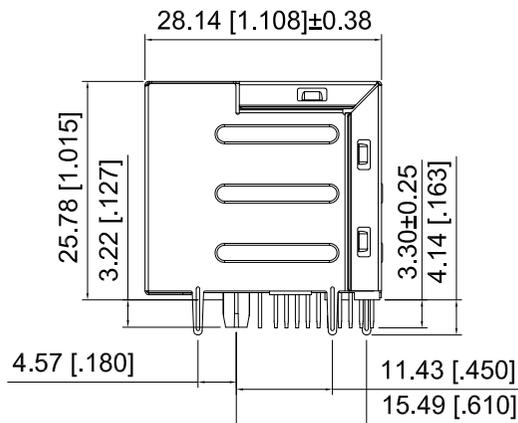
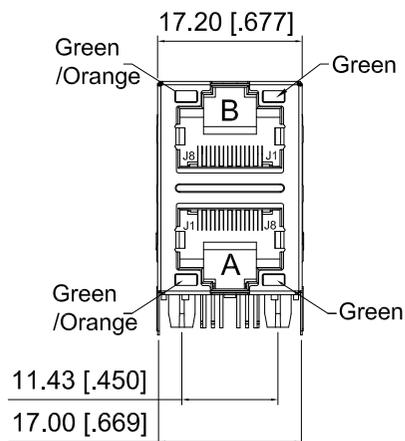
REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0.1			Modify Led Mark	2023/11/30	Vince
A0.2			Change Led Color Location	2023/11/30	Vince

1. MECHANICAL DIMENSIONS :

2. PCB LAYOUT :



Printing:
MATRIX
MRJ2G-21T1694RS
YYWW

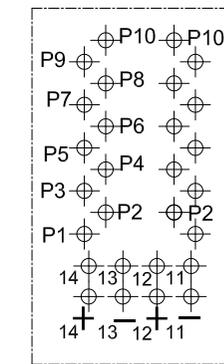
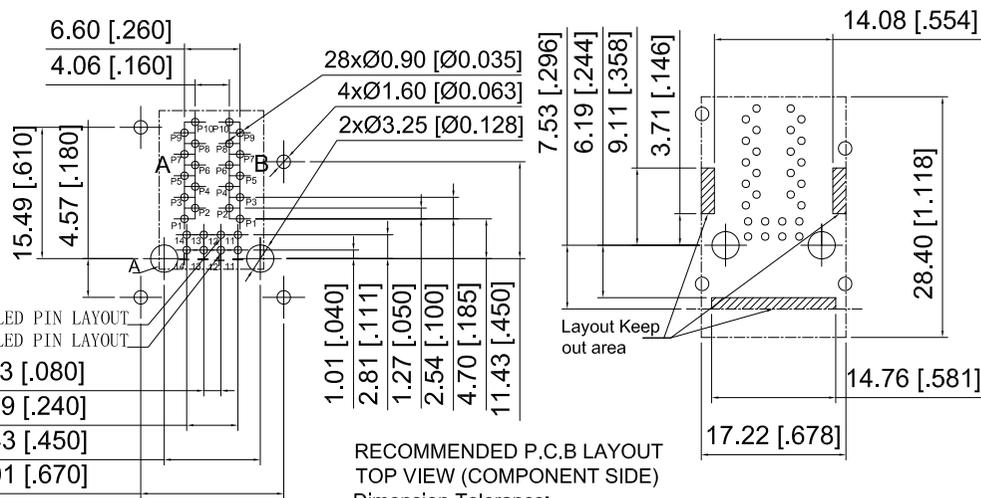


MATERIALS

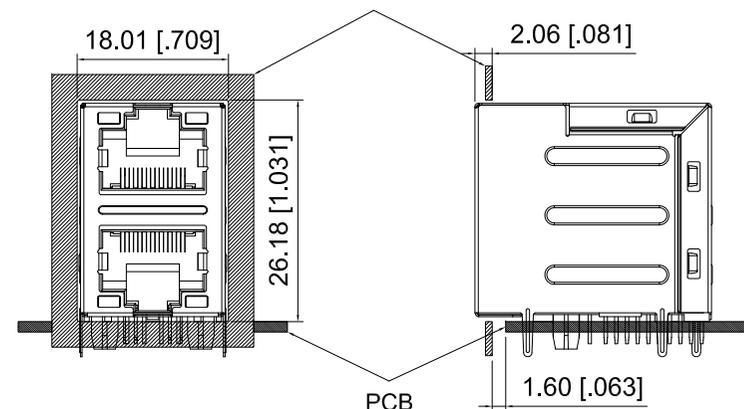
1.HOUSING:GLASS FILLED PA46(BLACK) UL94V-0(ROHS)
2.CONTACT: PHOSPHOR BRONZE,50 MICROINCHES MIN. OVERALL DUCTILE NICKEL UNDERPLATE WITH SELECTED 6U" GOLD AT MATING INTERFACE
3.SHIELD:0.2mm THICKNESS WITH BRASS.
4.JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68 SUBPART F
5.THE PART IS RECOMMENDED FOR WAVE SOLDERING PROCESS PEAK SOLDERING TEMPERATURE IS 260°C MAX,10 SECS MAX.
6.OPERATING TEMPERATURE:0°C TO +70°C. STORAGE TEMPERATURE:-40°C TO +85°C.

UPPER ROW LED PIN LAYOUT
LOWER ROW LED PIN LAYOUT

2.03 [.080]
6.09 [.240]
11.43 [.450]
17.01 [.670]



DETAIL A
SCALE 2:1

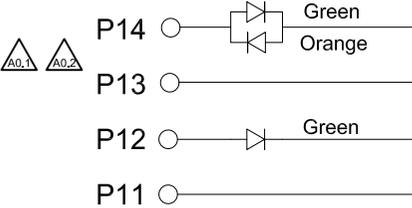
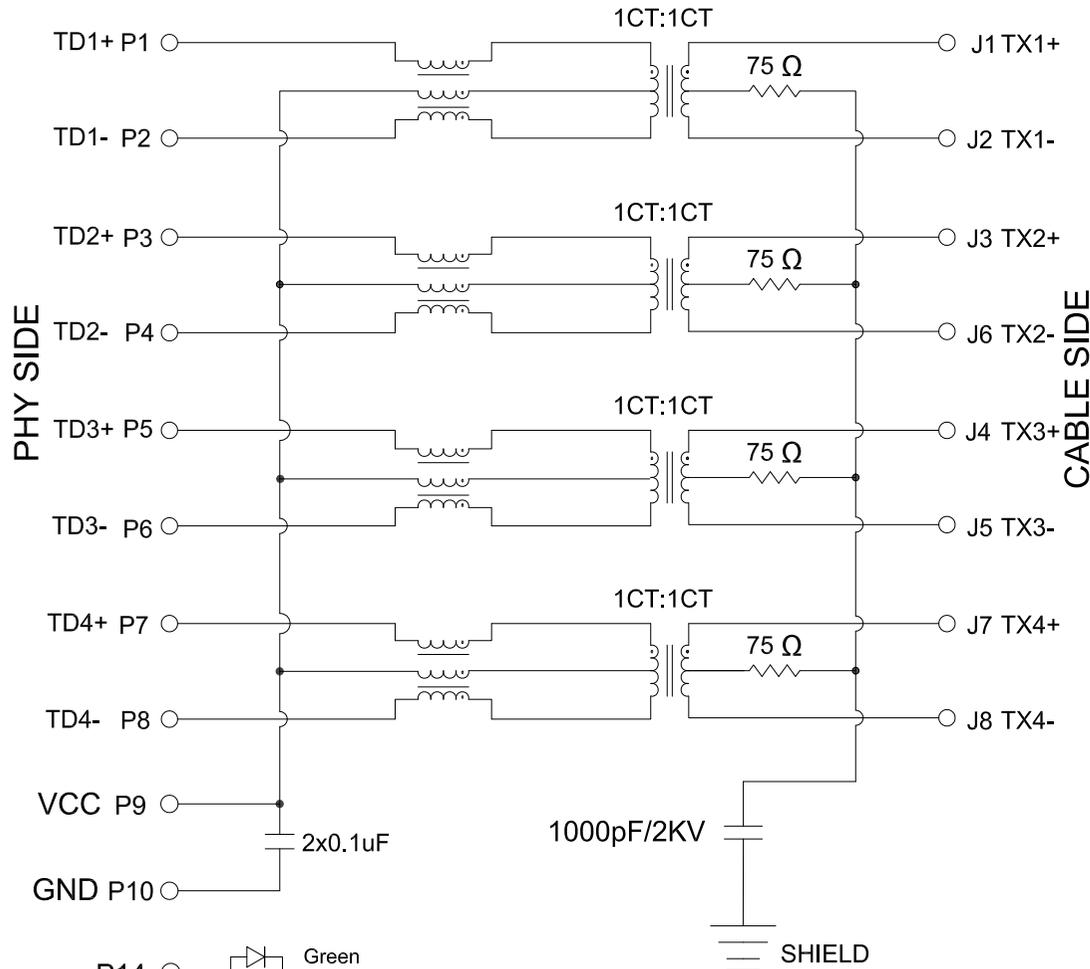


Matrix Electronics Co.,Ltd			
TOLERANCE: X:X X:XX X:XXX ANGLE: ±1	DESIGN BY : Vince Chen	DATE : 2023/11/30	PART NAME: RJ45 2X1 CONN W/LED 2.5G BASE FILTER
X:XX X:XXX ANGLE: ±1	CHECKED BY: Hanson Huang	DATE : 2023/11/30	PART NO. MRJ2G-21T1694RS
UNIT: mm [inch]	APPROVED BY1: Richard Hsieh	DATE : 2023/11/30	MOLD NO. NA
SCALE:1:1 SIZE:A4	APPROVED BY2: Richard Hsieh	DATE : 2023/11/30	DRAW NO. SHEET NO. 1 OF 3

GP Component

2. SCHEMATIC:

Schematic



REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0.1			Modify Led Mark	2023/11/30	Vince
A0.2			Change Led Color Location	2023/11/30	Vince

PARAMETER	SPECIFICATIONS
OPERATING TEMPERATURE	0°C To +70°C
TURNS RATIO	1:1±2%
OPEN CIRCUIT INDUCTANCE(OCL)	180uH MIN@100KHz/100mV With 8mA DC Bias For(CHANNEL1,2,3,4)
INSERTION LOSS(IL)	-1.0dB MAX@1MHz-50MHz; -1.0dB MAX@50MHz-125MHz;
RETURN LOSS(RL) (Z out=100 OHM)	-20dB MIN@1MHz-40MHz; -20+15LOG (f/40MHz)dB MIN@40MHz-250MHz;
CROSSTALK (ADJACENT CHANNELS)	-35dB MIN@1MHz-40MHz; -35+15LOG (f/40MHz)dB MIN@40MHz-125MHz;
COMMON MODE REJECTION RATIO(CMRR)	-30dB MIN@1MHz-250MHz;
COMMON TO DIFFERENTIAL MODE REJECTION(REF)	-35dB MIN@1MHz-125MHz;
DIFFERENTIAL TO COMMON MODE REJECTION(REF)	-40dB MIN@1MHz-10MHz; -28dB MIN@40MHz-100MHz; -23dB MIN@250MHz;
HI-POT	2250 VDC@60 SECONDS

LED SPECIFICATION			
STANDARD LED	WAVELENGTH	Farward V(max)	TYP
GREEN	570nm	2.6V	2.1V
ORANGE	588nm	2.6V	2.2V

*WITH A FORWARD CURRENT OF 20mA

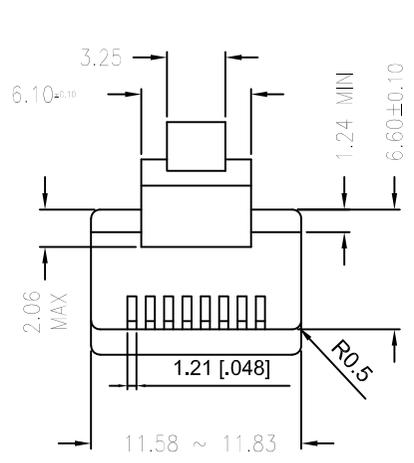
MATRIX PART NO:
MRJ 2G-21 T 1694 RS
 Matrix-RJ45 T: THT RS:ROHS HF:Halogen Free
 F:10/100 S: SMD
 G:10/100/1G/2.5G T: THT
 N:Pure Connector Series Number
 11:1X1 ; 21:2X1
 12:1X2 ; 22:2X2
 14:1X4 ; 24:2X4
 16:1X6 ; 26:2X6
 18:1X8 ; 28:2X8
 1U:RJ+USB

Matrix Electronics Co.,Ltd

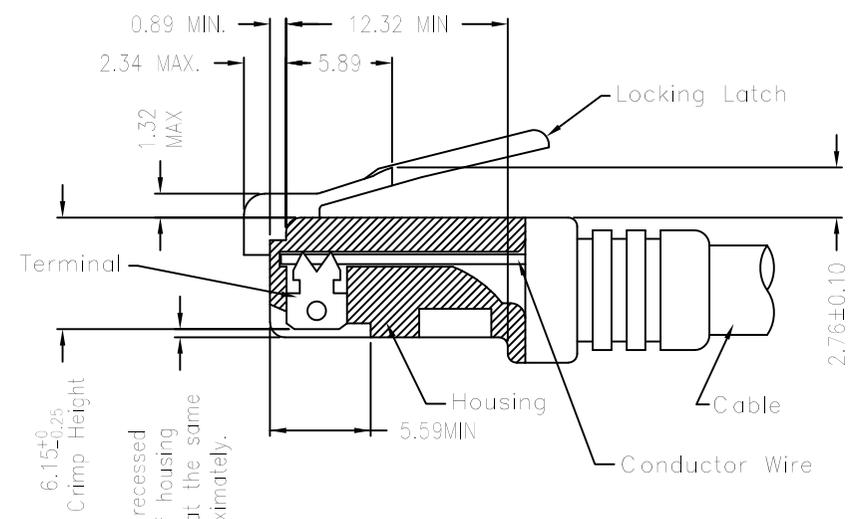
TOLERANCE: X:X X:X.X ±0.30 X:XX ±0.25 X:XXX ±0.13 ANGLE: ±1	DESIGN BY : Vince Chen	DATE : 2023/11/30	PART NAME: RJ45 2X1 CONN W/LED 2.5G BASE FILTER	
	CHECKED BY: Hanson Huang	DATE : 2023/11/30	PART NO.	MRJ2G-21T1694RS
 UNIT: mm [inch] SCALE:1:1 SIZE:A4	APPROVED BY1: Richard Hsieh	DATE : 2023/11/30	MOLD NO.	NA
	APPROVED BY2: Richard Hsieh	DATE : 2023/11/30	DRAW NO.	
			SHEET NO.	2 OF 3

GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0.1			Modify Led Mark	2023/11/30	Vince
A0.2			Change Led Color Location	2023/11/30	Vince



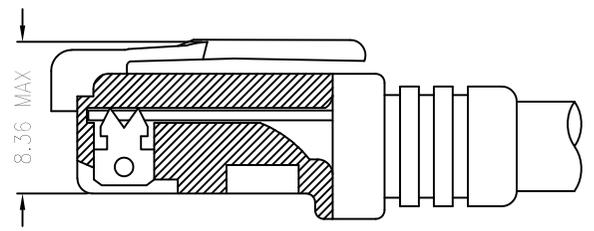
* There must be no damage to housing or locking latch. There must be no nicks or cuts in cable.
 * Durability : 750 cycles generally



All contacts recessed below top of housing and must be at the same height approximately.

FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F FIGURE 68.500 (C)(2)(i) AND IEC 603-7 FIGURE 23 & 24

STANDARD MODULAR PLUG ASSEMBLY



FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F FIGURE 68.500 (C)(2)(ii)

 Matrix Electronics Co.,Ltd				
TOLERANCE: X:X X:XX ±0.38 X:XXX ±0.25 X:XXX ±0.13 ANGLE: ±1°	DESIGN BY :	DATE :	PART NAME:	
	Vince Chen	2023/11/30	RJ45 2X1 CONN W/LED 2.5G BASE FILTER	
  UNIT: mm [inch] SCALE:1:1 SIZE:A4	CHECKED BY:	DATE :	PART NO.	MRJ2G-21T1694RS
	Hanson Huang	2023/11/30		
	APPROVED BY1:	DATE :	MOLD NO.	NA
	Richard Hsieh	2023/11/30		
	APPROVED BY2:	DATE :	DRAW NO.	
	Richard Hsieh	2023/11/30	SHEET NO.	3 OF 3