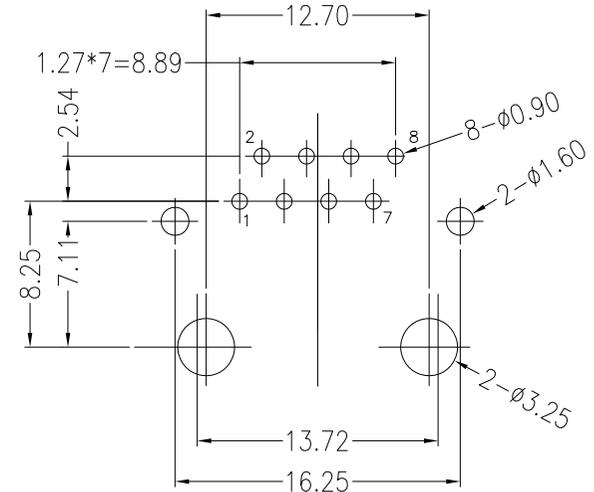
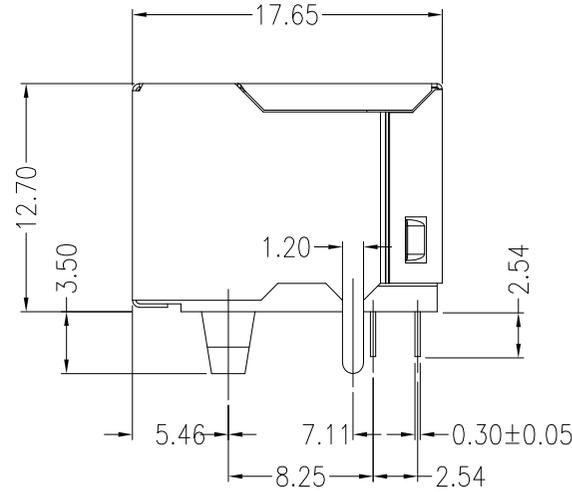
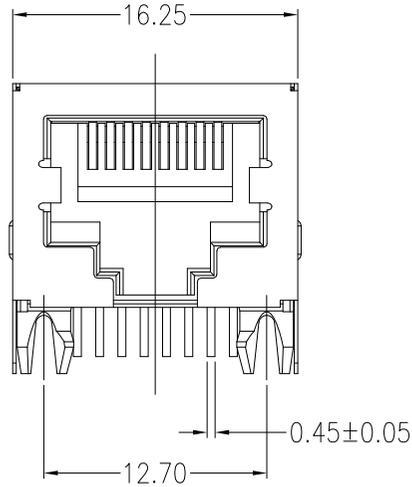


GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
AO			Initial	2018/05/30	Phebe Su
A1			Modify drawing	2018/06/30	Phebe Su
A1.1			Add Printing	2024/12/23	Vince



RECOMMEND PCB LAYOUT (COMPONENT SIDE)
DIMENSIONAL TOLERANCE: ±0.05

Printing:

YYWW

NOTES:

1. MATERIALS:

HOUSING: PBT, UL 94V-0, BLACK.

CONTACTS: T=0.30 mm PHOSPHOR BRONZE,

50u" NICKEL UNDERPLATE

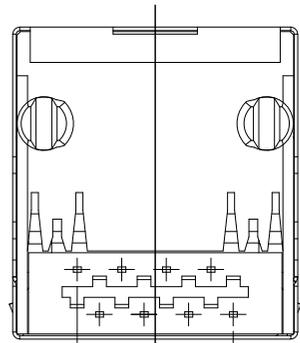
100u" MATTE TIN ON SOLDER TAIL

GOLD 15u" ON CONTACT AREA.

SHELL: STAINLESS T=0.20mm, NICKEL 30u" MIN. OVERALL

2. OPERATING TEMPERATURE: -40°C ~ +85°C

3. JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68, SUBPART F



MATRIX PART NO:

M R J N - 1 1 T 861 T RS

Matrix-RJ45

F: 10/100

G: 10/100/1000

N: Pure Connector

11:1X1 ; 21:2X1
12:1X2 ; 22:2X2
14:1X4 ; 24:2X4
16:1X6 ; 26:2X6
18:1X8 ; 28:2X8
1U:RJ+USB

S: SMD
T: THT

RS: ROHS
HF: Halogen Free
Industrial temperature
Series Number



Matrix Electronics Co., Ltd

TOLERANCE:	DESIGN BY :	DATE :	PART NAME:	
X:X ±0.38	Vince Chen	2024/12/23	RJ45 1X1 W/SHELL W/O LED	
X:XX ±0.25	CHECKED BY:	DATE :	PART NO.	MRJN-11T861TRS
X:XXX ±0.13	Hanson Huang	2024/12/23	MOLD NO.	NA
ANGLE: ±1	APPROVED BY1:	DATE :	DRAW NO.	
	Richard Hsieh	2024/12/23	SHEET NO.	1 OF 1
	APPROVED BY2:	DATE :		
UNIT: mm [inch]	Richard Hsieh	2024/12/23		
SCALE:1:1	SIZE:A4			