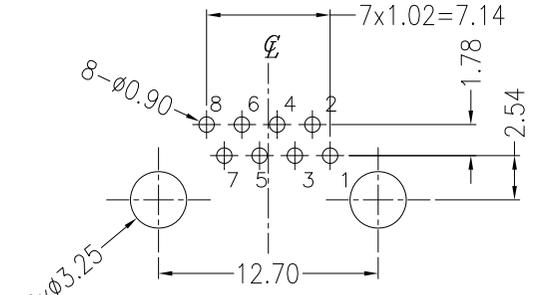
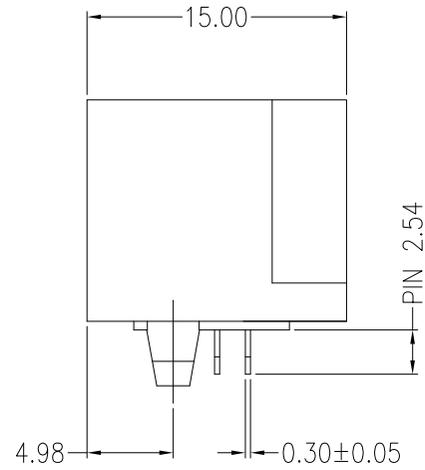
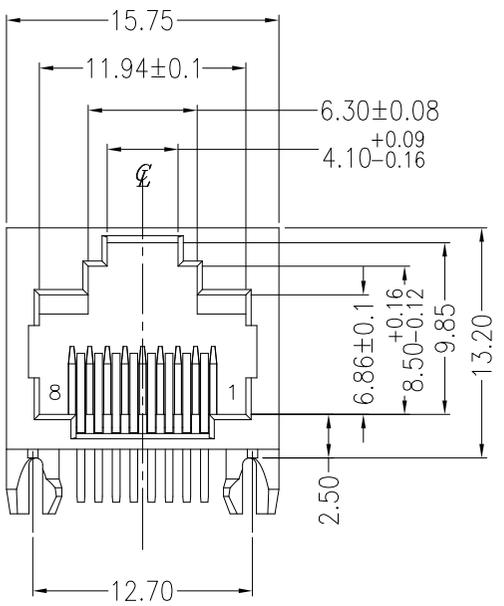


GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
AO			Initial	2016/08/12	Phebe Su
A1			Sealing on the back side of housing	2018/10/04	Phebe Su

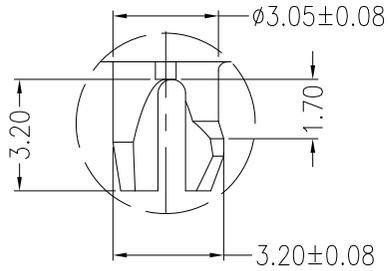
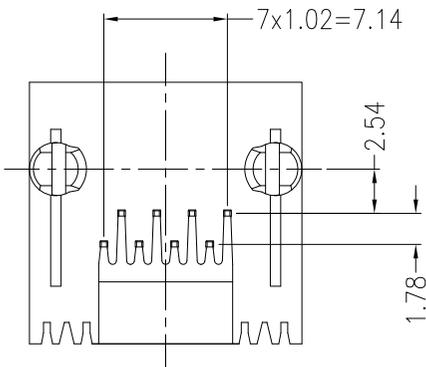
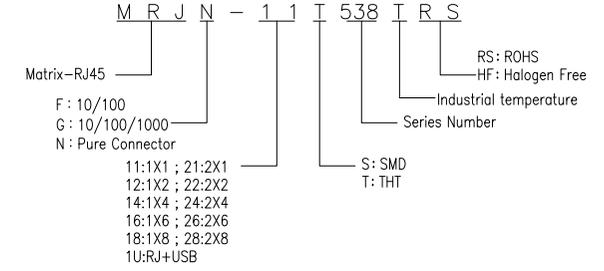


RECOMMENDED PCB LAYOUT
COMPONENT SIDE
TOLERANCE ±0.05

NOTES:

- MATERIALS:**
HOUSING: PBT, UL94V-0, BLACK.
CONTACTS: T=0.30 mm PHOSPHOR BRONZE,
50u" NICKEL UNDERPLATE
100u" TIN ON SOLDER TAIL
GOLD 30u" ON CONTACT AREA.
- OPERATING TEMPERATURE:** -40°C ~ +85°C
- JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68, SUBPART F**

MATRIX PART NO:



BOARD LOCK DIMENSION



Matrix Electronics Co., Ltd

TOLERANCE: X: X X.X: ±0.38 X.XX: ±0.25 X.XXX: ±0.13 ANGLE: ±3°	DESIGN BY : Phebe Su	DATE : 2018/10/04	PART NAME: RJ45.CONNECTORS,W/O LED&SHIELDING	
	CHECKED BY: Vicky Hsieh	DATE : 2018/10/04	PART NO.	MRJN-11T538TRS
	APPROVED BY1: Richard Hsieh	DATE : 2018/10/04	MOLD NO.	NA
UNIT: mm [inch]	APPROVED BY2: Richard Hsieh	DATE : 2018/10/04	DRAW NO.	
SCALE:1:1	SIZE:A4		SHEET NO.	1 OF 1