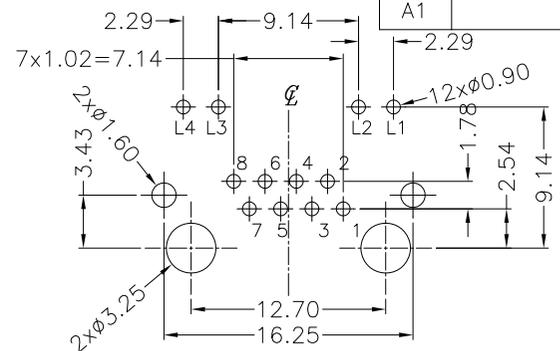
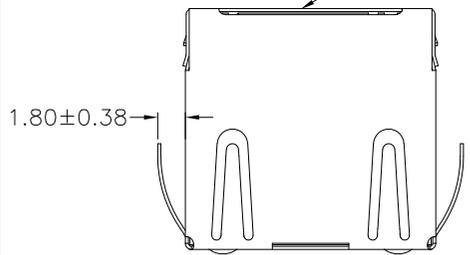


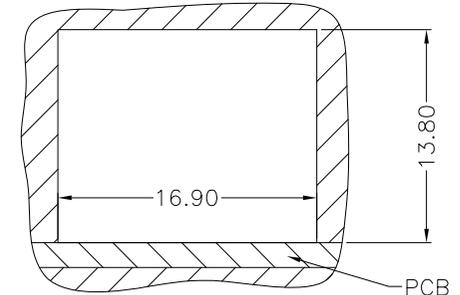
# GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2016/04/01	Phebe Su
A1			Sealing on the back side of housing	2018/09/26	Phebe Su

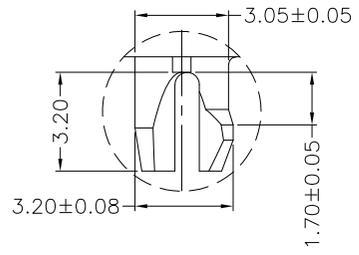
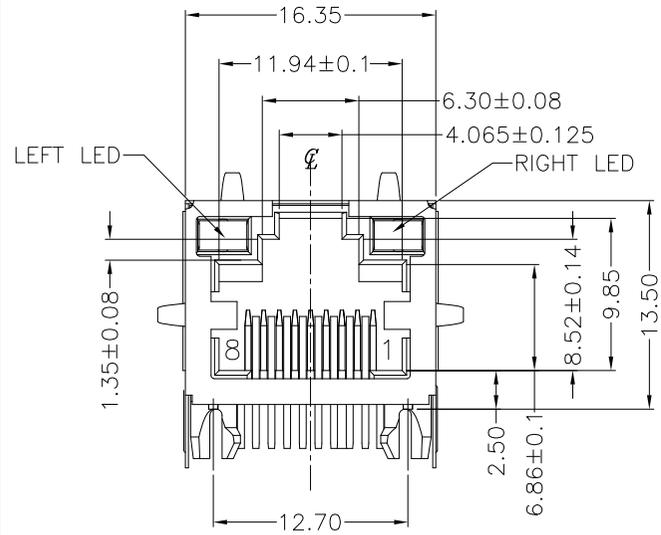
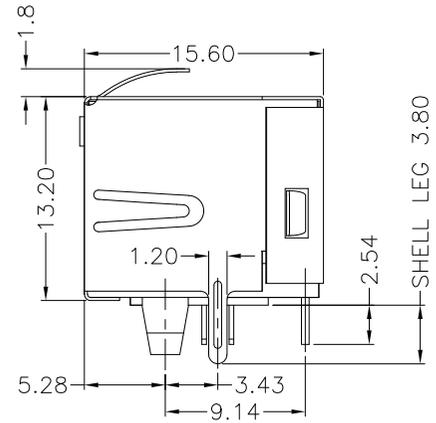
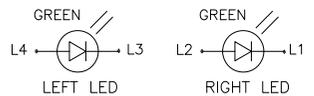
DATE CODE



RECOMMENED PCB LAYOUT  
COMPONENT SIDE  
TOLERANCE ±0.05



RECOMMENDED PANEL CUTOUT DIMENSION  
DIMENSIONAL TOLERANCE: ±0.15

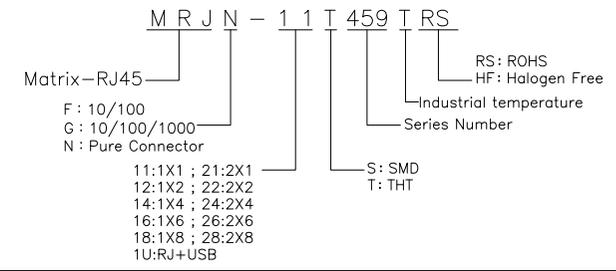


BOARD LOCK

NOTES:

- MATERIALS:  
HOUSING: PBT, UL94V-0, BLACK.  
CONTACTS: T=0.30 mm PHOSPHOR BRONZE,  
50u" NICKEL UNDERPLATE  
100u" TIN ON SOLDER TAIL  
GOLD 3u" ON CONTACT AREA.  
SHELL: STAINLESS T=0.20 NICKEL OVERALL
- OPERATING TEMPERATURE: -40°C ~ +85°C
- LED SPEC: FORWARD VOLTAGE: 1.8~2.4V  
REVERSE CURRENT: 5uA MAX  
PEAK WAVE LENGTH: GREEN: 565~575nm
- JACK CAVITY MEET IEC60603-7 AND FCC PART 68, SUBPART F

MATRIX PART NO:



Matrix Electronics Co., Ltd

<b>TOLERANCE:</b> X.X ±0.38 X.XX ±0.25 X.XXX ±0.13 ANGLE: ±1°	DESIGN BY : Phebe Su	DATE : 2018/09/26	PART NAME: RJ45 PCB JACK W/SHELL&LED	
	CHECKED BY: Vicky Hsieh	DATE : 2018/09/26	PART NO.	MRJN-11T459TRS
UNIT: mm [inch] SCALE: 1:1 SIZE: A4	APPROVED BY1: Richard Hsieh	DATE : 2018/09/26	MOLD NO.	NA
	APPROVED BY2: Richard Hsieh	DATE : 2018/09/26	DRAW NO.	
			SHEET NO.	1 OF 1