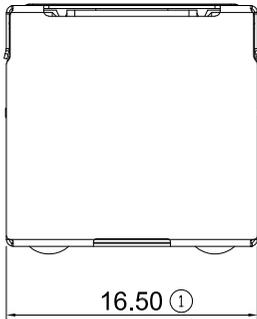


GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2013/1/23	Jamie Zie
A1			Change front view	2021/10/15	Vince

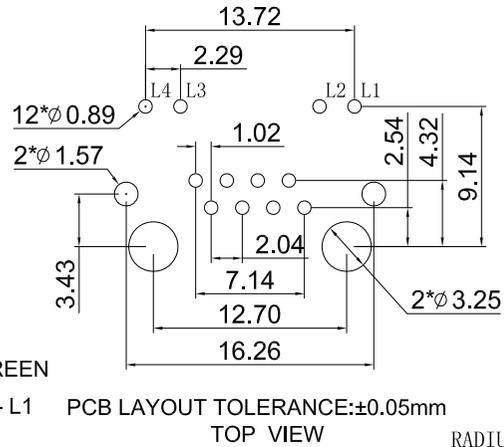


LED SCHEMATIC:

LEFT LED:YELLOW RIGHT LED:GREEN

L4 + - L3

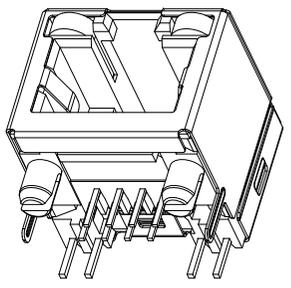
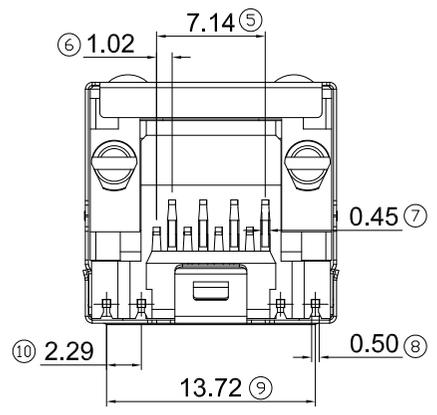
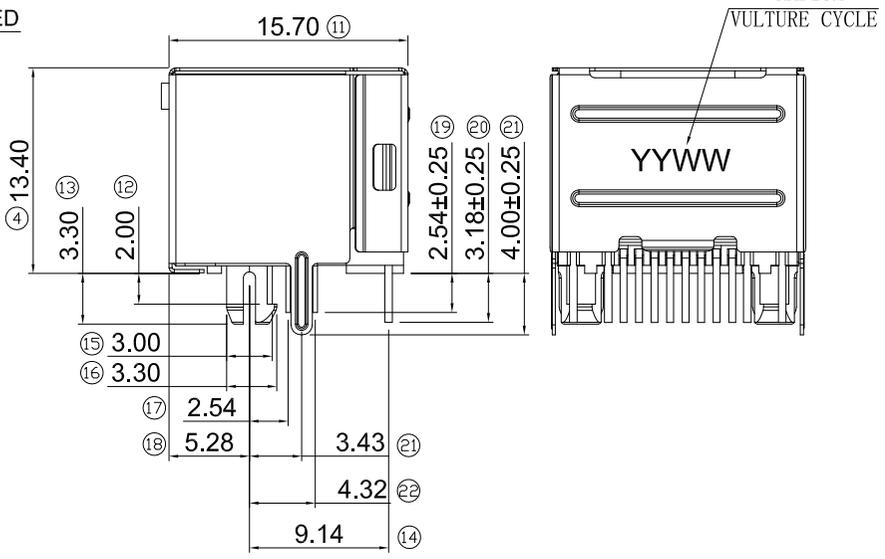
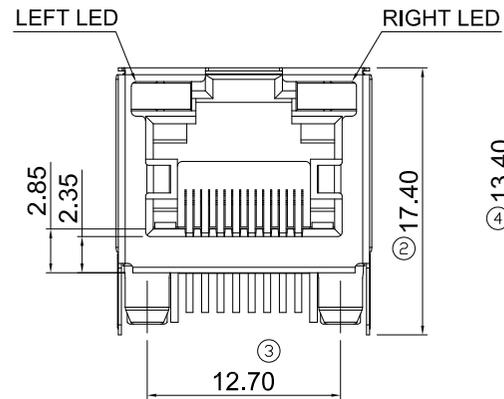
L2 + - L1



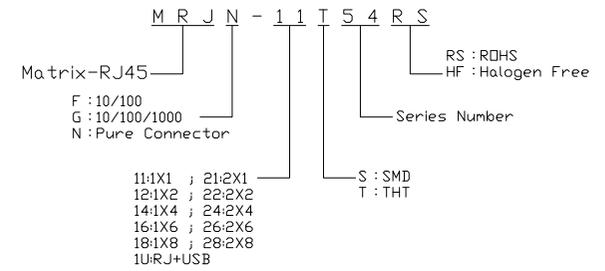
PCB LAYOUT TOLERANCE:±0.05mm
TOP VIEW

NOTES:

- MATERIALS:**
HOUSING: PBT BLACK (UL94V-0).
CONTACTS: PHOSPHOR BRONZE T=0.30mm.
SHIELD: BRASS,T=0.20mm
- FINISH:**
CONTACT: 50U" NICKE LUNDERPLAT ON ALL AREA.
100U"MIN. TIN ON SOLDERTAILS.
3U" PLATED ON CONTACT AREA.
SHIELD:COPPER ALLOY PLATED WITH NICKEL.
- OPERATING TEMPERATURE :** -40°C ~ +85°C.
- STORAGE TEMPERATURE:** -40°C ~ +85°C.
- JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68, SUBPART F.**
- THIS PRODUCT IS A REFLOW SOLDERING PROCESS.**



ORDER INFORMATION :



NO	VARIETY	QTY	MATERIAL	REMARK
			Matrix Electronics Co.,Ltd	
TOLERANCE:		DESIGN BY :	DATE :	PART NAME:
X:X	±0.30	Vince Chen	2021/10/15	1x1RJ45 TAB UP
X:XX	±0.25	CHECKED BY:	DATE :	PART NO.
X:XXX	±0.13	Hanson Huang	2021/10/15	MRJN-11T54RS
ANGLE: ±3°		APPROVED BY1:	DATE :	MOLD NO.
		Richard Hsieh	2021/10/15	NA
UNIT: mm [inch]		APPROVED BY2:	DATE :	DRAW NO.
SCALE:1:1	SIZE:A4	Richard Hsieh	2021/10/15	SHEET NO.
				1 OF 1