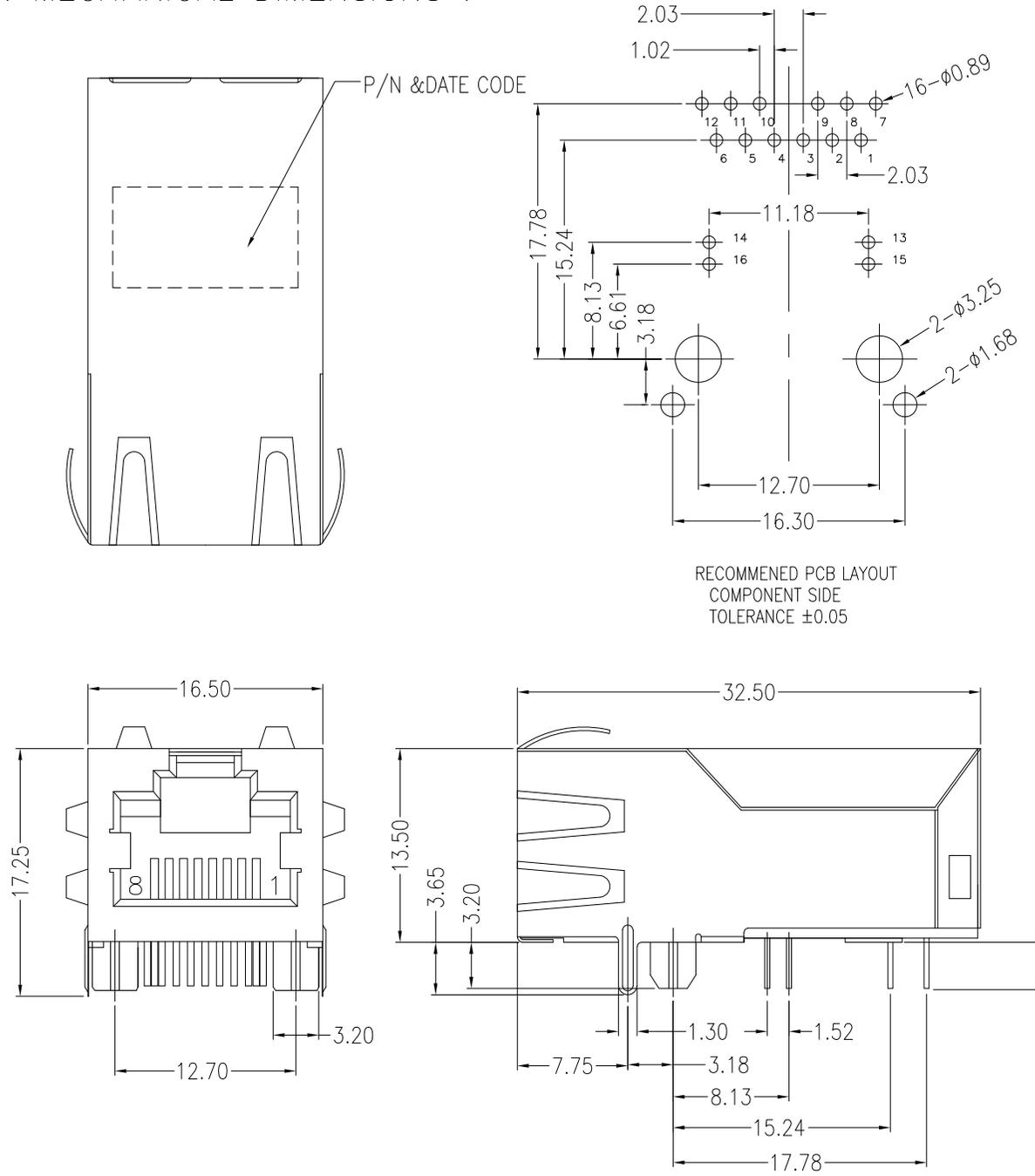


# GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2017/07/19	Phebe Su
A1			Change Shell	2020/09/10	Hanson
A1.1			Add Electrical Item 9	2024/01/15	Vince

## 1. MECHANICAL DIMENSIONS :



RECOMMENED PCB LAYOUT  
COMPONENT SIDE  
TOLERANCE  $\pm 0.05$

### NOTES:

- MATERIAL:  
HOUSING: PBT BLACK UL94V-0  
CONTACTS: PHOSPHOR BRONZE T=0.35, GOLD 6 $\mu$  ON CONTACT AREA, TIN PLATING ON SOLDER TAIL  
SHIELD : COPPER ALLOY T=0.20, NICKEL PLATING OVERALL
- CAVITY CONFIRMS TO FCC RULES AND REGULATIONS PART 68, SUBPART F.

### ORDER INFORMATION :

Matrix-RJ45  
F: 10/100  
G: 10/100/1000  
N: Pure Connector

M R J G - 1 1 T 888 R S

RS: ROHS  
HF: Halogen Free

Series Number

S: SMD  
T: THT

11:1X1 ; 21:2X1  
12:1X2 ; 22:2X2  
14:1X4 ; 24:2X4  
16:1X6 ; 26:2X6  
18:1X8 ; 28:2X8  
1U:RJ+USB

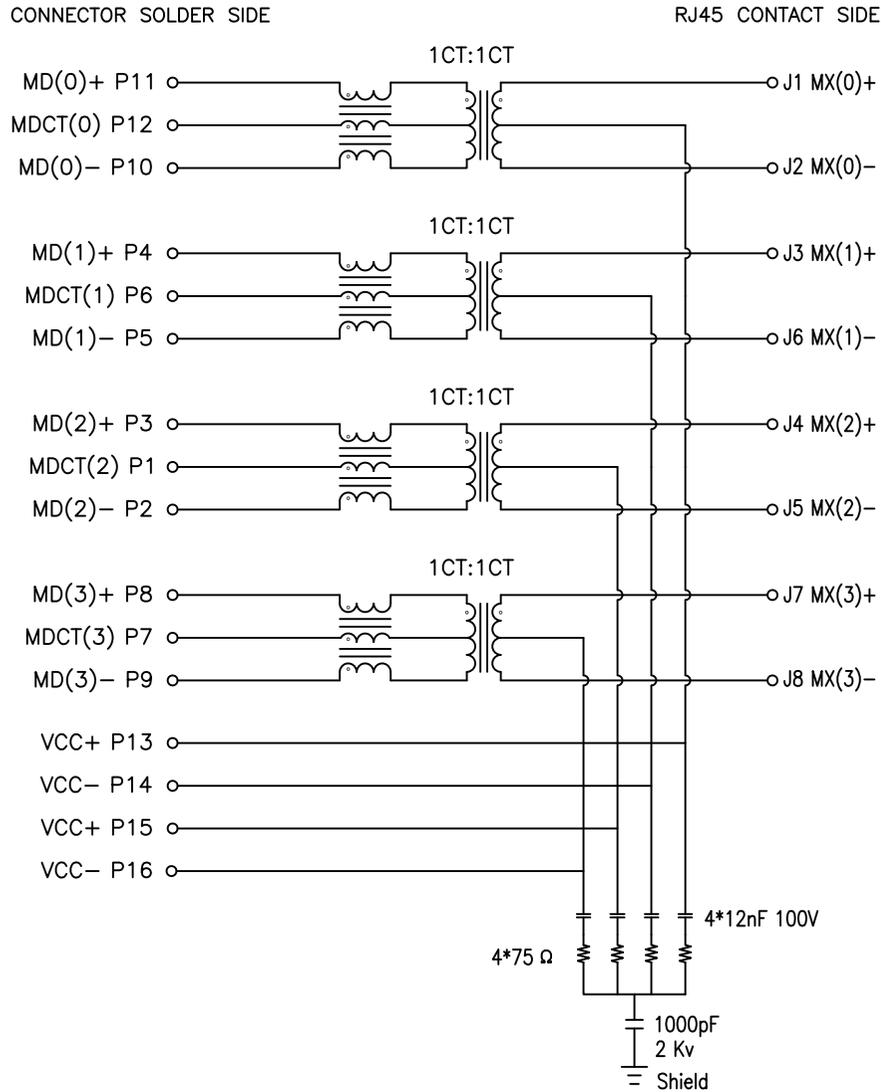
### Matrix Electronics Co.,Ltd

<b>TOLERANCE:</b> X.X $\pm 0.38$ X.XX $\pm 0.25$ X.XXX $\pm 0.15$ ANGLE: $\pm 3^\circ$	<b>DESIGN BY :</b> Vince Chen	<b>DATE :</b> 2024/01/15	<b>PART NAME:</b> RJ45, CONNECTORS, W/O LED 100/1000, FILTER, POE
 <b>UNIT: mm [inch]</b> <b>SCALE: 1:1 SIZE: A4</b>	<b>CHECKED BY:</b> Hanson Huang	<b>DATE :</b> 2024/01/15	<b>PART NO.</b> MRJG-11T888RS
	<b>APPROVED BY1:</b> Richard Hsieh	<b>DATE :</b> 2024/01/15	<b>MOLD NO.</b> NA
	<b>APPROVED BY2:</b> Richard Hsieh	<b>DATE :</b> 2024/01/15	<b>DRAW NO.</b> SHEET NO. 1 OF 3

# GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2017/07/19	Phebe Su
A1			Change Shell	2020/09/10	Hanson
A1.1			Add Electrical Item 9	2024/01/15	Vince

## 2. SCHEMATIC:



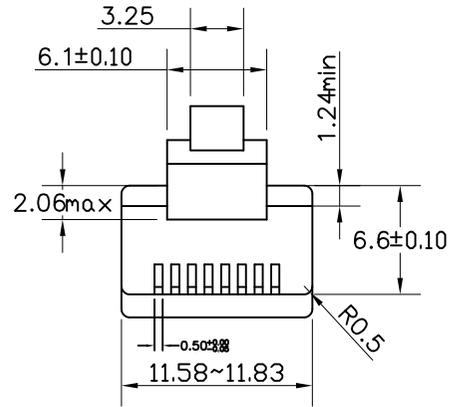
## 3. ELECTRICAL CHARACTERISTICS :

- TR:(100KHz,0.1V);  
PINS:(P11,P10):(J1,J2)=1:1±3%;(P4,P5):(J3,J6)=1:1±3%  
PINS:(P3,P2):(J4,J5)=1:1±3%;(P8,P9):(J7,J8)=1:1±3%
- LX:(100KHz,100mV,8mA, DC Bias)  
PINS:(J1,J2),(J3,J6)=350uH MINIMUM  
PINS:(J4,J5),(J7,J8)=350uH MINIMUM
- DCR:  
PINS:(J1,J2),(J3,J6)=1.2 Ω MAXIMUM  
PINS:(J4,J5),(J7,J8)=1.2 Ω MAXIMUM
- HIPOT:  
PINS(P11,P10)TO(J1,J2),(P4,P5)TO(J3,J6)=1500VAC FOR 60 SECONDS  
PINS(P3,P2)TO(J4,J5),(P8,P9)TO(J7,J8)=1500VAC FOR 60 SECONDS
- INSERTION LOSS:  
-1.0dB MAXIMUM AT 1MHz TO 100MHz;
- RETURN LOSS:  
-18dB MINIMUM AT 1MHz TO 30MHz;  
-16dB MINIMUM AT 30MHz TO 60MHz  
-12dB MINIMUM AT 60MHz TO 80MHz  
-10dB MINIMUM AT 80MHz TO 100MHz
- CROSS TALK:  
-30dB MINIMUM AT 1MHz TO 100MHz
- COMMON TO COMMON MODE REJECTION:  
-30dB MINIMUM AT 1MHz TO 100MHz
- DC current:Meeting IEEE802.3at Standards.  
720mA MAX @57VDC Continuous.  
Supports 4 pair of 30W lines.

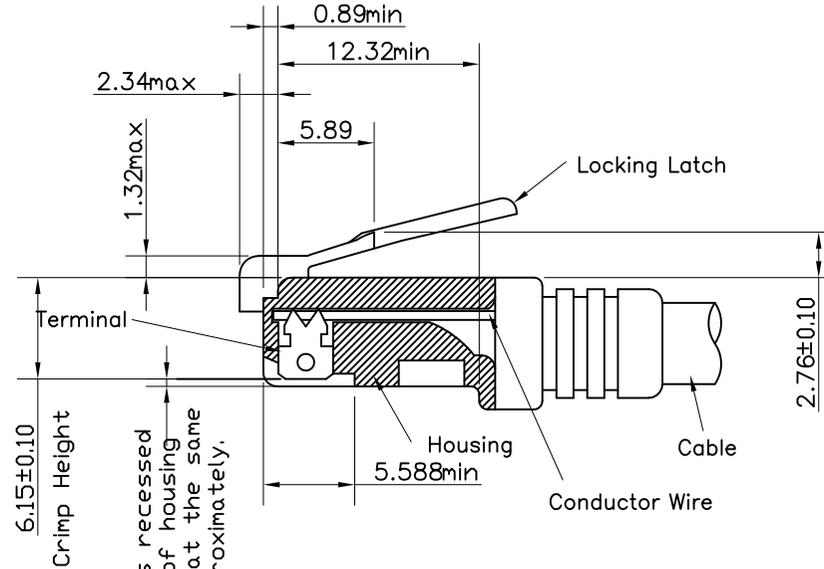
Matrix Electronics Co.,Ltd			
TOLERANCE: X:X ±0.38 X:XX ±0.25 X:XXX ±0.15 ANGLE: ±3	DESIGN BY : Vince Chen	DATE : 2024/01/15	PART NAME: RJ45,CONNECTORS,W/O LED 100/1000,FILTER,POE
	CHECKED BY: Hanson Huang	DATE : 2024/01/15	PART NO. MRJG-11T888RS
	APPROVED BY1: Richard Hsieh	DATE : 2024/01/15	MOLD NO. NA
	APPROVED BY2: Richard Hsieh	DATE : 2024/01/15	DRAW NO.
			SHEET NO. 2 OF 3

# GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2017/07/19	Phebe Su
A1			Change Shell	2020/09/10	Hanson
A1.1			Add Electrical Item 9	2024/01/15	Vince



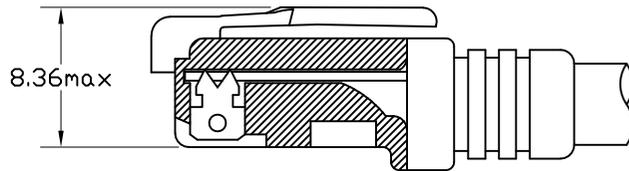
- \* There must be no damage to housing or locking latch. There must be no nicks or cuts in cable.
- \* Durability : 750 cycles generally



All contacts recessed below top of housing and must be at the same height approximately.

FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F FIGURE 68.500 (C)(2)(i) AND IEC 603-7 FIGURE 23 & 24

STANDARD MODULAR PLUG ASSEMBLY



FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F FIGURE 68.500 (C)(2)(ii)

 <b>Matrix Electronics Co.,Ltd</b>				
<b>TOLERANCE:</b> X.X ±0.38 X.XX ±0.25 X.XXX ±0.15 ANGLE: ±3°	DESIGN BY :	DATE :	<b>PART NAME:</b>	
	Vince Chen	2024/01/15	RJ45 CONNECTORS,W/O LED 100/1000,FILTER,POE	
 UNIT: mm [inch] SCALE:1:1 SIZE:A4	CHECKED BY:	DATE :	PART NO.	MRJG-11T888RS
	Hanson Huang	2024/01/15	MOLD NO.	NA
APPROVED BY1: APPROVED BY2:	DATE :	DATE :	DRAW NO.	
	Richard Hsieh	2024/01/15	SHEET NO.	3 OF 3
	Richard Hsieh	2024/01/15		