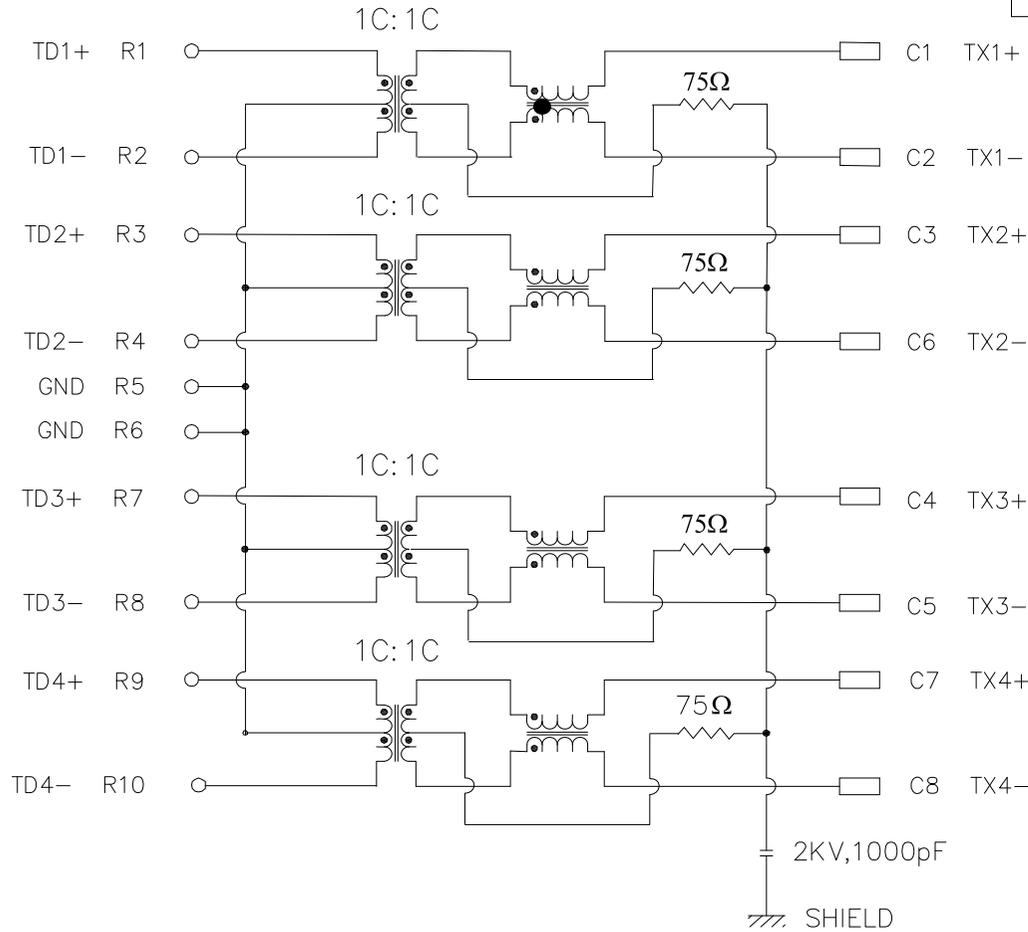
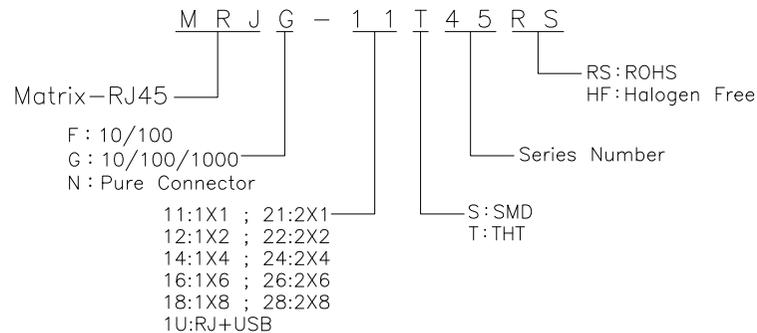


GP Component

2. SCHEMATIC:



ORDER INFORMATION :



REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2012/12/17	Jamie Zie
A1			Change	2017/08/07	Phebe Su
A2			Change PA46 to LCP	2017/08/14	Phebe Su
A3			Change SHIELD	2020/08/19	Hanson

3. ELECTRICAL CHARACTERISTICS :

TEST NOTES:(25°±5°C)

1. TR:(100KHz,0.1V); 100%

PINS:(P1,P2):(J1,J2)=1:1±3%;(P3,P4):(J3,J6)=1:1±3%

PINS:(P7,P8):(J4,J5)=1:1±3%;(P9,P10):(J7,J8)=1:1±3%

2. LX:(100KHz,100mV,8mA, DC Bias) 100%

3. DCR: 100%

PINS:(P1,P2),(P3,P4),(P7,P8),(P9,P10)=350uH MINIMUM

PINS:(J1,J2),(J3,J6),(J4,J5),(J7,J8)=1.2Ω MAXIMUM

4. HIPOT: 100%

PINS(P1,P2)TO(J1,J2),(P3,P4)TO(J3,J6)=1500VAC FOR 2 SECONDS

PINS(P7,P8)TO(J4,J5),(P9,P10)TO(J7,J8)=1500VAC FOR 2 SECONDS

5. INSERTION LOSS:

-1.0dB MAXIMUM AT 1MHz TO 100MHz;

6.RETURN LOSS:

-20dB MINIMUM AT 1MHz TO 30MHz;

-15dB MINIMUM AT 30MHz TO 60MHz

7.CROSS TALK:

-10dB MINIMUM AT 60MHz TO 80MHz

-30dB MINIMUM AT 1MHz TO 100MHz

8.COMMON TO COMMON MODE REJECTION:

-30dB MINIMUM AT 1MHz TO 100MHz

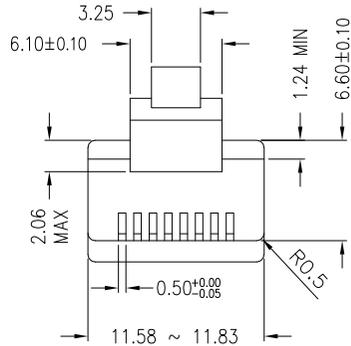


Matrix Electronics Co.,Ltd

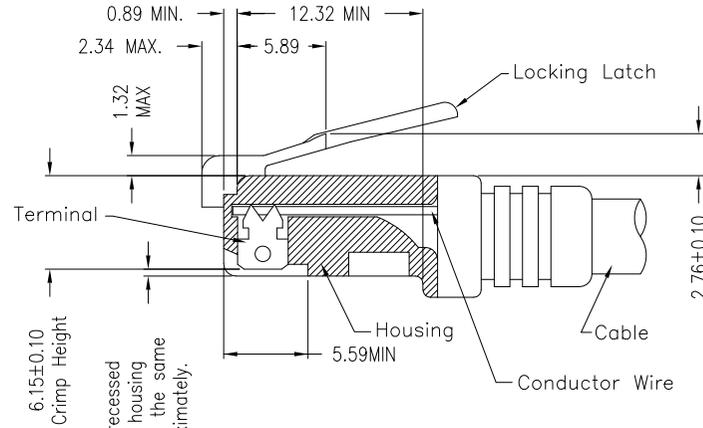
TOLERANCE: X:X X:XX ±0.38 X:XX ±0.25 X:XXX ±0.13 ANGLE: ±1°	DESIGN BY : Hanson Huang	DATE : 2017/08/14	PART NAME: RJ1X1 W/LED W/Giga LAN
	CHECKED BY: Vicky Hsieh	DATE : 2017/08/14	PART NO. MRJG-11T45RS
	APPROVED BY1: Richard Hsieh	DATE : 2017/08/14	MOLD NO. NA
UNIT: mm [inch]	APPROVED BY2: Richard Hsieh	DATE : 2017/08/14	DRAW NO.
SCALE:1:1 SIZE:A4			SHEET NO. 2 OF 3

GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2012/12/17	Jamie Zie
A1			Change	2017/08/07	Phebe Su
A2			Change PA46 to LCP	2017/08/14	Phebe Su
A3			Change SHIELD	2020/08/19	Hanson



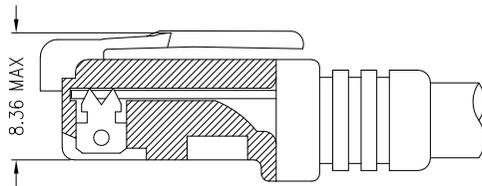
* There must be no damage to housing or locking latch. There must be no nicks or cuts in cable.
 * Durability : 750 cycles generally



All contacts recessed below top of housing and must be at the same height approximately.

FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F FIGURE 68.500 (C)(2)(i) AND IEC 603-7 FIGURE 23 & 24

STANDARD MODULAR PLUG ASSEMBLY



FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F FIGURE 68.500 (C)(2)(ii)



Matrix Electronics Co.,Ltd

TOLERANCE: X.X ±0.38 X.XX ±0.25 X.XXX ±0.13 ANGLE: ±1°	DESIGN BY : Hanson Huang	DATE : 2017/08/14	PART NAME: RJ1X1 W/LED W/Giga LAN	
	CHECKED BY: Vicky Hsieh	DATE : 2017/08/14	PART NO.	MRJG-11T45RS
 UNIT: mm [inch] SCALE:1:1 SIZE:A4	APPROVED BY1: Richard Hsieh	DATE : 2017/08/14	MOLD NO.	NA
	APPROVED BY2: Richard Hsieh	DATE : 2017/08/14	DRAW NO.	SHEET NO.
				3 OF 3