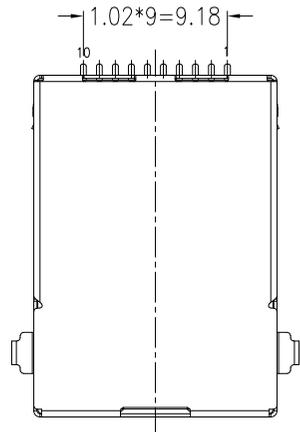


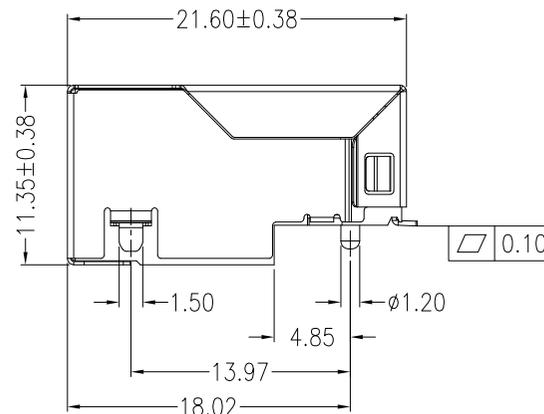
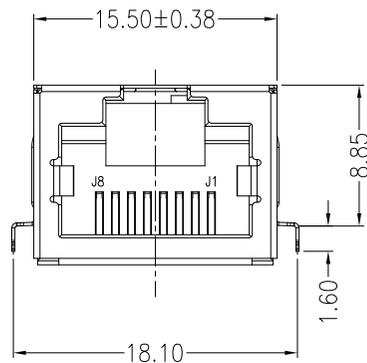
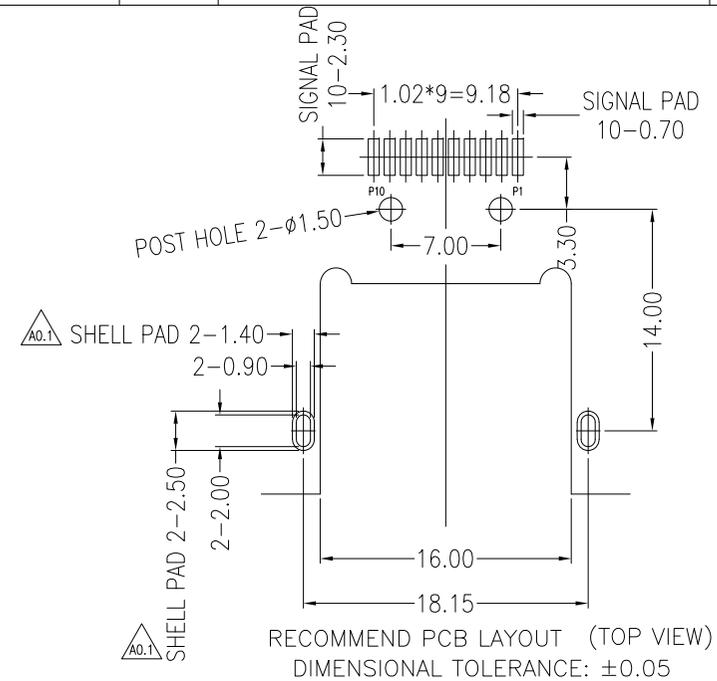
# GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
AO			Initial	2018/11/22	Phebe Su
AO.1			Add Pcb layout Pad	2024/12/24	Vince

## 1. MECHANICAL DIMENSIONS :



Printing :  
 MATRIX  
 MRJG-11S1104RS  
 YYWW



- MATERIAL:  
 HOUSING: HIGH TEMPERATURE PLASTIC UL94V-0 BLACK  
 CONTACT: COPPER ALLOY  
 Shell: COPPER ALLOY
- Plating:  
 CONTACT AREA: PLATING GOLD 1U"  
 SHELL: NICKEL PLATING OVERALL
- OPERATING TEMPERATURE: 0~70°C



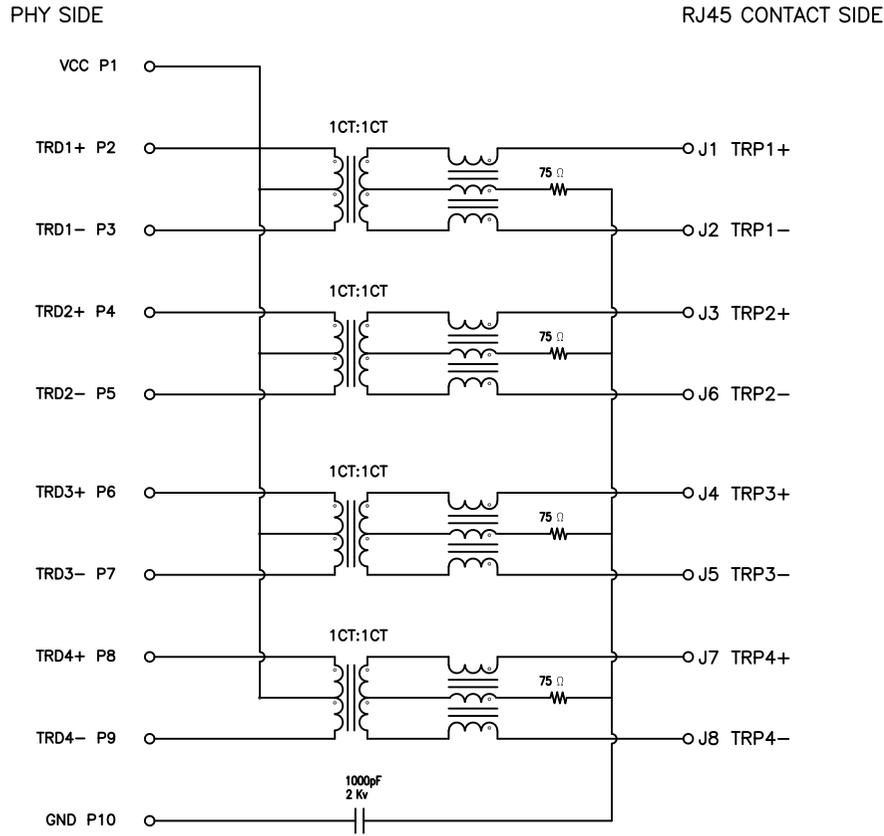
Matrix Electronics Co.,Ltd

TOLERANCE: X:X $\pm 0.38$ X:XX $\pm 0.25$ X:XXX $\pm 0.13$ ANGLE: $\pm 3$	DESIGN BY :	DATE :	PART NAME:	
	Vince Chen	2024/12/24	RJ45 1X1 TAB UP SMT TYPE GIGABIT W/SHELL&W/O LED	
 UNIT: mm [inch] SCALE:1:1 SIZE:A4	CHECKED BY:	DATE :	PART NO.	MRJG-11S1104RS
	Hanson Huang	2024/12/24	MOLD NO.	NA
	APPROVED BY1:	DATE :	DRAW NO.	
	Richard Hsieh	2024/12/24	SHEET NO.	1 OF 3
	APPROVED BY2:	DATE :		
	Richard Hsieh	2024/12/24		

# GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
AO			Initial	2018/11/22	Phebe Su
AO.1			Add Pcb layout Pad	2024/12/24	Vince

## 2. SCHEMATIC:



## 3. ELECTRICAL CHARACTERISTICS :

- TR:(100KHz,0.1V);  
PINS:(P2,P3):(J1,J2)=1:1±3%:(P4,P5):(J3,J6)=1:1±3%  
PINS:(P6,P7):(J4,J5)=1:1±3%:(P8,P9):(J7,J8)=1:1±3%
- LX:(100KHz,100mV,8mA, DC Bias)  
PINS:(P2,P3),(P4,P5)=350uH MINIMUM  
PINS:(P6,P7),(P8,P9)=350uH MINIMUM
- DCR:  
PINS:(J1,J2),(J3,J6)=1.2 Ω MAXIMUM  
PINS:(J4,J5),(J7,J8)=1.2 Ω MAXIMUM
- HIPOT: 100%  
PINS(P2,P3)TO(J1,J2),(P4,P5)TO(J3,J6)=1500VAC FOR 60 SECONDS  
PINS(P6,P7)TO(J4,J5),(P8,P9)TO(J7,J8)=1500VAC FOR 60 SECONDS
- INSERTION LOSS:  
-1.0dB MAXIMUM AT 1MHz TO 100MHz;
- RETURN LOSS:  
-18dB MINIMUM AT 1MHz TO 30MHz;  
-16dB MINIMUM AT 30MHz TO 60MHz  
-12dB MINIMUM AT 60MHz TO 80MHz  
-10dB MINIMUM AT 80MHz TO 100MHz
- CROSS TALK:  
-30dB MINIMUM AT 1MHz TO 100MHz
- COMMON TO COMMON MODE REJECTION:  
-30dB MINIMUM AT 1MHz TO 100MHz

MATRIX PART NO:

**MRJG-11S1104RS**

Matrix-RJ45  
F : 10/100  
G : 10/100/1000  
N : Pure Connector

RS: ROHS  
HF: Halogen Free

Series Number

S: SMD  
T: THT

11:1X1 ; 21:2X1  
12:1X2 ; 22:2X2  
14:1X4 ; 24:2X4  
16:1X6 ; 26:2X6  
18:1X8 ; 28:2X8  
1U:RJ+USB

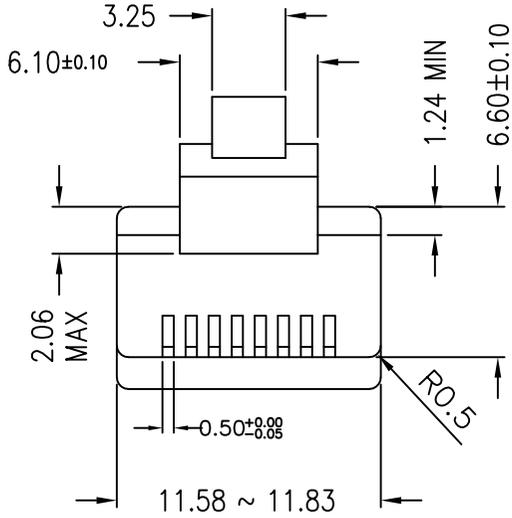


Matrix Electronics Co.,Ltd

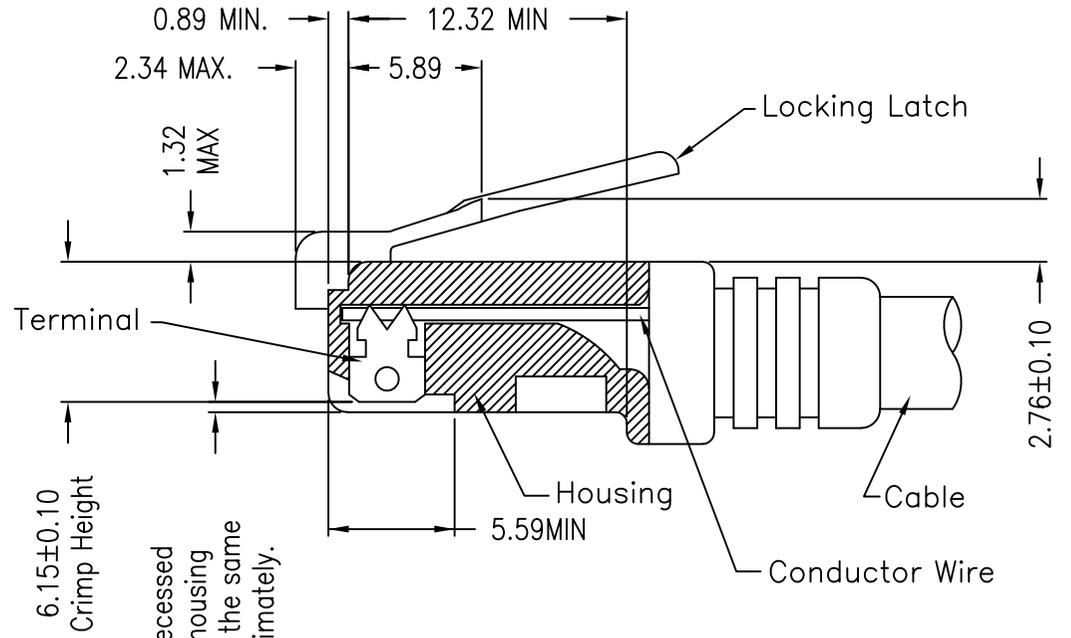
<b>TOLERANCE:</b> X:X ±0.38 X:XX ±0.25 X:XXX ±0.13 ANGLE: ±3	<b>DESIGN BY :</b> Vince Chen	<b>DATE :</b> 2024/12/24	<b>PART NAME:</b> RJ45 1X1 TAB UP SMT TYPE GIGABIT W/SHELL&W/O LED		
	<b>CHECKED BY:</b> Hanson Huang	<b>DATE :</b> 2024/12/24	<b>PART NO.</b> MRJG-11S1104RS		
 <b>UNIT: mm [inch]</b> <b>SCALE:1:1 SIZE:A4</b>	<b>APPROVED BY1:</b> Richard Hsieh	<b>DATE :</b> 2024/12/24	<b>MOLD NO.</b> NA		
	<b>APPROVED BY2:</b> Richard Hsieh	<b>DATE :</b> 2024/12/24	<b>DRAW NO.</b>		
			<b>SHEET NO.</b> 2 OF 3		

# GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
AO			Initial	2018/11/22	Phebe Su
AO.1			Add Pcb layout Pad	2024/12/24	Vince



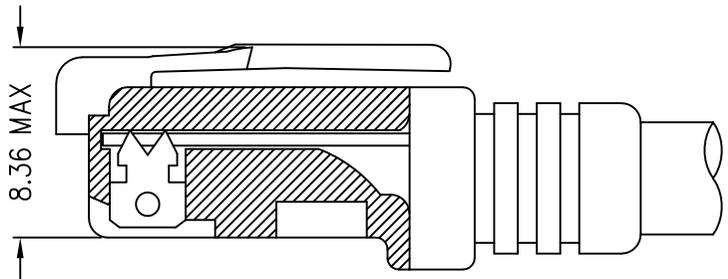
- \* There must be no damage to housing or locking latch. There must be no nicks or cuts in cable.
- \* Durability : 750 cycles generally



All contacts recessed below top of housing and must be at the same height approximately.

FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F FIGURE 68.500 (C)(2)(i) AND IEC 603-7 FIGURE 23 & 24

## STANDARD MODULAR PLUG ASSEMBLY



FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F FIGURE 68.500 (C)(2)(ii)

 <b>Matrix Electronics Co.,Ltd</b>			
<b>TOLERANCE:</b> X:X ±0.38 X:XX ±0.25 X:XXX ±0.13 ANGLE: ±3°	<b>DESIGN BY :</b> Vince Chen	<b>DATE :</b> 2024/12/24	<b>PART NAME:</b> RJ45 1X1 TAB UP SMT TYPE GIGABIT W/SHELL&W/O LED
 UNIT: mm [inch] SCALE:1:1 SIZE:A4	<b>CHECKED BY:</b> Hanson Huang	<b>DATE :</b> 2024/12/24	<b>PART NO.</b> MRJG-11S1104RS
	<b>APPROVED BY1:</b> Richard Hsieh	<b>DATE :</b> 2024/12/24	<b>MOLD NO.</b> NA
	<b>APPROVED BY2:</b> Richard Hsieh	<b>DATE :</b> 2024/12/24	<b>DRAW NO.</b> SHEET NO. 3 OF 3