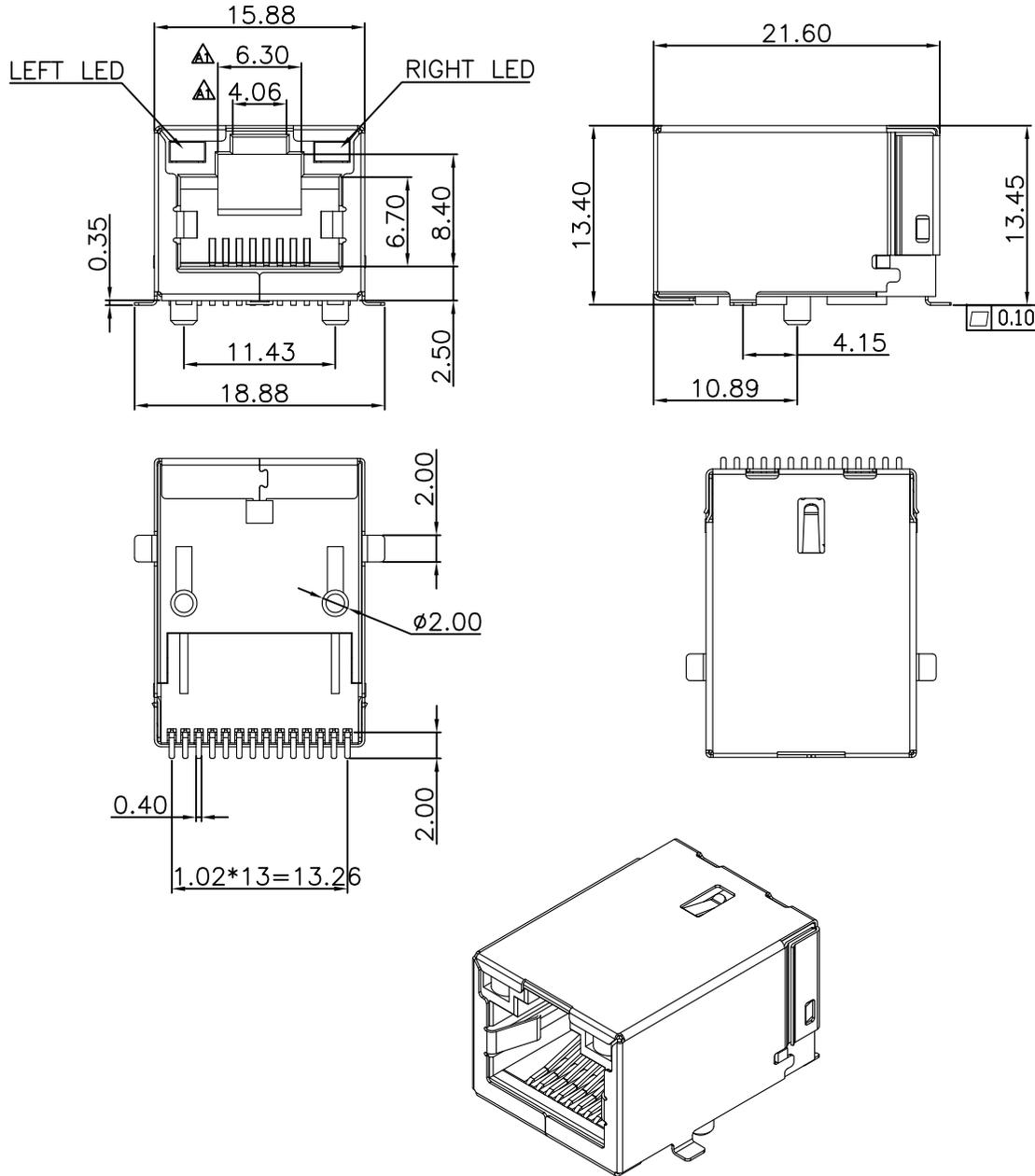


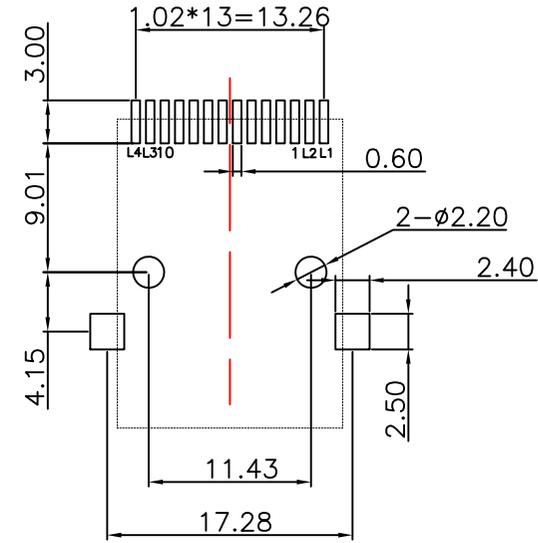
# GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2013/9/26	Jamie Zie
A1			Modify Size	2024/11/20	Vince

## 1. MECHANICAL:



## 2. PCB LAYOUT :



Suggested PCB Layout(Top View)  
(Tolerance:±0.05)

### 1.Material:

- 1.1 Housing: LCP UL94V-0 Black.
- 1.2 Contact :Phosphor Bronze,Gold plated on contact area Fu".
- 1.3 Input Terminal : Brass.
- 1.4 RJ Shielding:Copper alloy with Nickel plated.

### 2.Environmental requirements:

- 2.1 Lead-free process,compliant RoHS.



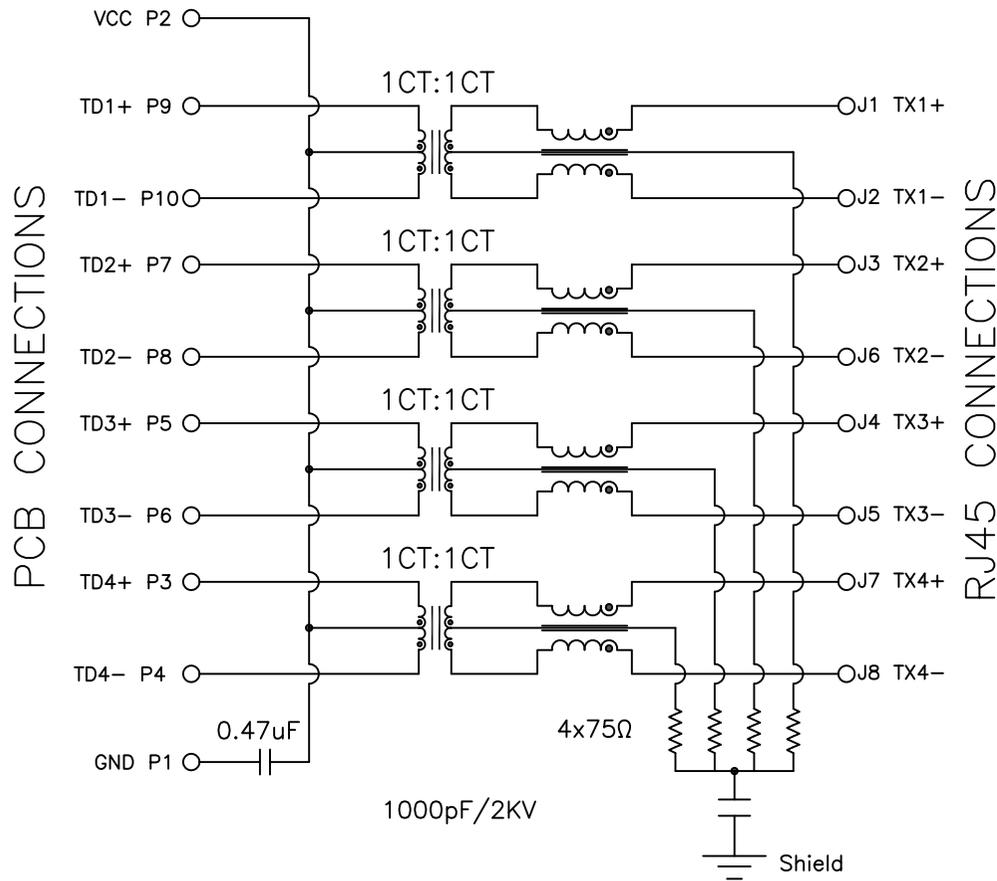
Matrix Electronics Co.,Ltd

TOLERANCE: X:X ±0.38 X:XX ±0.25 X:XXX ±0.13 ANGLE: ±3	DESIGN BY : Vince Chen	DATE : 2024/11/20	PART NAME: 1x1RJ45 W/LED TAB UP	
 UNIT: mm [inch] SCALE:1:1 SIZE:A4	CHECKED BY: Hanson Huang	DATE : 2024/11/20	PART NO.	MRJG-11S88RS
	APPROVED BY1: Richard Hsieh	DATE : 2024/11/20	MOLD NO.	NA
	APPROVED BY2: Richard Hsieh	DATE : 2024/11/20	DRAW NO.	
			SHEET NO.	1 OF 3

# GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2013/9/26	Jamie Zie
A1			Modify Size	2024/11/20	Vince

## 3. SCHEMATIC:



## 3. Electrical Specification @25°C:

3.1 Inductance: 350uH Min With 8mA DC bias.

3.2 Insertion loss : 1-100MHZ -1dB max.

3.3 Return loss : 1-30MHZ -18dB min.

30-60MHZ -16dB min.

60-80MHZ -12dB min.

80-100MHZ -10dB min.

3.4 Common Mode Rejection:1-100MHZ -30dB min.

3.5 Cross Talk:1-100MHZ -30dB min.

## 4. Operating and Storage Temperature:

4.1 Operating Temperature : 0°C to +70°C.

4.2 Storage Temperature : -40°C to +85°C.

5. IR Reflow Peak Temperature:250±5°C/10s Max.



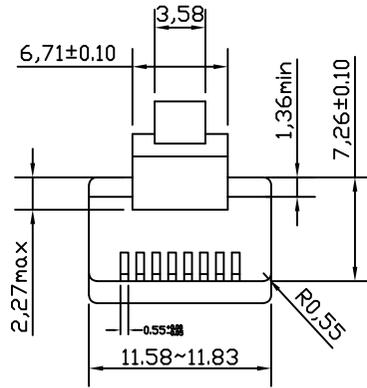
Emitting Color	$\lambda$ p(nm)	Vf@If=20mA
GREEN	565	1.8-2.5V
YELLOW	585	1.8-2.5V
ORANGE	610	1.8-2.5V

Matrix Electronics Co.,Ltd			
TOLERANCE: X:X ±0.38 X:XX ±0.25 X:XXX ±0.13 ANGLE: ±3	DESIGN BY : Vince Chen	DATE : 2024/11/20	PART NAME: 1x1RJ45 W/LED TAB UP
	CHECKED BY: Hanson Huang	DATE : 2024/11/20	PART NO. MRJG-11S88RS
	APPROVED BY1: Richard Hsieh	DATE : 2024/11/20	MOLD NO. NA
UNIT: mm [inch]	APPROVED BY2: Richard Hsieh	DATE : 2024/11/20	DRAW NO.
SCALE:1:1 SIZE:A4			SHEET NO. 2 OF 3

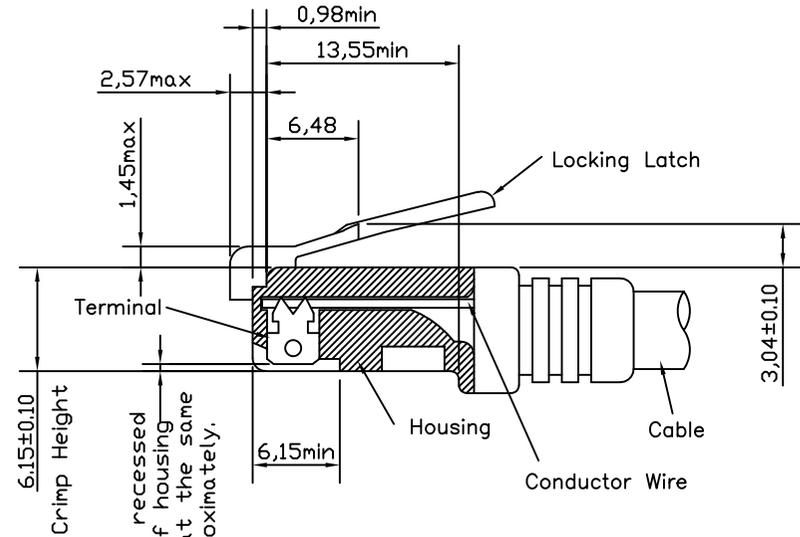
# GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2013/9/26	Jamie Zie
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## 4. Crystal head size:



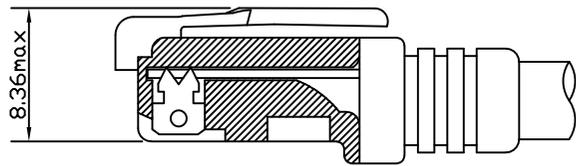
- \* There must be no damage to housing or locking latch. There must be no nicks or cuts in cable.
- \* Durability : 750 cycles generally



All contacts recessed below top of housing and must be at the same height approximately.

FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F FIGURE 68.500 (C)(2)(i) AND IEC 603-7 FIGURE 23 & 24

## STANDARD MODULAR PLUG ASSEMBLY



FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F FIGURE 68.500 (C)(2)(ii)

 <b>Matrix Electronics Co.,Ltd</b>				
<b>TOLERANCE:</b> X.X ±0.38 X.XX ±0.25 X.XXX ±0.13 ANGLE: ±3°		<b>DESIGN BY :</b> Vince Chen	<b>DATE :</b> 2024/11/20	<b>PART NAME:</b> 1x1RJ45 W/LED TAB UP
UNIT: mm [inch] SCALE:1:1 SIZE:A4		<b>CHECKED BY:</b> Hanson Huang	<b>DATE :</b> 2024/11/20	<b>PART NO.</b> MRJG-11S88RS
		<b>APPROVED BY1:</b> Richard Hsieh	<b>DATE :</b> 2024/11/20	<b>MOLD NO.</b> NA
		<b>APPROVED BY2:</b> Richard Hsieh	<b>DATE :</b> 2024/11/20	<b>DRAW NO.</b> SHEET NO. 3 OF 3