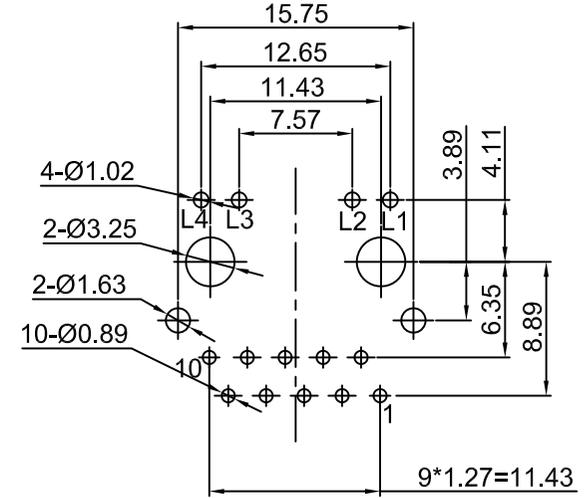
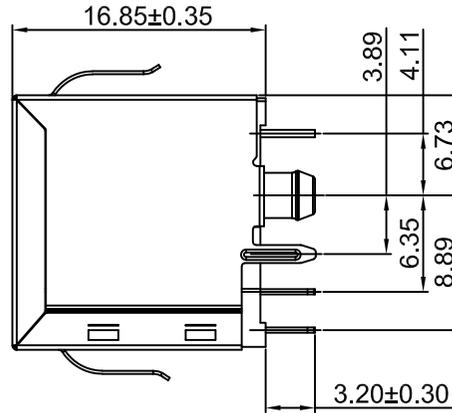
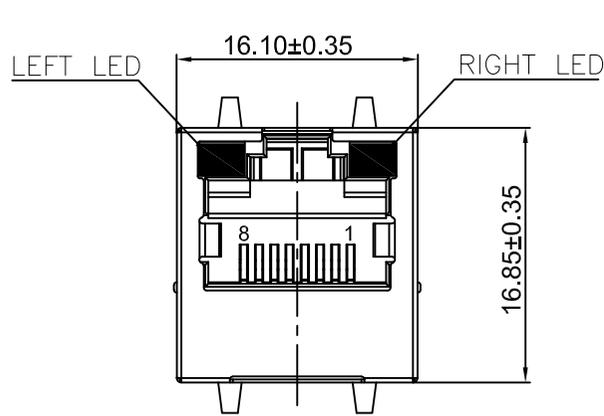


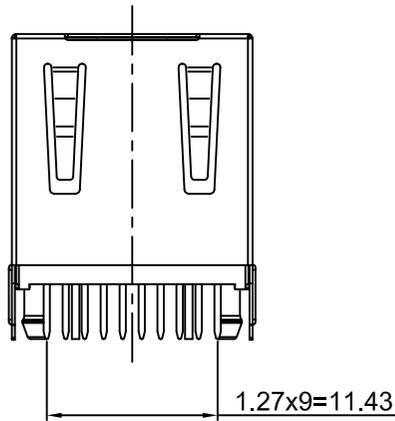
# GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2022/10/11	Vince
A0.1			Modify Part No	2023/10/11	Vince
A0.2			Modify Gold plated	2024/01/11	Vince

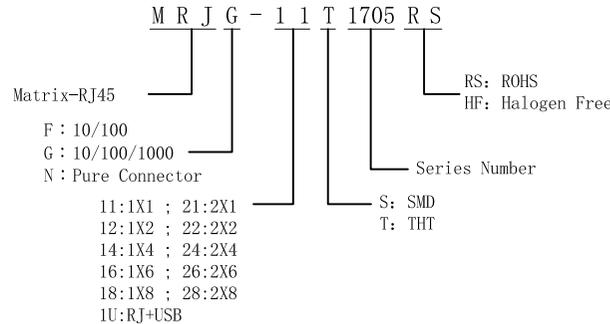
## 1. MECHANICAL DIMENSIONS :



Suggested PCB Layout(Top View)  
(Tolerance:±0.05)



MATRIX PART NO:



### 1.Material:

- 1.1 Housing: PBT UL94V-0 Black.
- 1.2 Insert core:PBT UL94V-0 Black.
- 1.3 Contact :Phosphor Bronze,Gold plated on contact area 30u",Tin plated on solder area 80u" min, overall nickel plating 30u" min.
- 1.4 Input Terminal : Brass,Tin plated on solder area 80u" min, Ni underplated overall 30u" min.
- 1.5 Metal shielding:Copper alloy with Nickel plated.

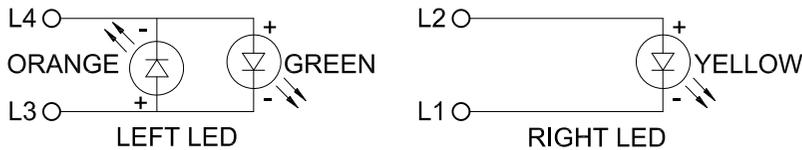
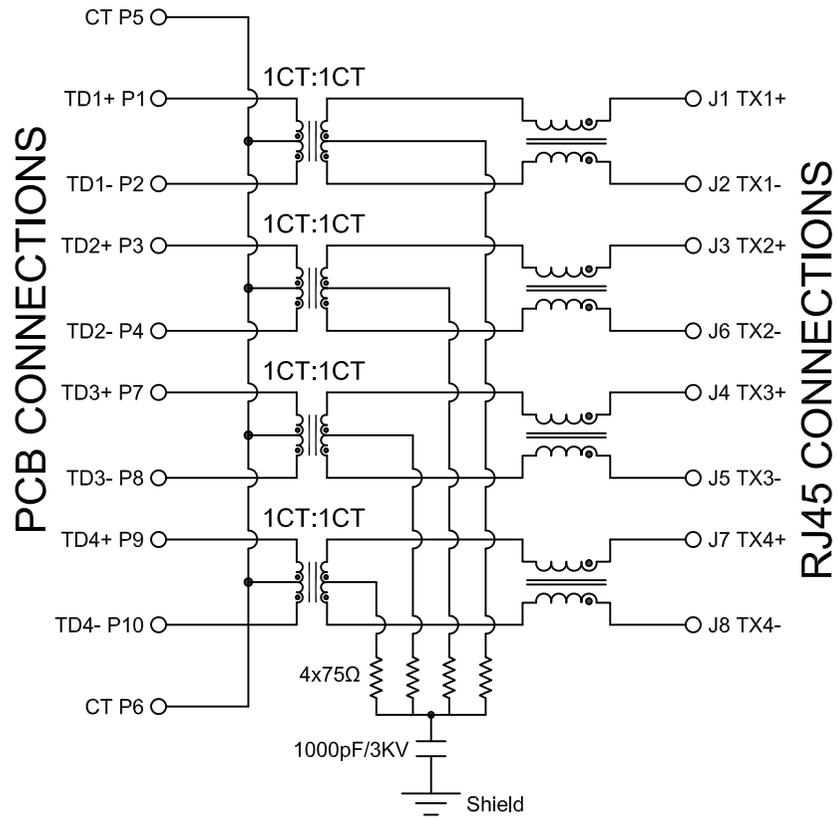
### 2.Environmental requirements:

- 2.1 Lead-free process,compliant RoHS.

Matrix Electronics Co., Ltd			
<b>TOLERANCE:</b> X: X.X ±0.38 X.XX ±0.25 X.XXX ±0.13 ANGLE: ±3°	<b>DESIGN BY :</b> Vince Chen	<b>DATE :</b> 2024/01/11	<b>PART NAME:</b> RJ45, CONNECTORS, W/LED DIP VERTICAL SC, 10P GIGA, FILTER
 <b>UNIT: mm [inch]</b> <b>SCALE:1:1 SIZE:A4</b>	<b>CHECKED BY:</b> Hanson Huang	<b>DATE :</b> 2024/01/11	<b>PART NO.</b> MRJG-11T1705RS
	<b>APPROVED BY1:</b> Richard Hsieh	<b>DATE :</b> 2024/01/11	<b>MOLD NO.</b> NA
	<b>APPROVED BY2:</b> Richard Hsieh	<b>DATE :</b> 2024/01/11	<b>DRAW NO.</b>
			<b>SHEET NO.</b> 1 OF 3

# GP Component

## 2. SCHEMATIC:



Emitting Color	$\lambda_p$ (nm)	$V_f@I_f=20mA$
GREEN	565	1.8-2.5V
YELLOW	585	1.8-2.5V
ORANGE	610	1.8-2.5V

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2022/10/11	Vince
A0.1			Modify Part No	2023/10/11	Vince
A0.2			Modify Gold plated	2024/01/11	Vince

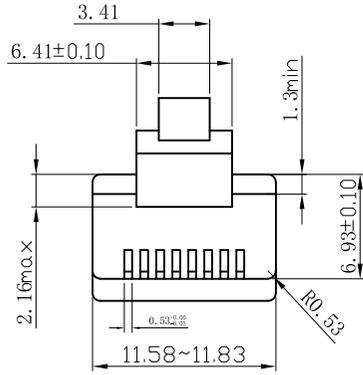
## 3. Electrical Specification @25°C:

- 3.1 Inductance: 350uH Min With 8mA DC bias.
- 3.2 Hi-Pot Test: Input to Output 2250VDC/1mA.
- 3.3 Insertion loss : 1-100MHZ -1dB max.
- 3.4 Return loss : 1-30MHZ -18dB min.  
30-60MHZ -16dB min.  
60-80MHZ -12dB min.  
80-100MHZ -10dB min.
- 3.5 Common Mode Rejection:1-100MHZ -30dB min.
- 3.6 Cross Talk:1-100MHZ -30dB min.
- 4. Operating and Storage Temperature:
  - 4.1 Operating Temperature : 0°C to +70°C.
  - 4.2 Storage Temperature : -40°C to +85°C.
- 5. Wave soldering peak temperature:260°C.

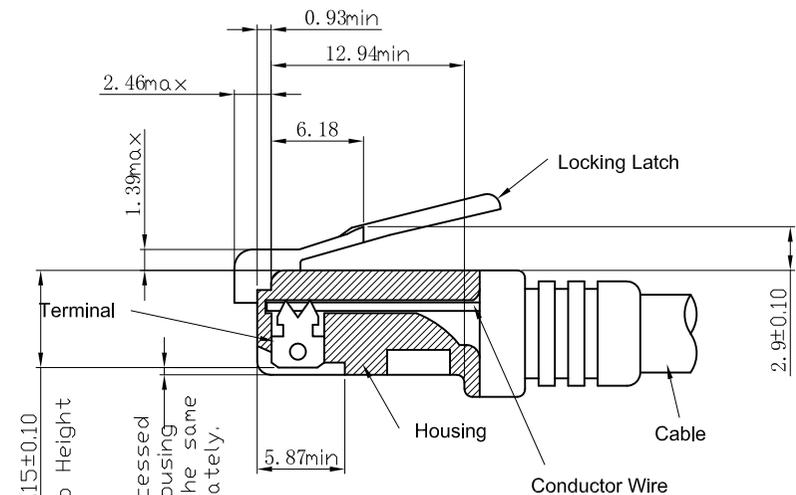
 Matrix Electronics Co., Ltd				
TOLERANCE: X: X X.X ±0.38 X.XX ±0.25 X.XXX ±0.13 ANGLE: ±3°		DESIGN BY : Vince Chen	DATE : 2024/01/11	PART NAME: RJ45, CONNECTORS, W/LED DIP VERTICAL SC, 10P, GIGA, FILTER
 UNIT: mm [inch]		CHECKED BY: Hanson Huang	DATE : 2024/01/11	PART NO. MRJG-11T1705RS
SCALE:1:1 SIZE:A4		APPROVED BY1: Richard Hsieh	DATE : 2024/01/11	MOLD NO. NA
		APPROVED BY2: Richard Hsieh	DATE : 2024/01/11	DRAW NO. SHEET NO. 2 OF 3

# GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2022/10/11	Vince
A0.1			Modify Part No	2023/10/11	Vince
A0.2			Modify Gold plated	2024/01/11	Vince



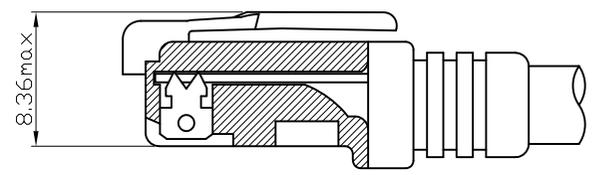
- \* There must be no damage to housing or locking latch. There must be no nicks or cuts in cable.
- \* Durability : 750 cycles generally



All contacts recessed below top of housing and must be at the same height approximately.

FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F FIGURE 68.500 (C)(2)(i) AND IEC 603-7 FIGURE 23 & 24

STANDARD MODULAR PLUG ASSEMBLY



FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F FIGURE 68.500 (C)(2)(ii)

 <b>Matrix Electronics Co., Ltd</b>			
<b>TOLERANCE:</b> X X.X ±0.38 X.XX ±0.25 X.XXX ±0.13 ANGLE: ±3°	<b>DESIGN BY :</b> Vince Chen	<b>DATE :</b> 2024/01/11	<b>PART NAME:</b> RJ45, CONNECTORS, W/LED DIP VERTICAL SC, 10P GIGA, FILTER
	<b>CHECKED BY:</b> Hanson Huang	<b>DATE :</b> 2024/01/11	<b>PART NO.</b> MRJG-11T1705RS
 UNIT: mm [inch] SCALE:1:1 SIZE:A4	<b>APPROVED BY1:</b> Richard Hsieh	<b>DATE :</b> 2024/01/11	<b>MOLD NO.</b> NA
	<b>APPROVED BY2:</b> Richard Hsieh	<b>DATE :</b> 2024/01/11	<b>DRAW NO.</b> SHEET NO. 3 OF 3