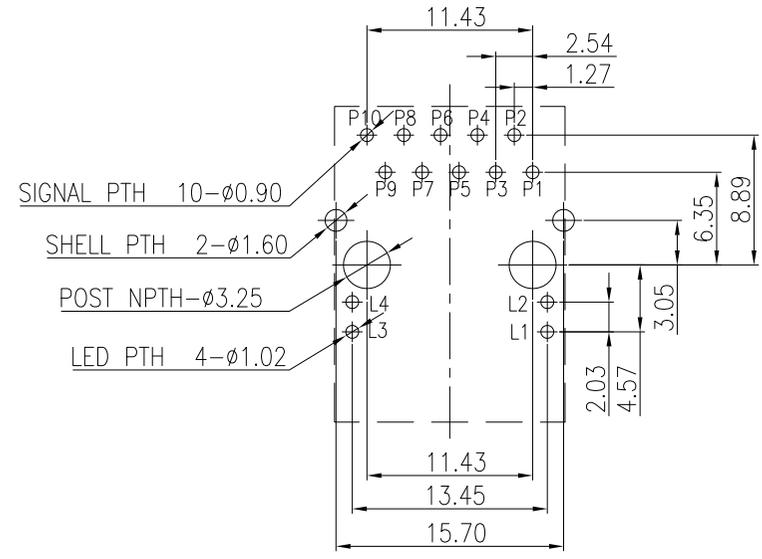
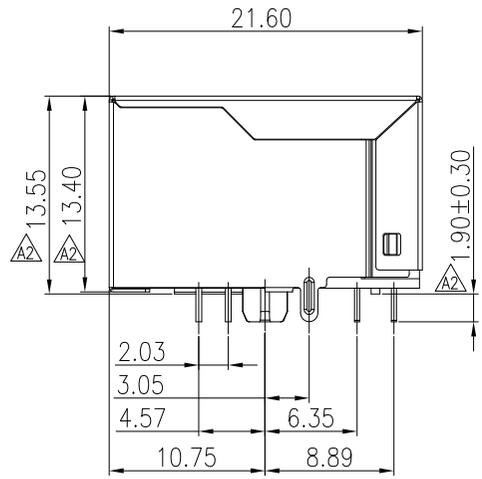
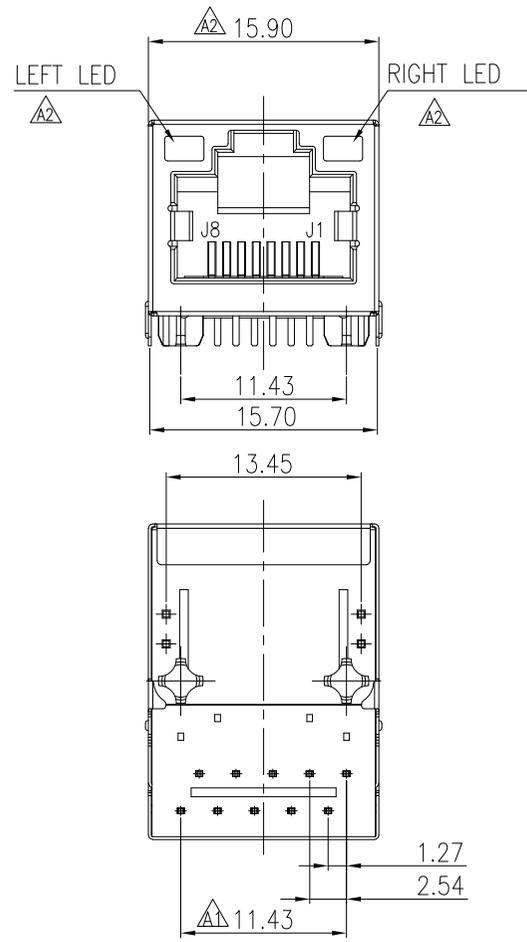


# GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2021/11/10	Vince
A1			Modify Size	2023/11/14	Vince
A2			Modify Size & Appearance	2025/09/30	Vince

## 1. MECHANICAL DIMENSIONS :



Suggested PCB Layout(Top View)  
(Tolerance:±0.05)

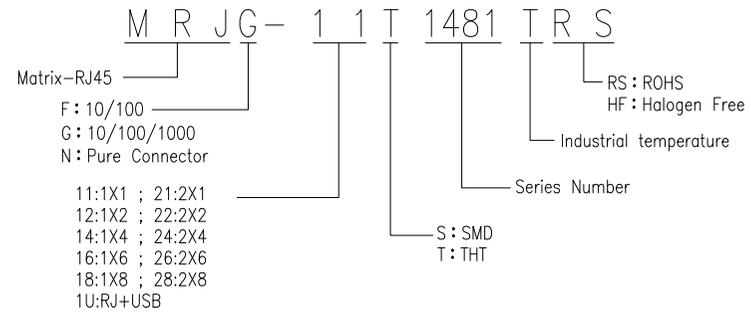
### 1. Material:

- 1.1 Housing: LCP UL94V-0 Black.
- 1.2 Insert core:LCP UL94V-0 Black.
- 1.2 Contact :Phosphor Bronze,Gold plated on contact area 30u",Tin plated on solder area,Ni underplated overall.
- 1.3 Input Terminal : Brass,Tin plated on solder area,Ni underplated overall.
- 1.4 Metal shielding:Copper alloy with Nickel plated.

### 2.Environmental requirements:

- 2.1 Lead-free process,compliant RoHS.

Printing:  
MATRIX  
MRJG-11T1481TRS  
Y Y W W

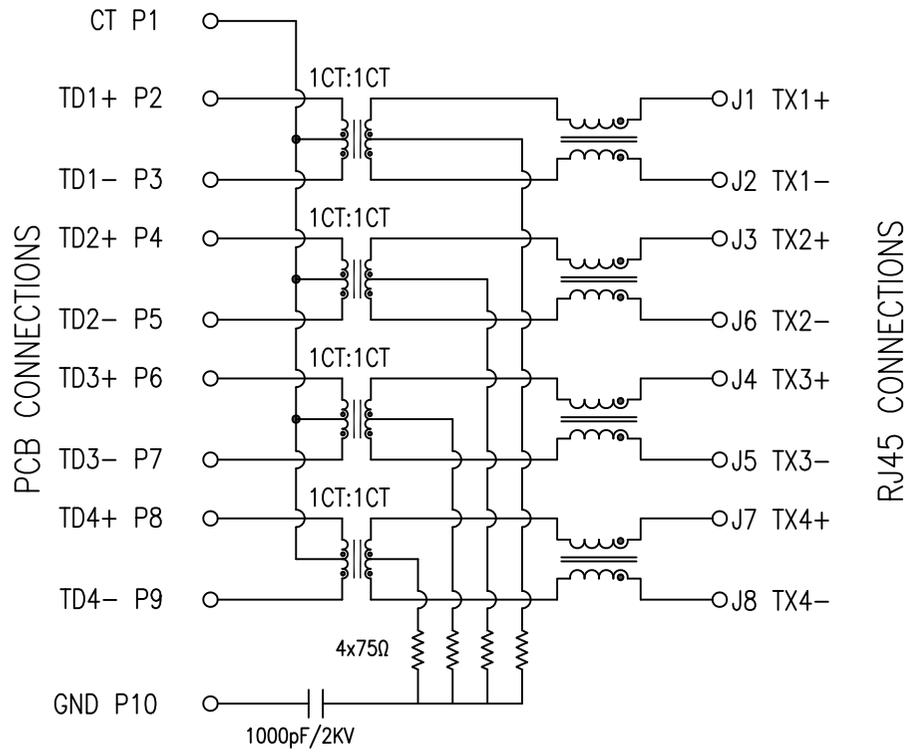


 <b>Matrix Electronics Co.,Ltd</b>			
<b>TOLERANCE:</b> X:X ± 0.38 X:XX ± 0.25 X:XXX ± 0.13 ANGLE: ± 3°	<b>DESIGN BY :</b> Vince Chen	<b>DATE :</b> 2025/09/30	<b>PART NAME:</b> RJ45.CONNECTORS,W/LED 10/100/1000,FILTER
 UNIT: mm [inch]	<b>CHECKED BY:</b> Hanson Huang	<b>DATE :</b> 2025/09/30	<b>PART NO.</b> MRJG-11T1481TRS
<b>SCALE:1:1</b>	<b>APPROVED BY1:</b> Richard Hsieh	<b>DATE :</b> 2025/09/30	<b>MOLD NO.</b> NA
<b>SIZE:A4</b>	<b>APPROVED BY2:</b> Richard Hsieh	<b>DATE :</b> 2025/09/30	<b>DRAW NO.</b> SHEET NO. 1 OF 4

# GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
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A1			Modify Size	2023/11/14	Vince
A2			Modify Size & Appearance	2025/09/30	Vince

## 2. SCHEMATIC:



## 3. ELECTRICAL SPECIFICATION:

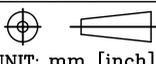
- 3.1 Inductance: 350uH Min With 8mA DC bias.
- 3.2 Hi-Pot Test: Input to Output 2250VDC/1mA.
- 3.3 Insertion loss : 1-100MHZ -1dB max.
- 3.4 Return loss : 1-30MHZ -18dB min.  
30-60MHZ -16dB min.  
60-80MHZ -12dB min.  
80-100MHZ -10dB min.
- 3.5 Common Mode Rejection:1-100MHZ -30dB min.
- 3.6 Cross Talk:1-100MHZ -30dB min.
- 4. Operating and Storage Temperature:
  - 4.1 Operating Temperature : -40°C to +85°C.
  - 4.2 Storage Temperature : -40°C to +85°C.
- 5. IR Reflow Peak Temperature:250±5°C/10s Max.



Emitting Color	λ p(nm)	Vf@If=20mA
GREEN	565	1.8-2.5V
YELLOW	585	1.8-2.5V



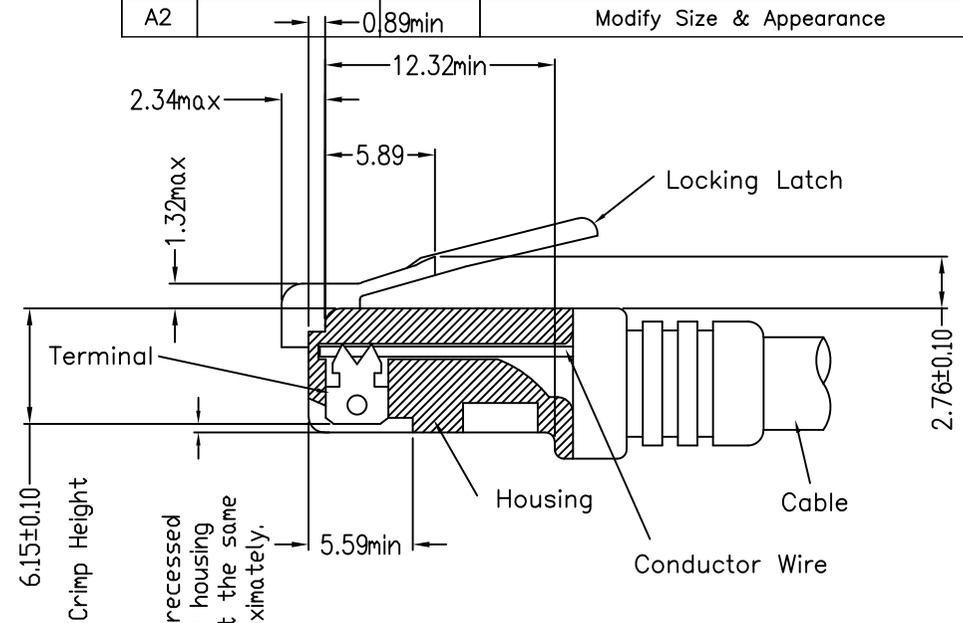
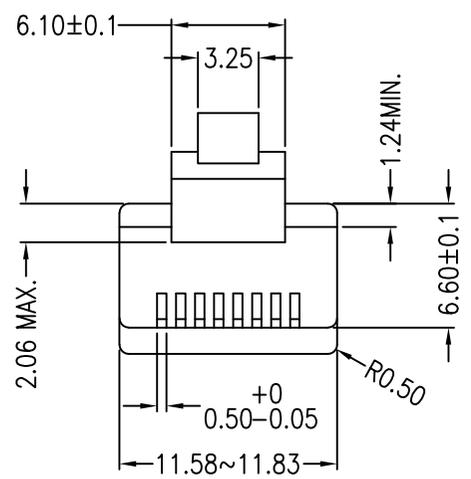
### Matrix Electronics Co.,Ltd

<b>TOLERANCE:</b> X:X ± 0.38 X:XX ± 0.25 X:XXX ± 0.13 ANGLE: ± 3°	DESIGN BY : Vince Chen	DATE : 2025/09/30	PART NAME: RJ45.CONNECTORS,W/LED 10/100/1000,FILTER	
	CHECKED BY: Hanson Huang	DATE : 2025/09/30	PART NO.	MRJG-11T1481TRS
 UNIT: mm [inch] SCALE:1:1 SIZE:A4	APPROVED BY1: Richard Hsieh	DATE : 2025/09/30	MOLD NO.	NA
	APPROVED BY2: Richard Hsieh	DATE : 2025/09/30	DRAW NO.	
			SHEET NO.	2 OF 4



# GP Component

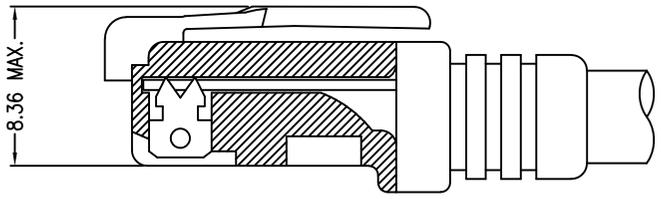
REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2021/11/10	Vince
A1			Modify Size	2023/11/14	Vince
A2			Modify Size & Appearance	2025/09/30	Vince



- \* There must be no damage to housing or locking latch. There must be no nicks or cuts in cable.
- \* Durability : 750 cycles generally

All contacts recessed below top of housing and must be at the same height approximately.

FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F FIGURE 68.500 (C)(2)(i) AND IEC 603-7 FIGURE 23 & 24



FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F FIGURE 68.500 (C)(2)(ii)

 <b>Matrix Electronics Co.,Ltd</b>			
<b>TOLERANCE:</b> X.X ± 0.38 X.XX ± 0.25 X.XXX ± 0.13 ANGLE: ± 3°	<b>DESIGN BY :</b> Vince Chen	<b>DATE :</b> 2025/09/30	<b>PART NAME:</b> RJ45.CONNECTORS,W/LED 10/100/1000,FILTER
 UNIT: mm [inch]	<b>CHECKED BY:</b> Hanson Huang	<b>DATE :</b> 2025/09/30	<b>PART NO.</b> MRJG-11T1481TRS
<b>SCALE:1:1</b>   <b>SIZE:A4</b>	<b>APPROVED BY1:</b> Richard Hsieh	<b>DATE :</b> 2025/09/30	<b>MOLD NO.</b> NA
	<b>APPROVED BY2:</b> Richard Hsieh	<b>DATE :</b> 2025/09/30	<b>DRAW NO.</b>
			<b>SHEET NO.</b> 4 OF 4