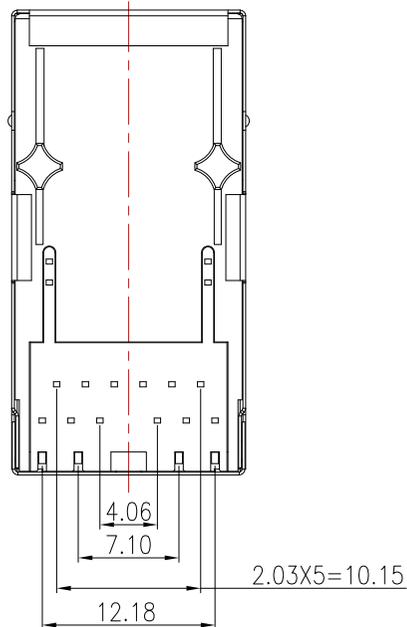
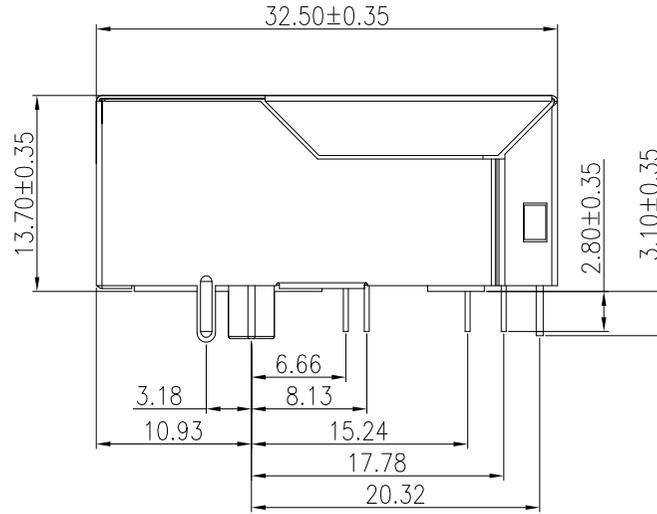
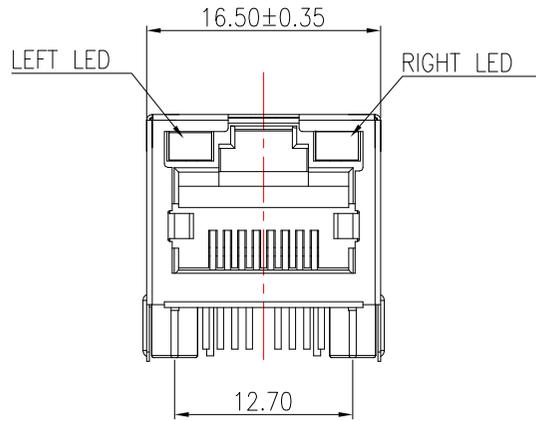


# GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2021/03/10	Vince
A1			Remove Shell shrapnel	2022/03/21	Vince

## 1. MECHANICAL DIMENSIONS :



### 1. Material:

- 1.1 Housing: PBT UL94V-0 Black.
- 1.2 Insert core: PBT UL94V-0 Black.
- 1.3 Contact : Phosphor Bronze, Gold plated on contact area 6u", Tin plated on solder area 80u" min, overall nickel plating 30u" min.
- 1.4 Input Terminal : Brass, Tin plated on solder area 80u" min, Ni underplated overall 30u" min.
- 1.5 Metal shielding: Copper alloy with Nickel plated.

### 2. Environmental requirements:

- 2.1 Lead-free process, compliant RoHS.

### ORDER INFORMATION

MRJG - 11T1368TRS

Matrix-RJ45

F: 10/100

G: 10/100/1000

N: Pure Connector

RS: ROHS

HF: Halogen Free

Industrial Temperature

Series Number

S: SMD

T: THT

11:1X1 ; 21:2X1

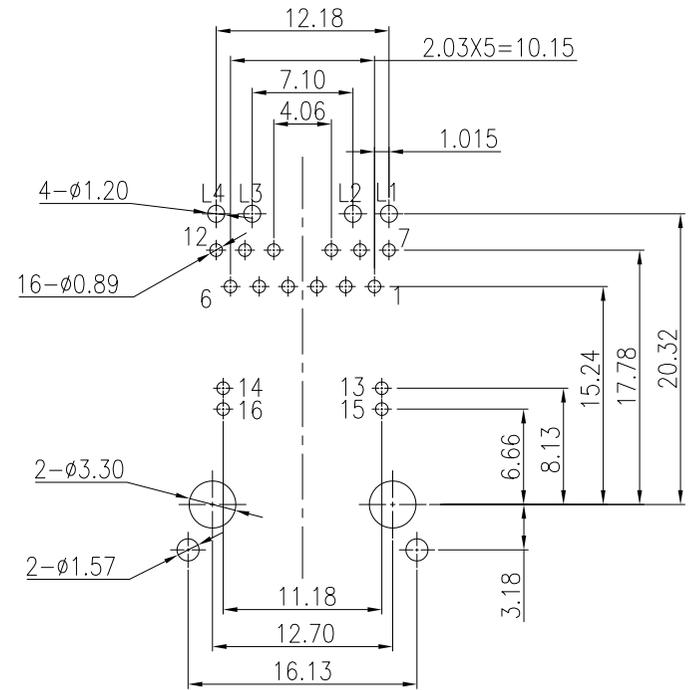
12:1X2 ; 22:2X2

14:1X4 ; 24:2X4

16:1X6 ; 26:2X6

18:1X8 ; 28:2X8

1U:RJ+USB



Suggested PCB Layout(Top View)  
(Tolerance:±0.05)

Printing:

MATRIX  
MRJG-11T1368TRS  
Y Y W W



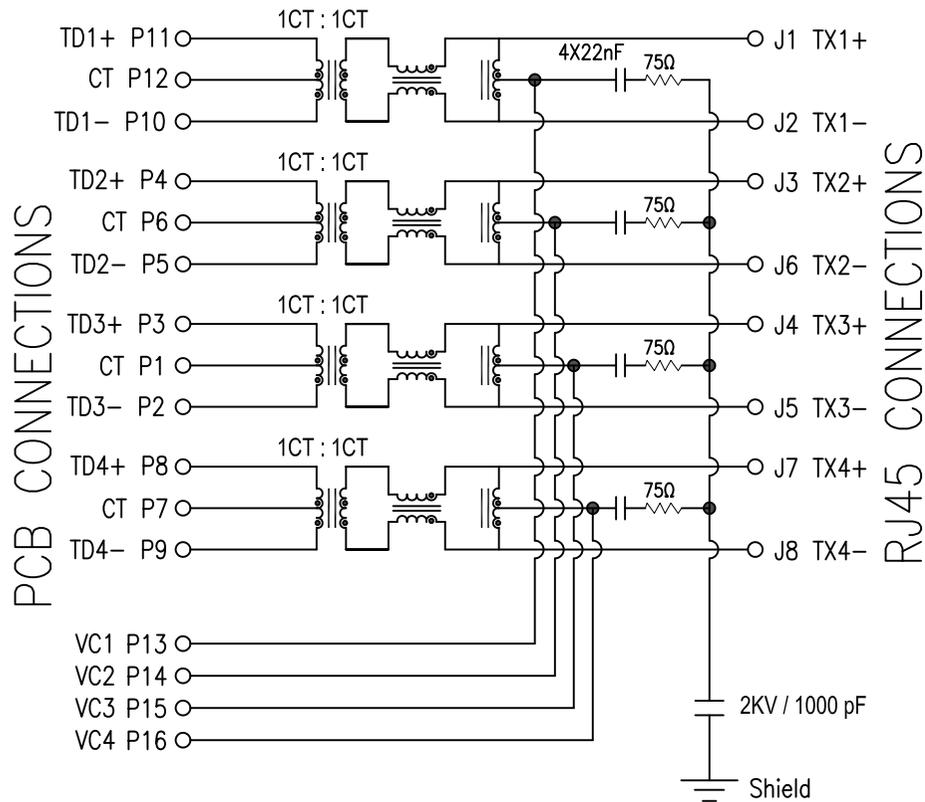
Matrix Electronics Co.,Ltd

TOLERANCE:		DESIGN BY :	DATE :	PART NAME:	
X:X	± 0.38	Vince Chen	2021/03/21	RJ45 W/LED W/Giga+POE TAB UP	
X.XX	± 0.25	CHECKED BY:	DATE :	PART NO.	MRJG-11T1368TRS
X.XXX	± 0.13	Hanson Huang	2021/03/21	MOLD NO.	NA
ANGLE:	± 3°	APPROVED BY1:	DATE :	DRAW NO.	
		Richard Hsieh	2021/03/21	SHEET NO.	1 OF 3
UNIT: mm [inch]	SCALE:1:1	APPROVED BY2:	DATE :		
SIZE:A4		Richard Hsieh	2021/03/21		

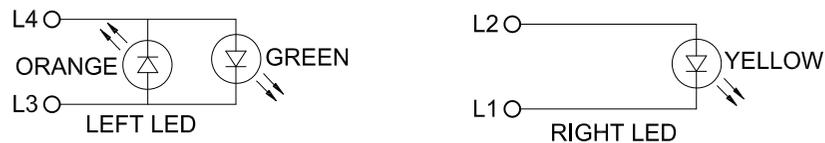
# GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2021/03/10	Vince
A1			Remove Shell shrapnel	2022/03/21	Vince

## 2. SCHEMATIC:



## 3. LED CONFIGURATION :



Emitting Color	$\lambda$ p(nm)	Vf@If=20mA
GREEN	565	1.8-2.5V
YELLOW	585	1.8-2.5V

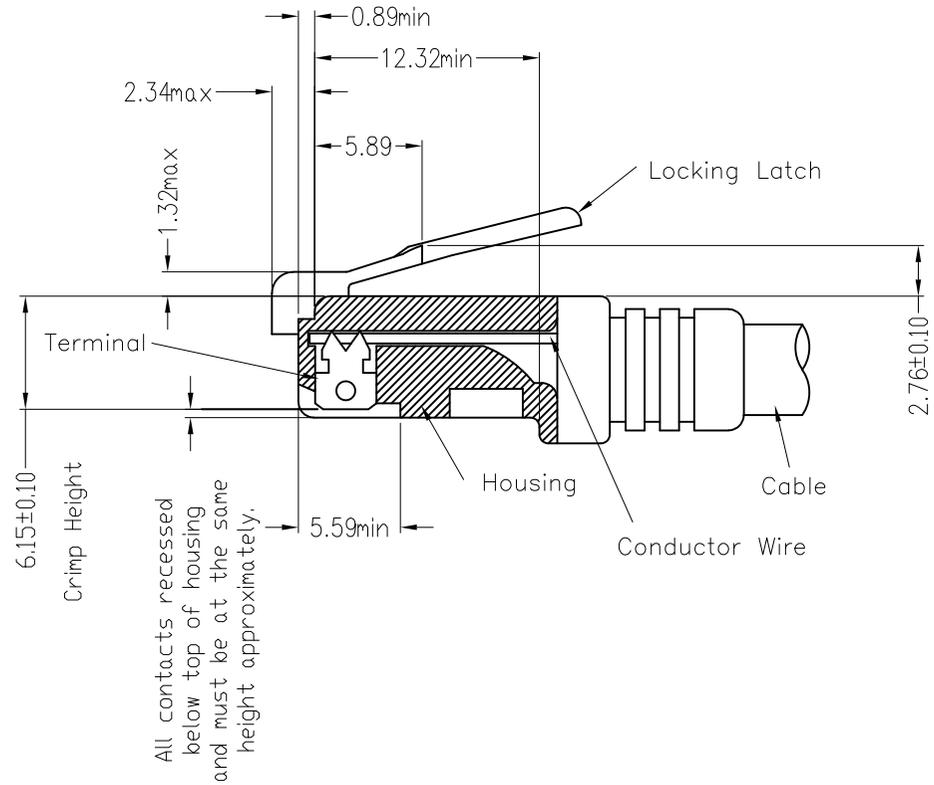
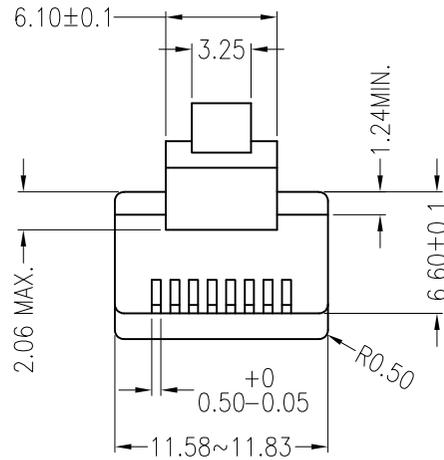
## 3. Electrical Specification @25°C:

- 3.1 Inductance: 350uH Min With 8mA DC bias.
  - 3.2 Hi-Pot Test: Input to Output 2250VDC/1mA.
  - 3.3 Insertion loss : 1-100MHZ -1dB max.
  - 3.4 Return loss :
    - 1-30MHZ -18dB min.
    - 30-60MHZ -16dB min.
    - 60-80MHZ -12dB min.
    - 80-100MHZ -10dB min.
  - 3.5 Common Mode Rejection:1-100MHZ -30dB min.
  - 3.6 Cross Talk:1-100MHZ -30dB min.
  - 3.7 DC current:720mA MAX @57VDC continuous.
4. Operating and Storage Temperature:
- 4.1 Operating Temperature : -40°C to +85°C.
  - 4.2 Storage Temperature : -40°C to +85°C.
5. Wave soldering peak temperature:260°C.

 Matrix Electronics Co.,Ltd			
<b>TOLERANCE:</b>	<b>DESIGN BY :</b>	<b>DATE :</b>	<b>PART NAME:</b>
X:X ± 0.38	Vince Chen	2021/03/21	RJ45 W/LED W/Giga+POE TAB UP
X.XX ± 0.25	<b>CHECKED BY:</b>	<b>DATE :</b>	<b>PART NO.</b>
X.XXX ± 0.13	Hanson Huang	2021/03/21	MRJG-11T1368TRS
ANGLE: ± 3°	<b>APPROVED BY1:</b>	<b>DATE :</b>	<b>MOLD NO.</b>
	Richard Hsieh	2021/03/21	NA
<b>UNIT: mm [inch]</b>	<b>APPROVED BY2:</b>	<b>DATE :</b>	<b>DRAW NO.</b>
<b>SCALE:1:1</b>	Richard Hsieh	2021/03/21	<b>SHEET NO.</b>
<b>SIZE:A4</b>			2 OF 3

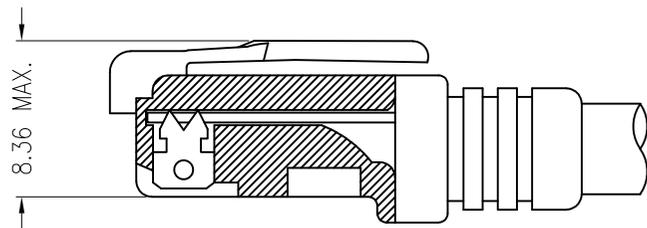
# GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2021/03/10	Vince
A1			Remove Shell shrapnel	2021/03/21	Vince



- \* There must be no damage to housing or locking latch. There must be no nicks or cuts in cable.
- \* Durability : 750 cycles generally

FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F FIGURE 68.500 (C)(2)(i) AND IEC 603-7 FIGURE 23 & 24



FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F FIGURE 68.500 (C)(2)(ii)

 <b>Matrix Electronics Co.,Ltd</b>			
<b>TOLERANCE:</b>	<b>DESIGN BY :</b>	<b>DATE :</b>	<b>PART NAME:</b>
X:X ± 0.38	Vince Chen	2021/03/21	RJ45 W/LED W/Giga+POE TAB UP
X.XX ± 0.25	<b>CHECKED BY:</b>	<b>DATE :</b>	<b>PART NO.</b>
X.XXX ± 0.13	Hanson Huang	2021/03/21	MRJG-11T1368TRS
ANGLE: ± 3°	<b>APPROVED BY1:</b>	<b>DATE :</b>	<b>MOLD NO.</b>
 	Richard Hsieh	2021/03/21	NA
<b>UNIT: mm [inch]</b>	<b>APPROVED BY2:</b>	<b>DATE :</b>	<b>DRAW NO.</b>
<b>SCALE:1:1</b> <b>SIZE:A4</b>	Richard Hsieh	2021/03/21	<b>SHEET NO.</b>
			3 OF 3