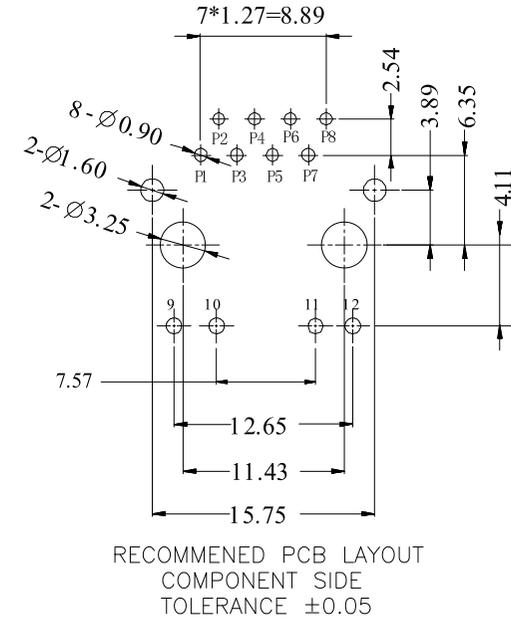
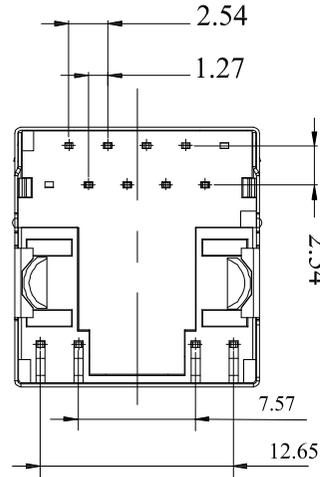
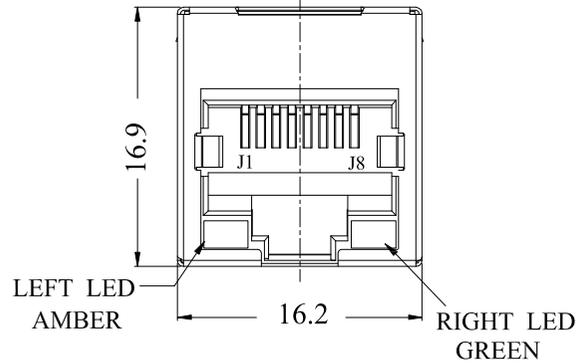


GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
AO			Initial	2018/08/11	Phebe Su

1. MECHANICAL DIMENSIONS :



Printing :
MATRIX
MRJF-11T1059TRS
YYWW

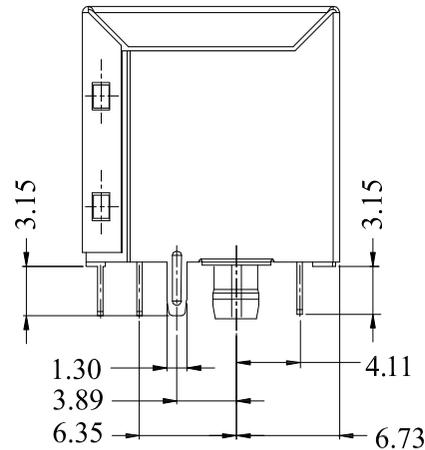
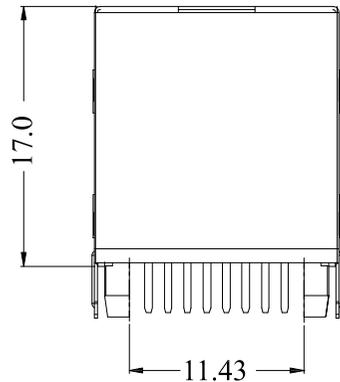
AMBER

GREEN



NOTES:

- MATERIALS:
HOUSING: PBT, UL94V-0, BLACK
CONTACTS: PHOSPHOR BRONZE, GOLD 6u" ON CONTACT AREA, TIN ON SOLDER TAIL
SHELL: COPPER ALLOY T=0.20
NICKEL PLATING OVERALL
- OPERATING TEMPERATURE: $-40^{\circ}\text{C} \sim +85^{\circ}\text{C}$
- JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68, SUBPART F



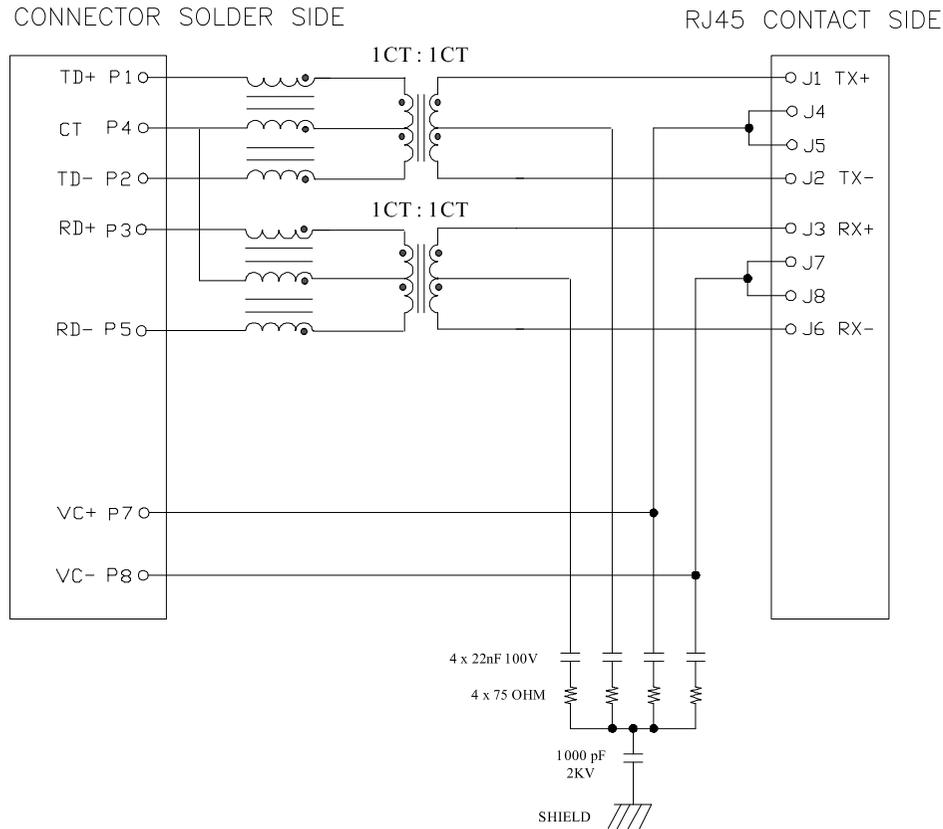
Matrix Electronics Co.,Ltd

TOLERANCE: X.X X.XX ± 0.38 X.XXX ± 0.25 ANGLE: $\pm 3^{\circ}$	DESIGN BY : Phebe Su	DATE : 2018/08/11	PART NAME: RJ45,CONNECTORS,W/LED THRU,HOLE10/100Mbps,POE,FILTER	
	CHECKED BY: Vicky Hsieh	DATE : 2018/08/11	PART NO.	MRJF-11T1059TRS
UNIT: mm [inch] SCALE:1:1 SIZE:A4	APPROVED BY1: Richard Hsieh	DATE : 2018/08/11	MOLD NO.	NA
	APPROVED BY2: Richard Hsieh	DATE : 2018/08/11	DRAW NO.	
			SHEET NO.	1 OF 3

GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2018/08/11	Phebe Su

2. SCHEMATIC:



MATRIX PART NO:

M R J F - 1 1 T 1059 T R S
 Matrix-RJ45
 F: 10/100
 G: 10/100/1000
 N: Pure Connector
 11:1X1 ; 21:2X1
 12:1X2 ; 22:2X2
 14:1X4 ; 24:2X4
 16:1X6 ; 26:2X6
 18:1X8 ; 28:2X8
 1U:RJ+USB
 S: SMD
 T: THT
 RS: ROHS
 HF: Halogen Free
 Industrial Temperature
 Series Number

3. ELECTRICAL CHARACTERISTICS(25±5°C) :

- TR:(100KHz,0.1V); 100%
PINS:(P1,P2):(J1,J2)=1:1±3%;
- LX:(100KHz,100mV,15mA, DC Bias) 100%
PINS:(P1,P2)=350uH MINIMUM
PINS:(P3,P5)=350uH MINIMUM
- DCR: 100%
PINS:(J1,J2),(J3,J6)=1.2 Ω MAXIMUM
- HIPOT: 100%
PINS(P1,P2)TO(J1,J2),(P3,P5)TO(J3,J6)=1500VAC FOR 60 SECONDS
- INSERTION LOSS:
-1.0dB MAXIMUM AT 1MHz TO 100MHz;
- RETURN LOSS:
-18dB MINIMUM AT 1MHz TO 30MHz;
-12dB MINIMUM AT 60MHz TO 80MHz
-10dB MINIMUM AT 80MHz TO 100MHz
- CROSS TALK:
-30dB MINIMUM AT 1MHz TO 100MHz
- COMMON TO COMMON MODE REJECTION:
-30dB MINIMUM AT 1MHz TO 100MHz
-20dB MINIMUM AT 150MHz
- Balanced DC line current 600mA Max@ 57VDC

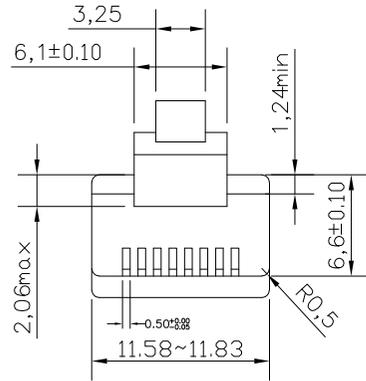


Matrix Electronics Co.,Ltd

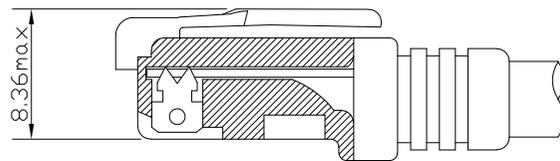
TOLERANCE: X:X ±0.38 X:XX ±0.25 X:XXX ±0.13 ANGLE: ±3°	DESIGN BY : Phebe Su	DATE : 2018/08/11	PART NAME: RJ45,CONNECTORS,W/LED THRU,HOLE10/100MBPS,POE,FILTER
	CHECKED BY: Vicky Hsieh	DATE : 2018/08/11	PART NO. MRJF-11T1059TRS
 UNIT: mm [inch] SCALE: 1:1 SIZE: A4	APPROVED BY1: Richard Hsieh	DATE : 2018/08/11	MOLD NO. NA
	APPROVED BY2: Richard Hsieh	DATE : 2018/08/11	DRAW NO. SHEET NO. 2 OF 3

GP Component

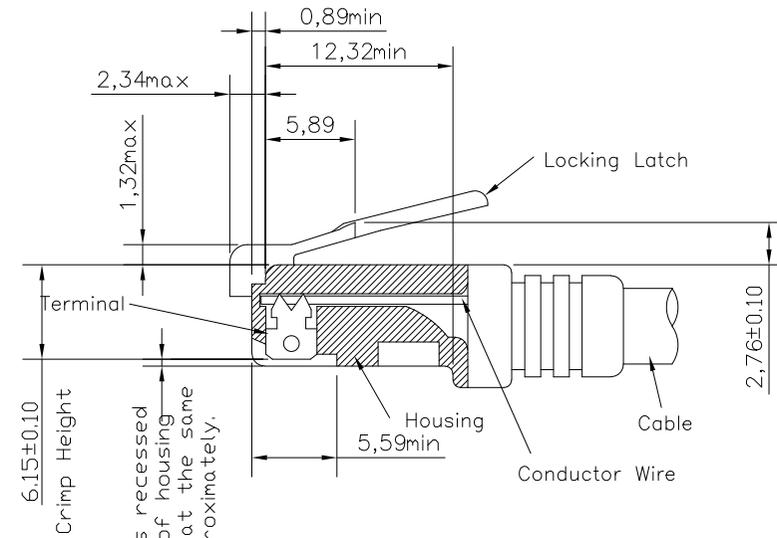
REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
AO			Initial	2018/08/11	Phebe Su



- * There must be no damage to housing or locking latch. There must be no nicks or cuts in cable.
- * Durability : 750 cycles generally



FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F
FIGURE 68.500 (C)(2)(ii)



All contacts recessed below top of housing and must be at the same height approximately.

FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F FIGURE 68.500 (C)(2)(i) AND IEC 603-7 FIGURE 23 & 24

STANDARD MODULAR PLUG ASSEMBLY



Matrix Electronics Co.,Ltd

TOLERANCE: X.X ±0.38 X.XX ±0.25 X.XXX ±0.13 ANGLE: ±3°	DESIGN BY : Phebe Su	DATE : 2018/08/11	PART NAME: RJ45 CONNECTORS,W/LED THRU,HOLE10/100MBPS,POE,FILTER	
	CHECKED BY: Vicky Hsieh	DATE : 2018/08/11	PART NO.	MRJF-11T1059TRS
	APPROVED BY1: Richard Hsieh	DATE : 2018/08/11	MOLD NO.	NA
	APPROVED BY2: Richard Hsieh	DATE : 2018/08/11	DRAW NO.	
SCALE:1:1	SIZE:A4		SHEET NO.	3 OF 3