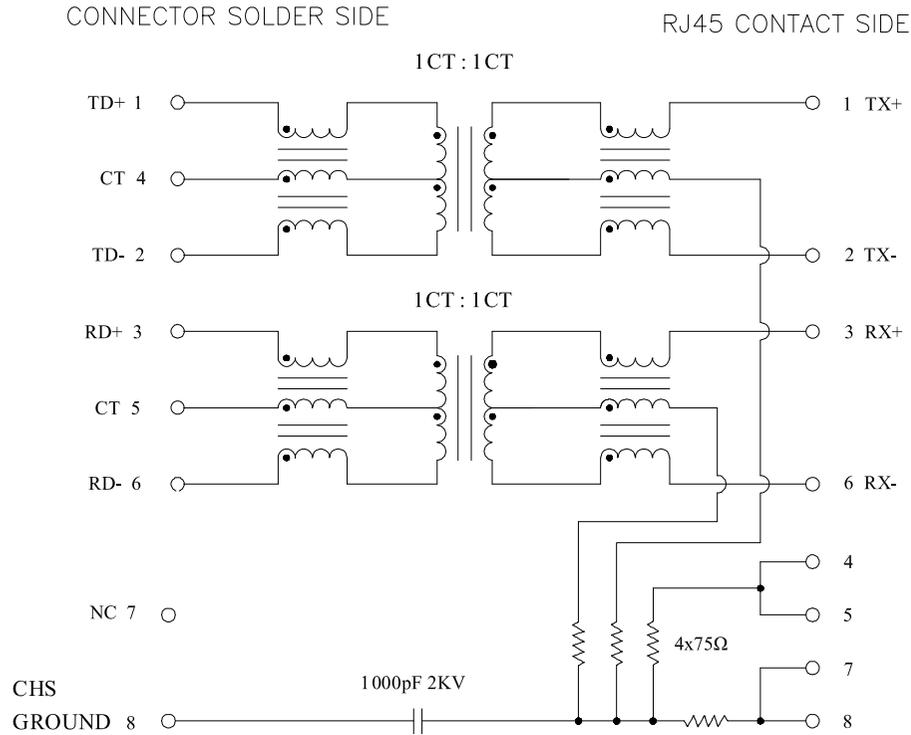




# GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
AO			Initial	2018/09/25	Phebe Su

## 2. SCHEMATIC:



## ORDER INFORMATION :

M R J F - 1 1 T 932 R S  
 Matrix-RJ45  
 F : 10/100  
 G : 10/100/1000  
 N : Pure Connector  
 RS : ROHS  
 HF : Halogen Free  
 Series Number  
 S : SMD  
 T : THT  
 11: 1X1 ; 21: 2X1  
 12: 1X2 ; 22: 2X2  
 14: 1X4 ; 24: 2X4  
 16: 1X6 ; 26: 2X6  
 18: 1X8 ; 28: 2X8  
 1U: RJ+USB

## 3. ELECTRICAL CHARACTERISTICS :

- TR: (100KHz, 0.1V); 100%  
PINS: (P1, P2): (J1, J2) = 1:1 ± 3%; (P3, P6): (J3, J6) = 1:1 ± 3%
- LX: (100KHz, 100mV, 8mA, DC Bias) 100%  
PINS: (P1, P2), (P3, P6) = 350uH MINIMUM
- DCR: 100%  
PINS: (J1, J2), (J3, J6) = 1.2 Ω MAXIMUM
- HIPOT: 100%  
PINS (P1, P2) TO (J1, J2), (P3, P6) TO (J3, J6) = 1500VAC FOR 60 SECONDS
- INSERTION LOSS:  
-1.0dB MAXIMUM AT 1MHz TO 100MHz;
- RETURN LOSS:  
-18dB MINIMUM AT 1MHz TO 30MHz;  
-12dB MINIMUM AT 60MHz TO 80MHz  
-10dB MINIMUM AT 80MHz TO 100MHz
- CROSS TALK:  
-30dB MINIMUM AT 1MHz TO 100MHz
- COMMON TO COMMON MODE REJECTION:  
-30dB MINIMUM AT 1MHz TO 100MHz  
-20dB MINIMUM AT 150MHz



Matrix Electronics Co., Ltd

<b>TOLERANCE:</b> X.X ±0.38 X.XX ±0.25 X.XXX ±0.13 ANGLE: ±3°	<b>DESIGN BY :</b> Phebe Su	<b>DATE :</b> 2018/09/25	<b>PART NAME:</b> RJ45.CONN.W/O_LED,8P8C HOLE10/100MBPS.FILTER
	<b>CHECKED BY:</b> Vicky Hsieh	<b>DATE :</b> 2018/09/25	<b>PART NO.</b> MRJF-11T932RS
 <b>UNIT: mm [inch]</b> <b>SCALE: 1:1 SIZE: A4</b>	<b>APPROVED BY1:</b> Richard Hsieh	<b>DATE :</b> 2018/09/25	<b>MOLD NO.</b> NA
	<b>APPROVED BY2:</b> Richard Hsieh	<b>DATE :</b> 2018/09/25	<b>DRAW NO.</b> <b>SHEET NO.</b> 2 OF 2