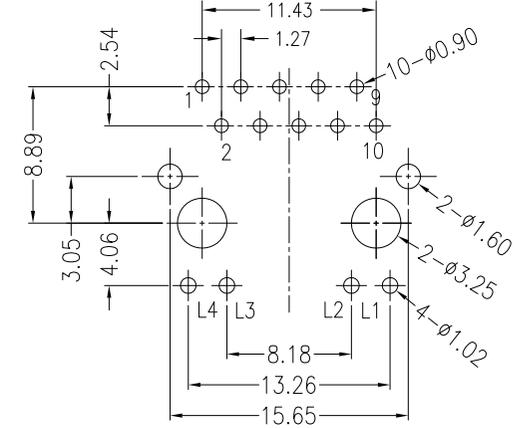
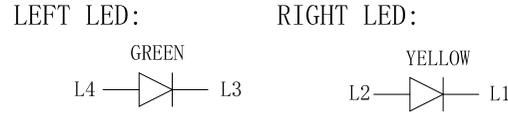
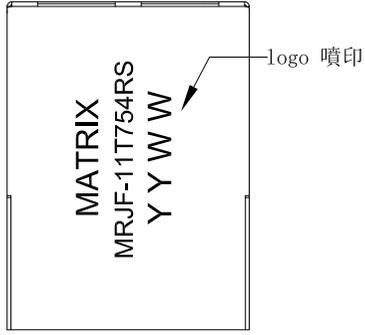


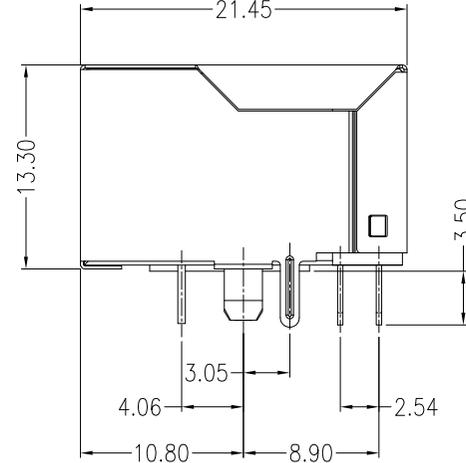
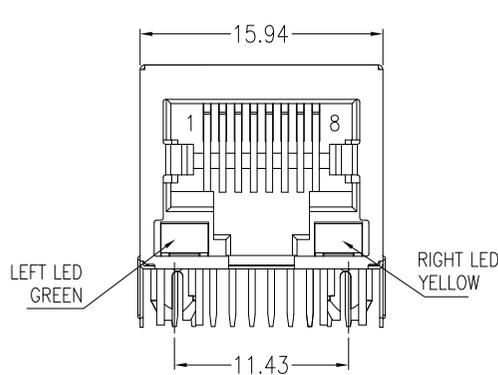
GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
AO			Initial	2017/10/31	Phebe Su

1. MECHANICAL DIMENSIONS :



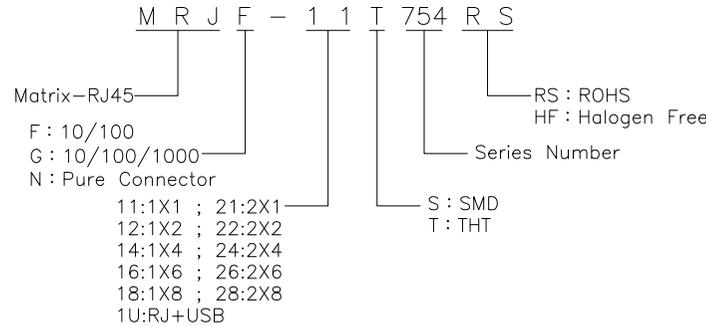
RECOMMEND PCB LAYOUT
(TOP VIEW)
DIMENSIONAL TOLERANCE: ±0.05



NOTES:

- MATERIALS:
HOUSING: PBT, UL94V-0, BLACK
CONTACTS: PHOSPHOR BRONZE T=0.30, G/F ON CONTACT AREA 80u" TIN ON SOLDER TAIL
SHELL: COPPER ALLOY T=0.20, NICKEL PLATING OVERALL
- OPERATING TEMPERATURE: 0°C ~ +70°C
- JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68, SUBPART F

ORDER INFORMATION :



NO	VARIETY	QTY	MATERIAL	REMARK
 Matrix Electronics Co.,Ltd				
TOLERANCE: X.X ±0.38 X.XX ±0.25 X.XXX ±0.13 ANGLE: ±3°		DESIGN BY : Phebe Su CHECKED BY: Vicky Hsieh	DATE : 2017/10/31 DATE : 2017/10/31 DATE : 2017/10/31	PART NAME: RJ45 CONNECTORS,W/LED,10P,8C, POE 10/100MBPS,FILTER PART NO. MRJF-11T754RS
 UNIT: mm [inch]		APPROVED BY1: Richard Hsieh APPROVED BY2: Richard Hsieh	DATE : 2017/10/31 DATE : 2017/10/31	MOLD NO. NA DRAW NO. SHEET NO. 1 OF 3
SCALE:1:1		SIZE:A4		

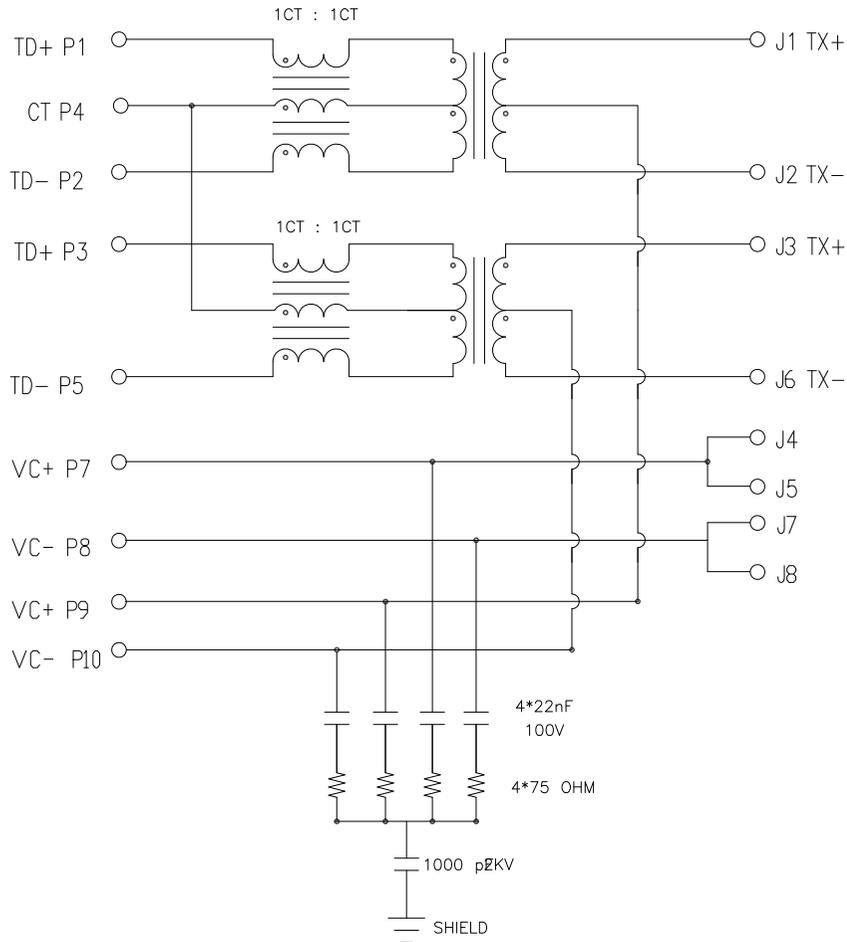
GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
AO			Initial	2017/10/31	Phebe Su

2. SCHEMATIC:

CONNECTOR SOLDER SIDE

RJ45 CONTACT SIDE



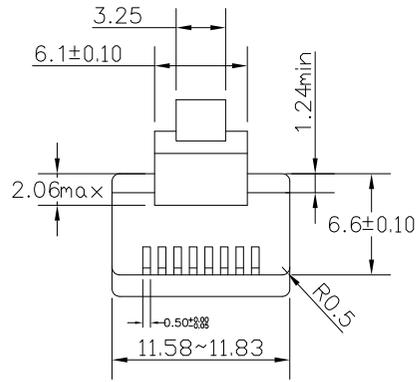
3. ELECTRICAL CHARACTERISTICS(25±5°C) :

- TR:(100KHz,0.1V); 100%
PINS:(P1,P2):(J1,J2)=1:1±3%;(P3,P5):(J3,J6)=1:1±3%
- LX:(100KHz,100mV,8mA, DC Bias) 100%
PINS:(P1,P2),(P3,P5)=350uH MINIMUM
- DCR: 100%
PINS:(J1,J2),(J3,J6)=1.2 Ω MAXIMUM
- HIPOT: 100%
PINS(P1,P2)TO(J1,J2),(P3,P5)TO(J3,J6)=1500VAC FOR 60 SECONDS
- INSERTION LOSS:
-1.0dB MAXIMUM AT 1MHz TO 100MHz;
- RETURN LOSS:
-18dB MINIMUM AT 1MHz TO 30MHz;
-16dB MINIMUM AT 30MHz TO 60MHz
-12dB MINIMUM AT 60MHz TO 80MHz
- CROSS TALK:
-30dB MINIMUM AT 1MHz TO 100MHz
- COMMON TO COMMON MODE REJECTION:
-30dB MINIMUM AT 1MHz TO 100MHz
- BALANCED DC LINE CURRENT
350mA MAX@57VDC
500mA MAX@57VDC FOR 200 MILLISECONDS

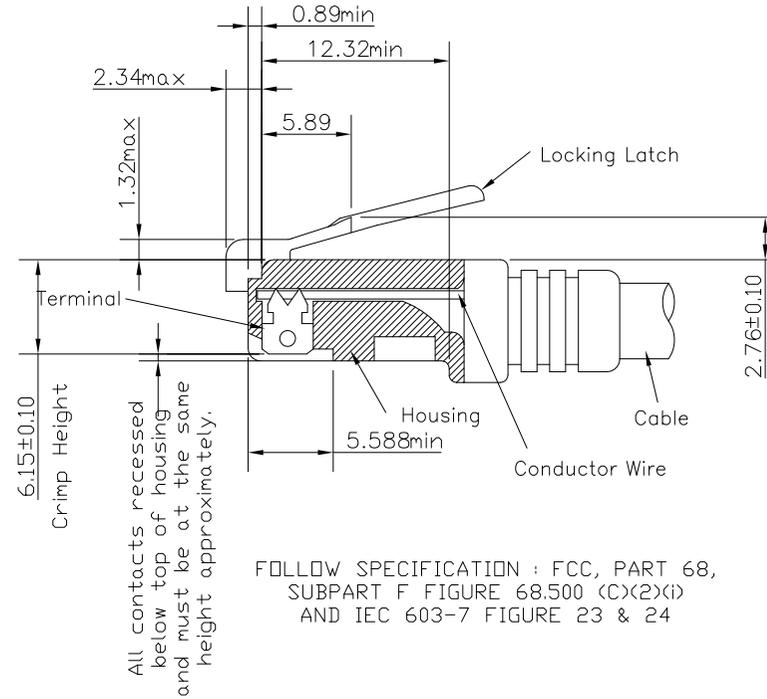
NO	VARIETY	QTY	MATERIAL	REMARK
 Matrix Electronics Co.,Ltd				
TOLERANCE: X.X ±0.38 X.XX ±0.25 X.XXX ±0.13 ANGLE: ±3°		DESIGN BY : Phebe Su	DATE : 2017/10/31	PART NAME: RJ45,CONNECTORS,W/LED,10P,8C, POE 10/100MBPS,FILTER
 UNIT: mm [inch]		CHECKED BY: Vicky Hsieh	DATE : 2017/10/31	PART NO. MRJF-11T754RS
SCALE:1:1		APPROVED BY1: Richard Hsieh	DATE : 2017/10/31	MOLD NO. NA
SIZE:A4		APPROVED BY2: Richard Hsieh	DATE : 2017/10/31	DRAW NO. SHEET NO. 2 OF 3

GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2017/10/31	Phebe Su

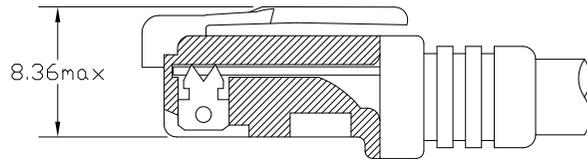


- * There must be no damage to housing or locking latch. There must be no nicks or cuts in cable.
- * Durability : 750 cycles generally



FOLLOW SPECIFICATION : FCC, PART 68,
SUBPART F FIGURE 68.500 (C)(2)(i)
AND IEC 603-7 FIGURE 23 & 24

STANDARD MODULAR PLUG ASSEMBLY



FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F
FIGURE 68.500 (C)(2)(ii)

NO	VARIETY	QTY	MATERIAL	REMARK
 Matrix Electronics Co.,Ltd				
TOLERANCE: X.X ±0.38 X.XX ±0.25 X.XXX ±0.13 ANGLE: ±3°		DESIGN BY : Phebe Su	DATE : 2017/10/31	PART NAME: RJ45,CONNECTORS,W/LED,10P,8C, POE 10/100MBPS,FILTER
 UNIT: mm [inch]		CHECKED BY: Vicky Hsieh	DATE : 2017/10/31	PART NO. MRJF-11T754RS
SCALE:1:1 SIZE:A4		APPROVED BY1: Richard Hsieh	DATE : 2017/10/31	MOLD NO. NA
		APPROVED BY2: Richard Hsieh	DATE : 2017/10/31	DRAW NO. SHEET NO. 3 OF 3