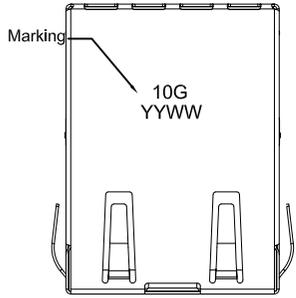


# GP Component

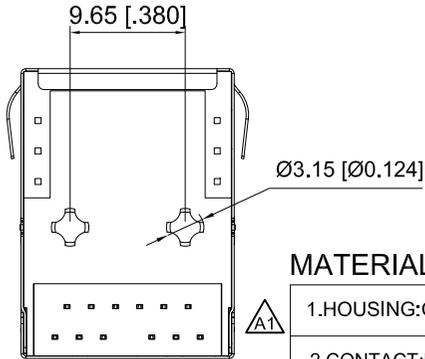
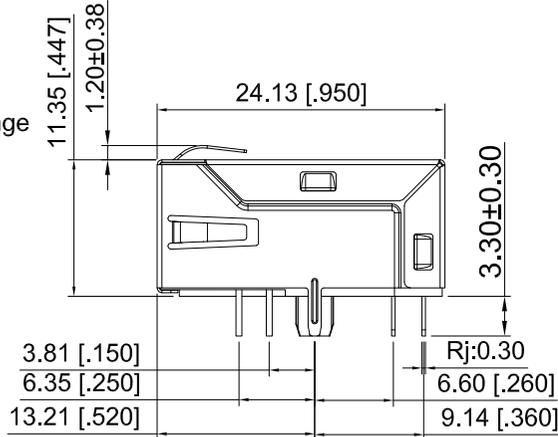
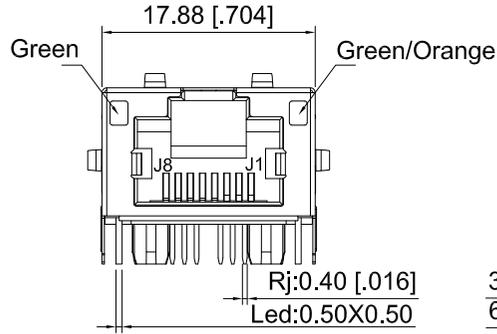
## 1. MECHANICAL DIMENSIONS :



GoldPlating	Option
1.8μ"	<input type="checkbox"/>
3μ"	<input type="checkbox"/>
6μ"	<input type="checkbox"/>
15μ"	<input type="checkbox"/>
30μ"	<input checked="" type="checkbox"/>
50μ"	<input type="checkbox"/>

Printing:

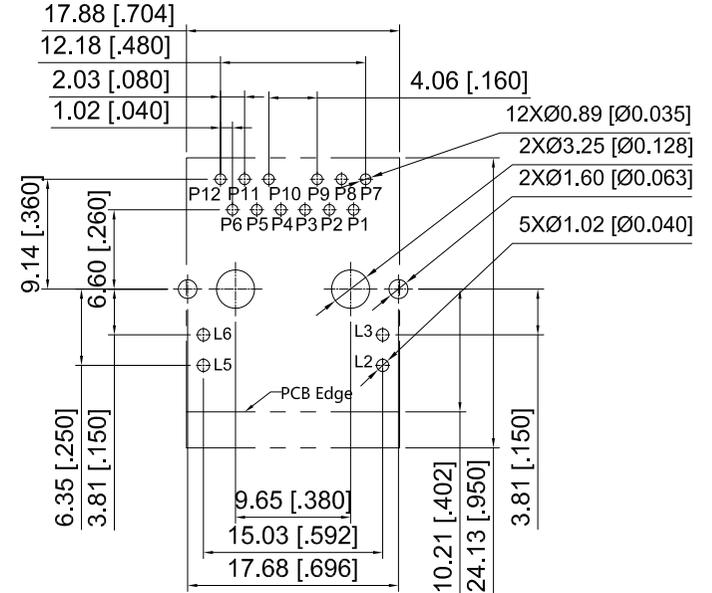
10G  
YYWW



## MATERIALS

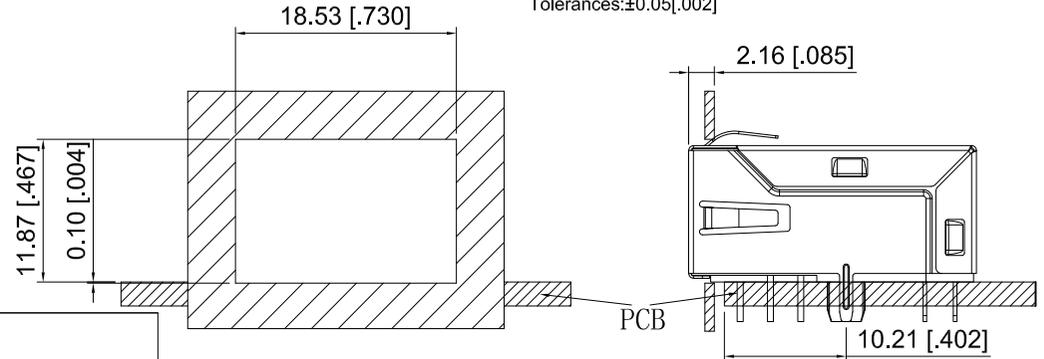
- HOUSING:GLASS FILLED LCP(BLACK) UL94V-0(ROHS)
- CONTACT:PHOSPHOR BRONZE.50 MICROINCHES MIN. OVERALL DUCTILE NICKEL UNDERPLATEWITH SELECTED 30 MICROINCHES GOLD AT MATING INTERFACE
- SHIELD:0.2mm THICKNESS WITH BRASS.
- JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68 SUBPART F
- THE PART IS RECOMMENDED FOR WAVE SOLDERING PROCESS PEAK SOLDERING TEMPERATURE IS 265±5°C ,10 SECS.
- OPERATING TEMPERATURE:-40°C TO +85°C.  
STORAGE TEMPERATURE:-40°C TO +85°C.

## 2. PCB LAYOUT :



RECOMMENDED P.C.B LAYOUT TOP VIEW  
(COMPONENT SIDE)

Dimensions Tolerance:All PCB LAYOUT  
Tolerances:±0.05[.002]

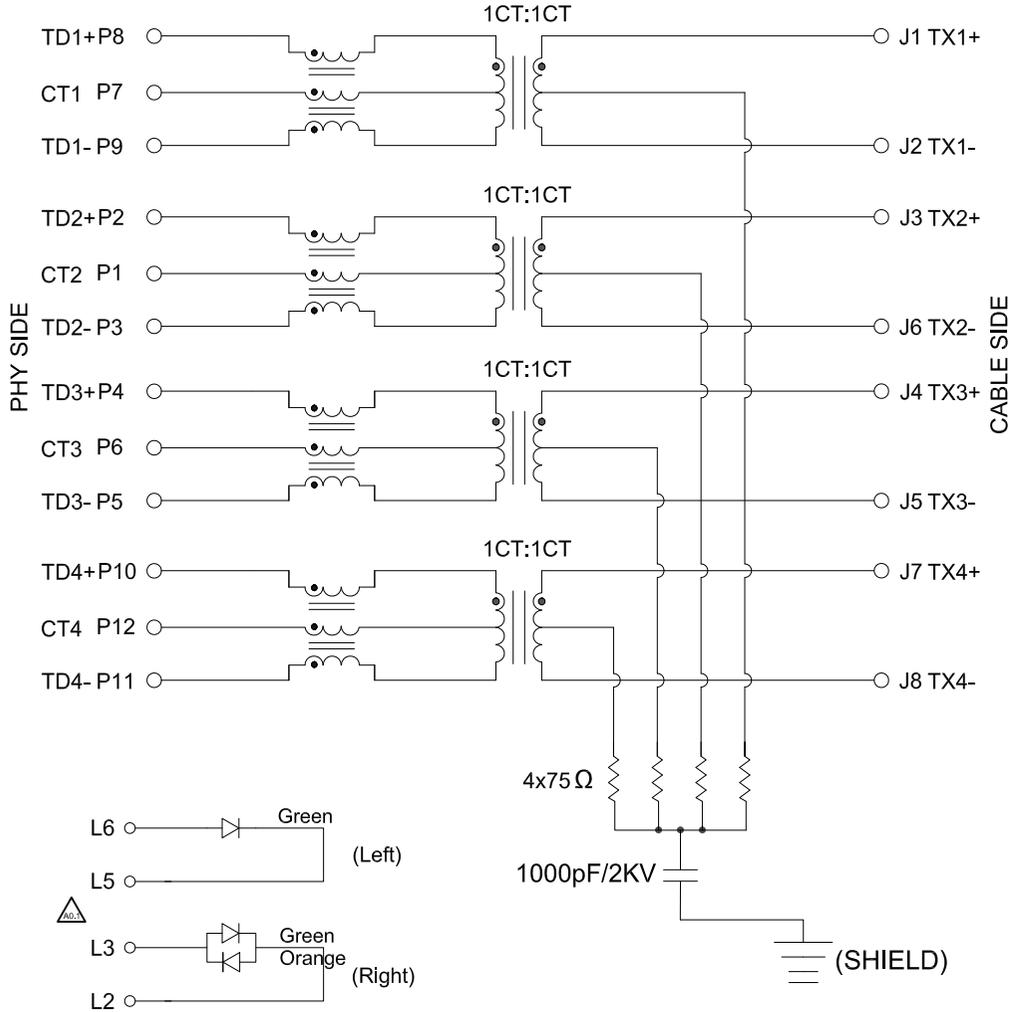


Matrix Electronics Co.,Ltd

TOLERANCE: X X ±0.38 X XX ±0.25 X XXX ±0.13 ANGLE:±3°	DESIGN BY : Vince Chen	DATE : 2024/02/19	PART NAME: RJ45 CONN W/ LED, 8P12C 10G FILTER	
 UNIT: mm [inch] SCALE:1:1 SIZE:A4	CHECKED BY: Hanson Huang	DATE : 2024/02/19	PART NO.	MRJ10G-11T1767TRS
	APPROVED BY1: Richard Hsieh	DATE : 2024/02/19	MOLD NO.	NA
	APPROVED BY2: Richard Hsieh	DATE : 2024/02/19	DRAW NO.	
			SHEET NO.	1 OF 3

# GP Component

## 3. SCHEMATIC:



MATRIX PART NO:  
**M R J 10G - 1 1 T 1767 T R S**  
 Matrix-RJ45  
 F : 10/100  
 G : 10/100/1000  
 10G: 10G  
 11:1X1 ; 21:2X1  
 12:1X2 ; 22:2X2  
 14:1X4 ; 24:2X4  
 16:1X6 ; 26:2X6  
 18:1X8 ; 28:2X8  
 1U:RJ+USB  
 RS: ROHS  
 HF: Halogen Free  
 Industrial Temperature  
 Series Number  
 S: SMD  
 T: THT

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2022/12/28	Vince
A0.1			Modify Led Mark	2023/12/29	Vince
A1			Modify Materials	2024/02/19	Vince

## 3. ELECTRICAL CHARACTERISTICS :

PARAMETER	SPECIFICATIONS
OPERATING TEMPERATURE	-40°C To +85°C
TURNS RATIO	1:1±2%
INDUCTANCE(OCL)	120uH MIN@100KHz/100mV With 8mA DC Bias For(Channel1/2/3/4)
INSERTION LOSS	-3.0dBMAX@1-500MHz;
RETURN LOSS	-22dBMIN@1-100MHz; -22+20.75xLOG(f/100)dBMIN@100-500MHz;
CROSSTALK (ADJACENT CHANNELS)	-28dBMIN@1-100MHz; -17dBMIN@100-500MHz;
COMMON MODE REJECTION RATIO	-30dBMIN@1-100MHz; -20dBMIN@100-500MHz;
COMMON MODE TO DIFFERENTIAL MODE ATTENUATION	-30dBMIN@1-250MHz; -20dBMIN@250-500MHz;
HI-POT	2250VDC@60 SECONDS(Channel1/2/3/4)

LED SPECIFICATION			
STANDARD LED	WAVELENGTH	Forward V(max)	TYP
GREEN	570nm	2.4V	2.2V
ORANGE	605nm	2.5V	2.1V

\*WITH A FORWARD CURRENT OF 20mA

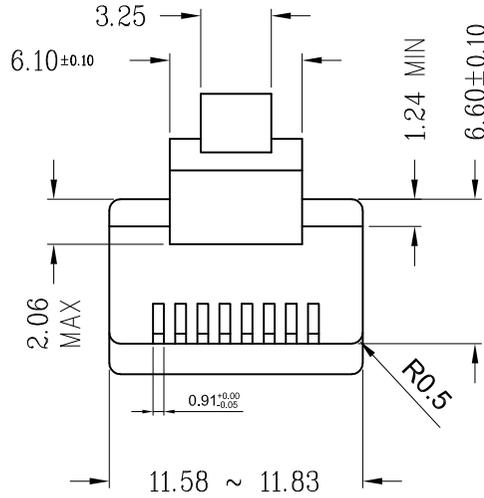


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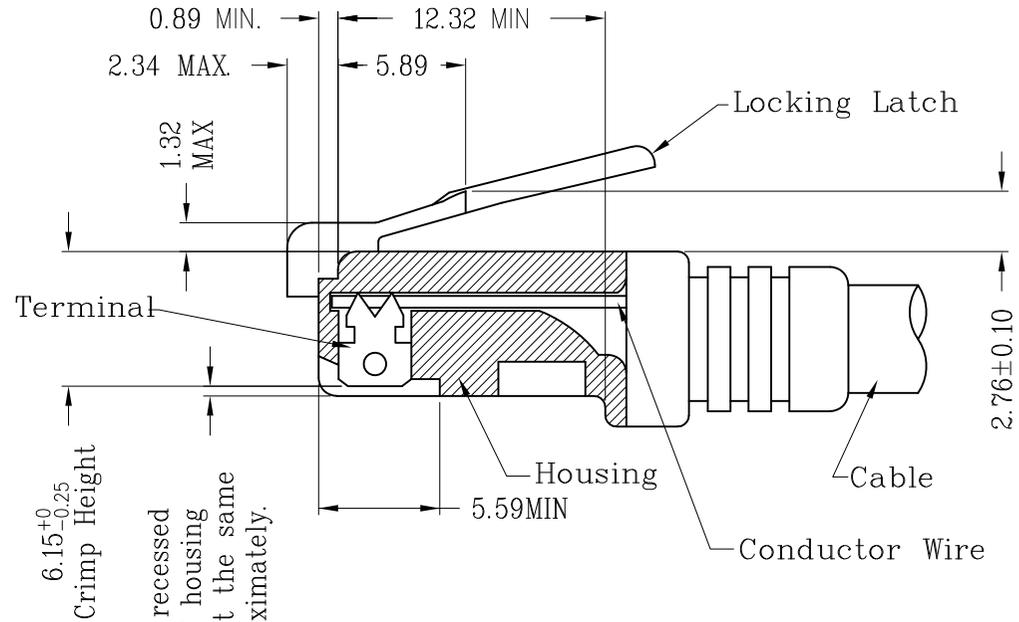
<b>TOLERANCE:</b> X.X ±0.38 X.XX ±0.25 X.XXX ±0.13 ANGLE: ±3°	<b>DESIGN BY :</b> Vince Chen	<b>DATE :</b> 2024/02/19	<b>PART NAME:</b> RJ45 CONN W/ LED, 8P12C 10G FILTER		
	<b>CHECKED BY:</b> Hanson Huang	<b>DATE :</b> 2024/02/19	<b>PART NO.</b> MRJ10G-11T1767TRS		
 <b>UNIT: mm [inch]</b> <b>SCALE:1:1 SIZE:A4</b>	<b>APPROVED BY1:</b> Richard Hsieh	<b>DATE :</b> 2024/02/19	<b>MOLD NO.</b> NA		
	<b>APPROVED BY2:</b> Richard Hsieh	<b>DATE :</b> 2024/02/19	<b>DRAW NO.</b> <b>SHEET NO.</b>	<b>1 OF 3</b>	

# GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2022/12/28	Vince
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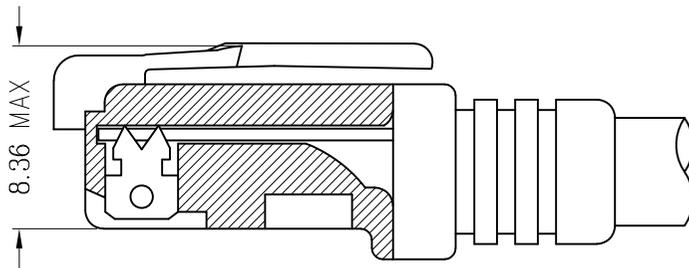
- \* There must be no damage to housing or locking latch. There must be no nicks or cuts in cable.
- \* Durability : 750 cycles generally



All contacts recessed below top of housing and must be at the same height approximately.

FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F FIGURE 68.500 (C)(2)(i) AND IEC 603-7 FIGURE 23 & 24

## STANDARD MODULAR PLUG ASSEMBLY



FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F FIGURE 68.500 (C)(2)(ii)

 <b>Matrix Electronics Co.,Ltd</b>				
<b>TOLERANCE:</b> X X ±0.38 X XX ±0.25 X XXX ±0.13 ANGLE: ±3°	<b>DESIGN BY :</b> Vince Chen	<b>DATE :</b> 2024/02/19	<b>PART NAME:</b> RJ45 CONN W/ LED, 8P12C 10G FILTER	
	<b>CHECKED BY:</b> Hanson Huang	<b>DATE :</b> 2024/02/19	<b>PART NO.</b>	MRJ10G-11T1767TRS
  <b>UNIT: mm [inch]</b> <b>SCALE:1:1 SIZE:A4</b>	<b>APPROVED BY1:</b> Richard Hsieh	<b>DATE :</b> 2024/02/19	<b>MOLD NO.</b>	NA
	<b>APPROVED BY2:</b> Richard Hsieh	<b>DATE :</b> 2024/02/19	<b>DRAW NO.</b>	
			<b>SHEET NO.</b>	1 OF 3