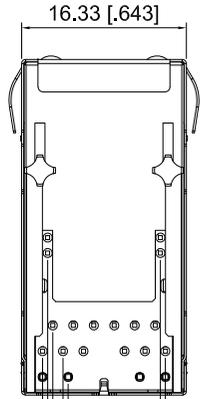
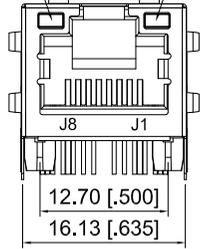


GP Component

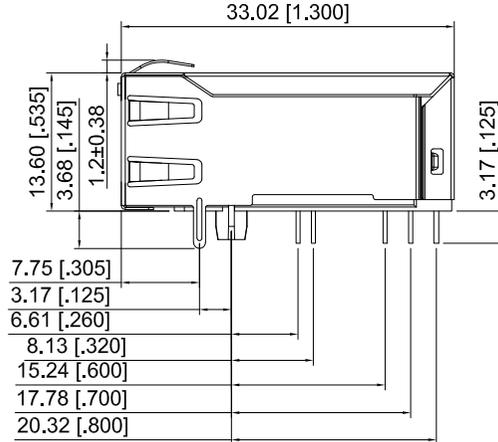
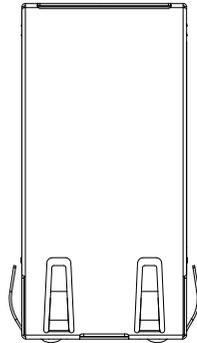
REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2022/07/17	Vince

1.MECHANICAL

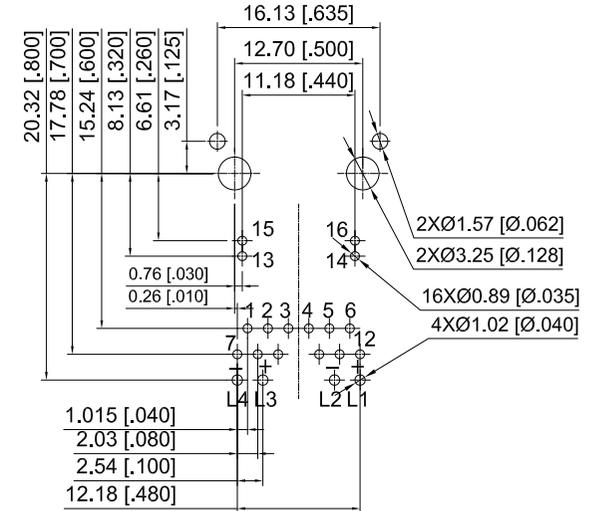
Green/Orange Orange/Green



11.18 [.440]
1.015 [.040]
2.03 [.080]
2.54 [.100]
12.18 [.480]

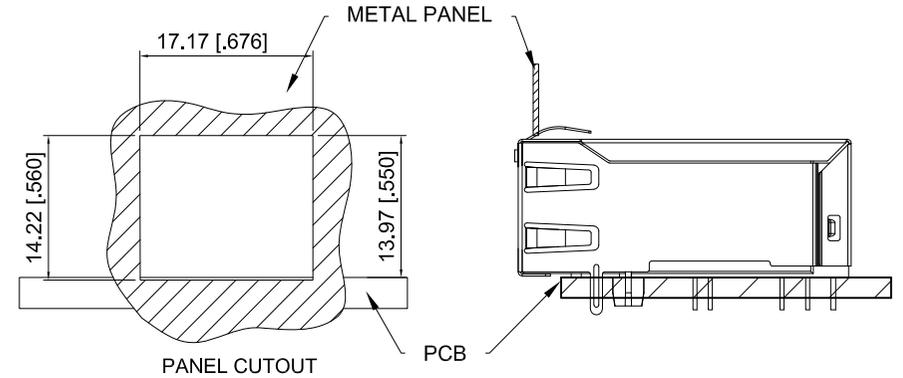


2.PCB LAYOUT



RECOMMENDED P.C.B LAYOUT
TOP VIEW (COMPONENT SIDE)

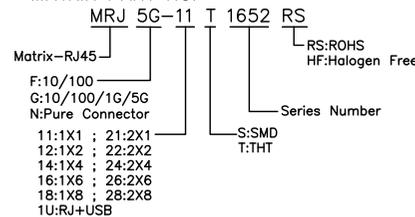
Dimension Tolerance:
All PCB LAYOUT
Tolerance:±0.05[.002]



MATERIALS

1.HOUSING:GLASS FILLED PA46(BLACK) UL94V-0(ROHS)
2.CONTACT: PHOSPHOR BRONZE.50 MICROINCHES MIN. OVERALL DUCTILE NICKEL UNDERPLATE WITH SELECTED 30 MICROINCHES GOLD AT MATING INTERFACE
3.SHIELD:0.2mm THICKNESS WITH BRASS.
4.JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68 SUBPART F
5.THE PART IS RECOMMENDED FOR WAVE SOLDERING PROCESS PEAK SOLDERING TEMPERATURE IS 260°C MAX,10 SECS MAX.
6.OPERATING TEMPERATURE:0°C TO +70°C. STORAGE TEMPERATURE:-40°C TO +85°C.

MATRIX PART NO:



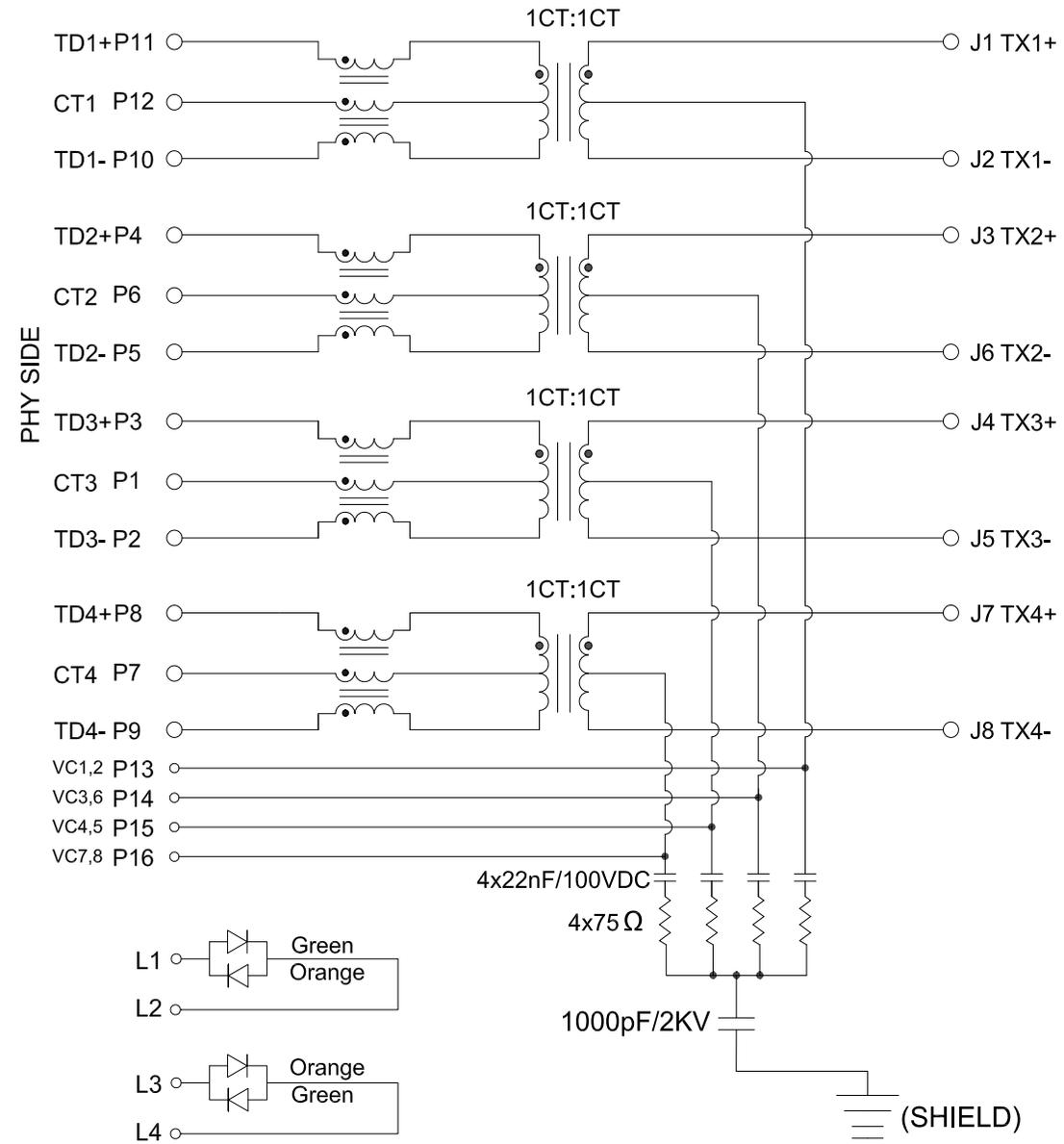
Matrix Electronics Co.,Ltd

TOLERANCE:	DESIGN BY :	DATE :	PART NAME:	
X X X ±0.38 X XX ±0.25 X XXX ±0.13 ANGLE:±3°	Vince Chen	2022/07/17	RJ45 CONN W/ LED, 5G BASE	
 UNIT: mm [inch] SCALE:1:1 SIZE:A4	CHECKED BY:	DATE :	PART NO.	MRJ5G-11T1652RS
	Hanson Huang	2022/07/17		
 ANGLE:±3°	APPROVED BY1:	DATE :	MOLD NO.	NA
	Richard Hsieh	2022/07/17		
	APPROVED BY2:	DATE :	DRAW NO.	
	Richard Hsieh	2022/07/17	SHEET NO.	1 OF 3

GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2022/07/17	Vince

3. SCHEMATIC:



4. Electrical Specification @25°C:

PARAMETER	SPECIFICATIONS
OPERATING TEMPERATURE	0°C To +70°C
TURNS RATIO	1:1±2%
OPEN CIRCUIT INDUCTANCE(OCL)	120uH MIN@100KHz/100mV With 19mA DC Bias For(CHANNEL1,2,3,4) 180uH MIN@100KHz/100mV With 8mA DC Bias For(CHANNEL1,2,3,4)
INSERTION LOSS(IL)	-0.5dB MAX@1MHz-50MHz; -1.0dB MAX@50MHz-125MHz; -2.0dB MAX@125MHz-200MHz; -2.5dB MAX@200MHz-250MHz;
RETURN LOSS(RL) (Z out=100 OHM)	-20dB MIN@1MHz-50MHz; -20+15LOG(f/40MHz)dB MIN@50MHz-250MHz;
CROSSTALK (ADJACENT CHANNELS)	-25dB MIN@1MHz-125MHz; -20dB MIN@125MHz-250MHz;
COMMON MODE REJECTION RATIO(CMRR)	-23dB MIN@1MHz-250MHz;
COMMON TO DIFFERENTIAL MODE REJECTION(REF)	-23dB MIN@1MHz-250MHz;
DC CURRENT/VOLTAGE RATING-PSE PINS	720mA MAXIMUM@57VDC(CONTINUOUS)
HI-POT	2250 VDC@60 SECONDS

LED SPECIFICATION			
STANDARD LED	WAVELENGTH	Forward V(max)	TYP
GREEN	570nm	2.4V	2.2V
ORANGE	605nm	2.5V	2.1V

*WITH A FORWARD CURRENT OF 20mA

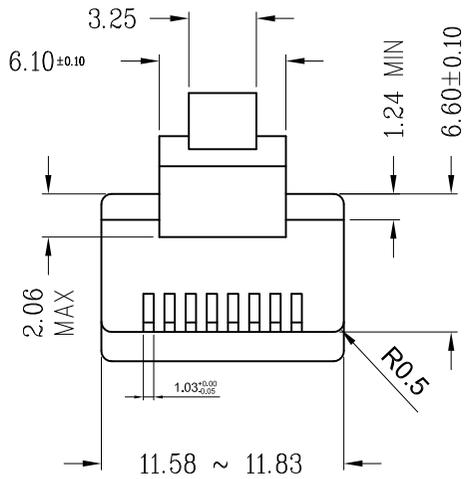
Printing:

MATRIX
MRJ5G-11T1652RS
YYWW

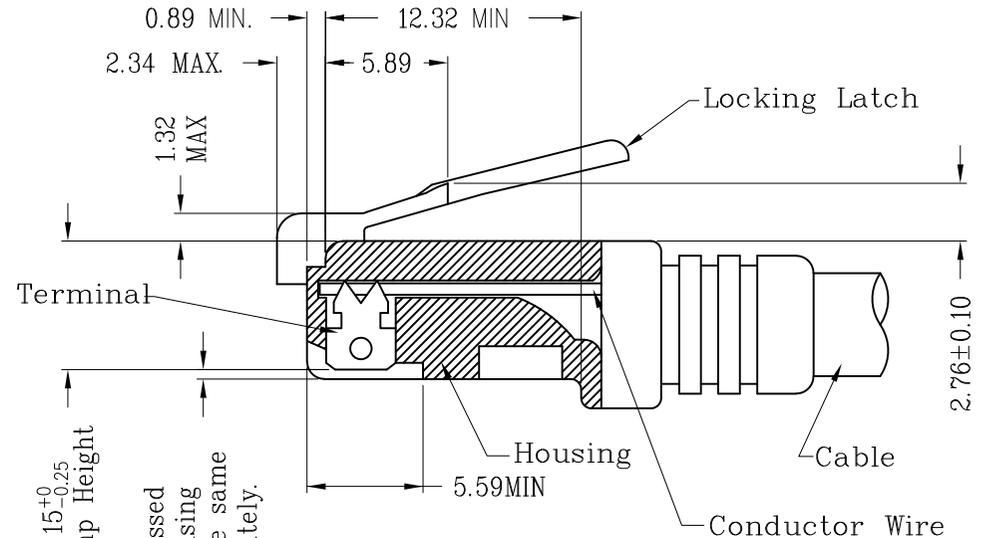
Matrix Electronics Co.,Ltd				
TOLERANCE: X X X ±0.38 X XX ±0.25 X XXX ±0.13 ANGLE: ±3°	DESIGN BY : Vince Chen	DATE : 2022/07/17	PART NAME: RJ45 CONN W/ LED, 5G BASE	
 UNIT: mm [inch] SCALE:1:1 SIZE:A4	CHECKED BY: Hanson Huang	DATE : 2022/07/17	PART NO.	MRJ5G-11T1652RS
	APPROVED BY1: Richard Hsieh	DATE : 2022/07/17	MOLD NO.	NA
	APPROVED BY2: Richard Hsieh	DATE : 2022/07/17	DRAW NO.	
			SHEET NO.	2 OF 3

GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2022/07/17	Vince



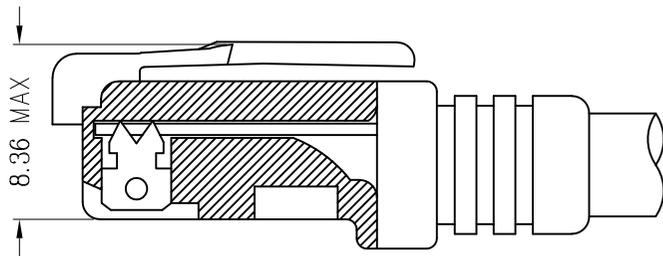
- * There must be no damage to housing or locking latch. There must be no nicks or cuts in cable.
- * Durability : 750 cycles generally



6.15⁺⁰_{-0.25}
Crimp Height

All contacts recessed below top of housing and must be at the same height approximately.

FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F FIGURE 68.500 (C)(2)(i) AND IEC 603-7 FIGURE 23 & 24



FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F FIGURE 68.500 (C)(2)(ii)

STANDARD MODULAR PLUG ASSEMBLY

 Matrix Electronics Co.,Ltd				
TOLERANCE: X ±0.38 X X ±0.25 X XX ±0.13 X XXX ±0.13 ANGLE: ±3°		DESIGN BY : Vince Chen	DATE : 2022/07/17	PART NAME: RJ45 CONN W/ LED, 5G BASE
 UNIT: mm [inch]		CHECKED BY: Hanson Huang	DATE : 2022/07/17	PART NO. MRJ5G-11T1652RS
SCALE:1:1 SIZE:A4		APPROVED BY1: Richard Hsieh	DATE : 2022/07/17	MOLD NO. NA
		APPROVED BY2: Richard Hsieh	DATE : 2022/07/17	DRAW NO. SHEET NO. 3 OF 3