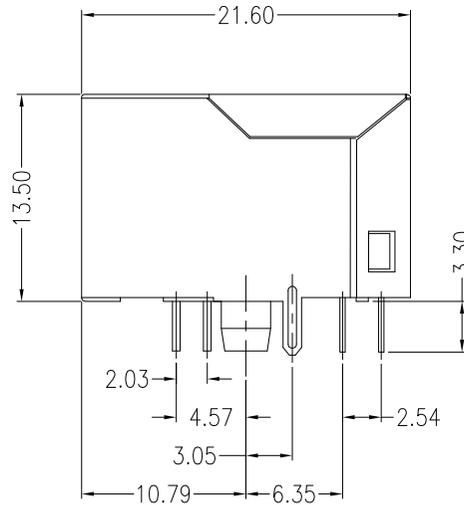
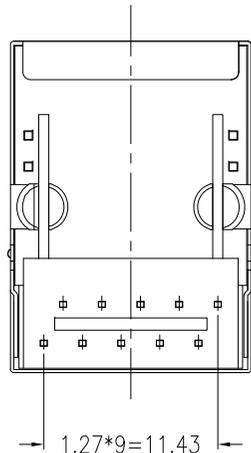
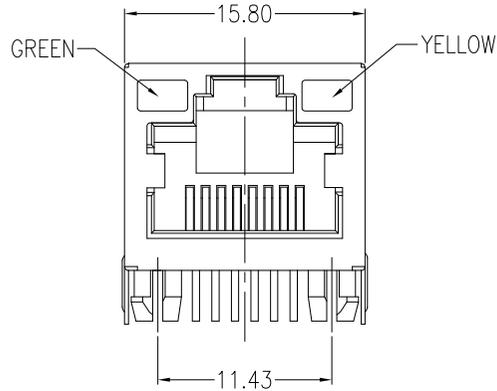
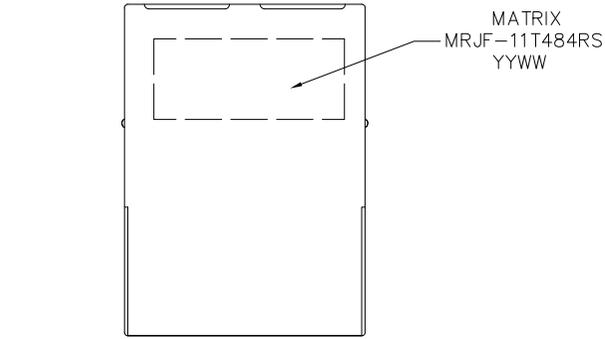


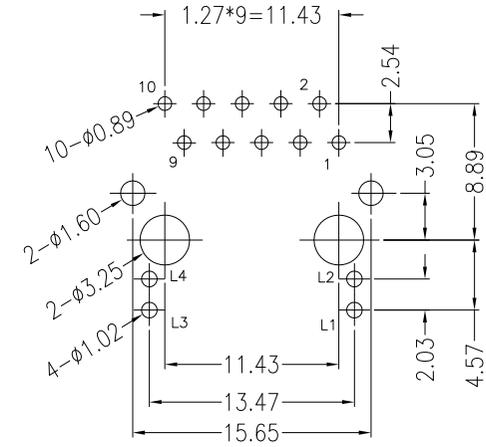
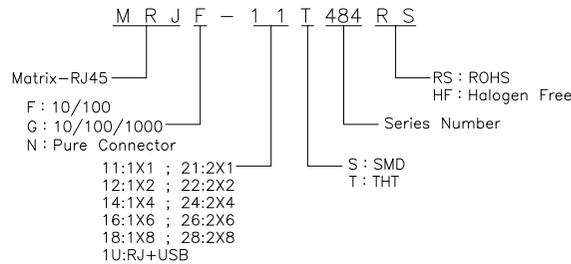
# GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2015/04/02	Phebe Su
A1			Update	2016/05/06	Phebe Su

## 1. MECHANICAL DIMENSIONS :



### ORDER INFORMATION :



RECOMMEND PCB LAYOUT  
(TOP VIEW)

DIMENSIONAL TOLERANCE:  $\pm 0.05$

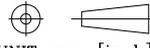


### NOTES:

#### 1. MATERIALS:

- HOUSING: PA46 UL94V-0 BLACK
- CONTACTS: T=0.35 PHOSPHOR BRONZE, 50u" NICKEL MIN UNDERPLATE ON ALL AREA, 100u" TIN ON TERMINATION END, GOLD 6u" ON CONTACT AREA.
- SHIELD: BRASS C2680-H T=0.20mm $\pm 0.01$  FOGGED (50% SHINE SOLVENT) NICKEL 30~50u" PLATING.

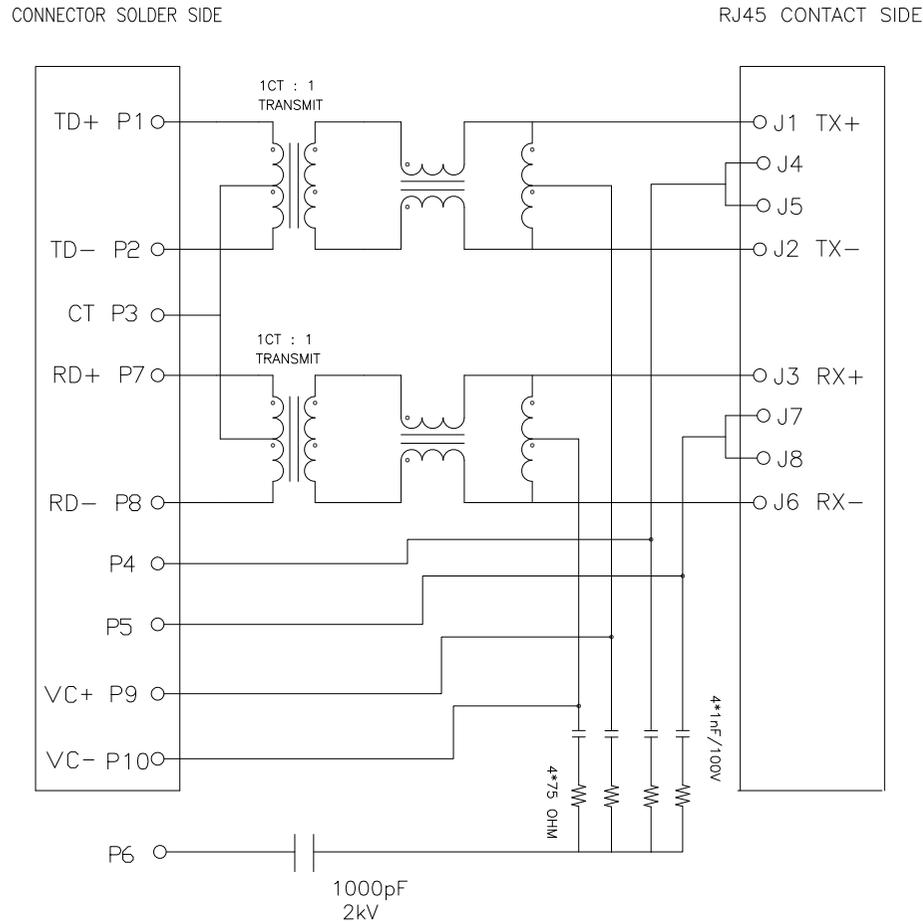
#### 2. JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68, SUBPART F

NO	VARIETY	QTY	MATERIAL	REMARK
 <b>Matrix Electronics Co.,Ltd</b>				
<b>TOLERANCE:</b> X.X: $\pm 0.38$ X.XX: $\pm 0.25$ X.XXX: $\pm 0.13$ ANGLE: $\pm 3^\circ$		<b>DESIGN BY :</b> Phebe Su  <b>CHECKED BY:</b> Vicky Hsieh	<b>DATE :</b> 2016/05/06  <b>DATE :</b> 2016/05/06	<b>PART NAME:</b> RJ45 CONNECTORS, W/LED, 10P, 8C SHIELDED/THRU, HOLE 10/100, FILTER  <b>PART NO.</b> MRJF-11T484RS
 <b>UNIT: mm [inch]</b> <b>SCALE: 1:1</b> <b>SIZE: A4</b>		<b>APPROVED BY1:</b> Richard Hsieh  <b>APPROVED BY2:</b> Richard Hsieh	<b>DATE :</b> 2016/05/06  <b>DATE :</b> 2016/05/06	<b>MOLD NO.</b> NA  <b>DRAW NO.</b>  <b>SHEET NO.</b> 1 OF 3

# GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
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A1			Update	2016/05/06	Phebe Su

## 2. SCHEMATIC:



## 3. ELECTRICAL CHARACTERISTICS :

TEST NOTES:(25°±5°C)

1. TR:(100KHz,0.1V); 100%

PINS:(P1,P2):(J1,J2)=1:1±3%:(P7,P8):(J3,J6)=1:1±3%

2. LX:(100KHz,100mV,18mA, DC Bias) 100%

PINS:(P1,P2),(P7,P8)=350uH MINIMUM

3. DCR: 100%

PINS:(J1,J2),(J3,J6)=1.2Ω MAXIMUM

4. HIPOT: 100%

PINS(P1,P2)TO(J1,J2),(P7,P8)TO(J3,J6)=1500VAC FOR 2 SECONDS

5. INSERTION LOSS:

-1.0dB MAXIMUM AT 1MHz TO 100MHz;

6.RETURN LOSS:

-20dB MINIMUM AT 1MHz TO 30MHz;

-15dB MINIMUM AT 30MHz TO 60MHz

-10dB MINIMUM AT 60MHz TO 80MHz

7.CROSS TALK:

-30dB MINIMUM AT 1MHz TO 100MHz

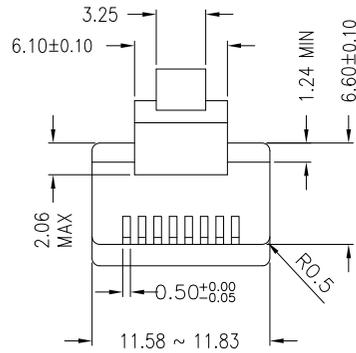
8.COMMON TO COMMON MODE REJECTION:

-30dB MINIMUM AT 1MHz TO 100MHz

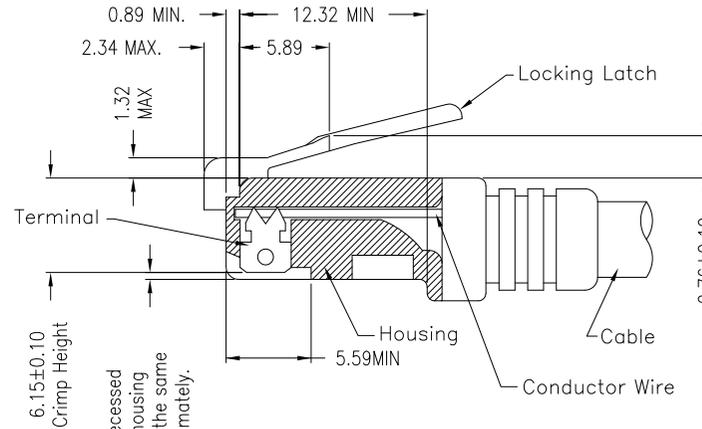
NO	VARIETY	QTY	METERIAL	REMARK
 <b>Matrix Electronics Co.,Ltd</b>				
<b>TOLERANCE:</b> X.X ±0.38 X.XX ±0.25 X.XXX ±0.13 ANGLE: ±3°		<b>DESIGN BY :</b> Phebe Su	<b>DATE :</b> 2016/05/06	<b>PART NAME:</b> RJ45,CONNECTORS,W/LED,10P,8C SHIELDED/THRU,HOLE10/100,FILTER
 <b>UNIT: mm [inch]</b>		<b>CHECKED BY:</b> Vicky Hsieh	<b>DATE :</b> 2016/05/06	<b>PART NO.</b> MRJF-11T484RS
 <b>SCALE:1:1 SIZE:A4</b>		<b>APPROVED BY1:</b> Richard Hsieh	<b>DATE :</b> 2016/05/06	<b>MOLD NO.</b> NA
		<b>APPROVED BY2:</b> Richard Hsieh	<b>DATE :</b> 2016/05/06	<b>DRAW NO.</b> <b>SHEET NO.</b> 2 OF 3

# GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2015/04/02	Phebe Su
A1			Update	2016/05/06	Phebe Su

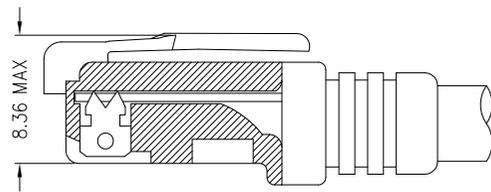


\* There must be no damage to housing or locking latch. There must be no nicks or cuts in cable.  
 \* Durability : 750 cycles generally



FOLLOW SPECIFICATION : FCC, PART 68,  
 SUBPART F FIGURE 68.500 (C)(2)(i)  
 AND IEC 603-7 FIGURE 23 & 24

STANDARD MODULAR PLUG ASSEMBLY



FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F  
 FIGURE 68.500 (C)(2)(ii)

NO	VARIETY	QTY	MATERIAL	REMARK
 <b>Matrix Electronics Co.,Ltd</b>				
<b>TOLERANCE:</b> X.X ±0.38 X.XX ±0.25 X.XXX ±0.13 ANGLE: ±3°		<b>DESIGN BY :</b> Phebe Su	<b>DATE :</b> 2016/05/06	<b>PART NAME:</b> RJ45,CONNECTORS,W/LED,10P,8C SHIELDED/THRU,HOLE10/100,FILTER
 UNIT: mm [inch]		<b>CHECKED BY:</b> Vicky Hsieh	<b>DATE :</b> 2016/05/06	<b>PART NO.</b> MRJF-11T484RS
SCALE:1:1 SIZE:A4		<b>APPROVED BY1:</b> Richard Hsieh	<b>DATE :</b> 2016/05/06	<b>MOLD NO.</b> NA
		<b>APPROVED BY2:</b> Richard Hsieh	<b>DATE :</b> 2016/05/06	<b>DRAW NO.</b> <b>SHEET NO.</b> 3 OF 3