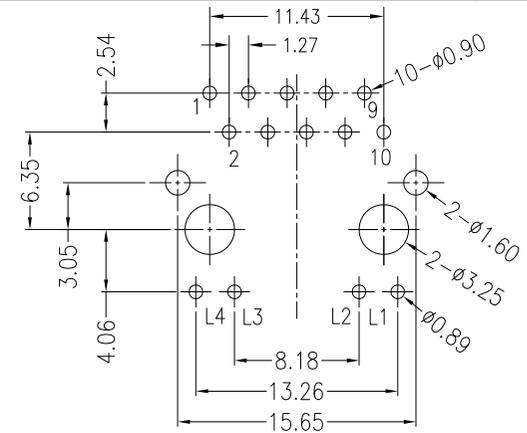
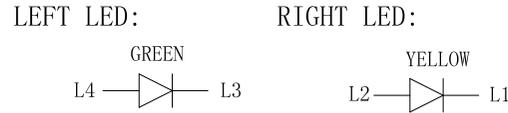
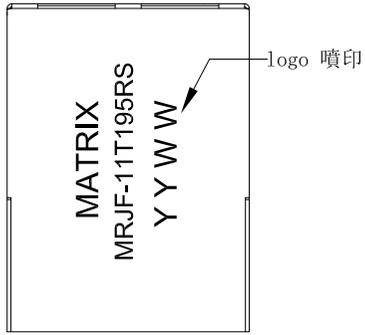


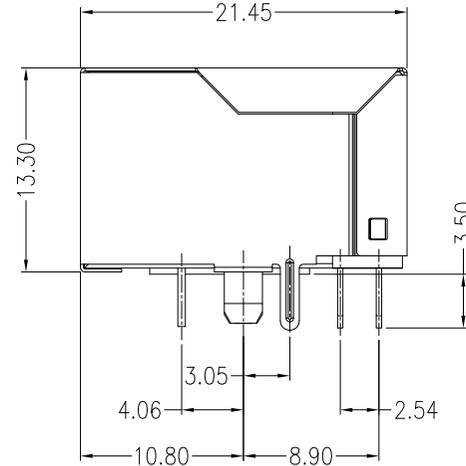
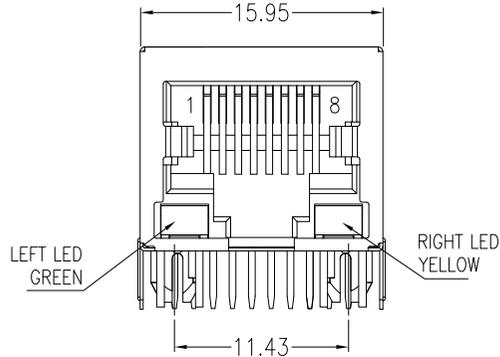
GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2014/12/10	Phebe Su
A1			Shared molding	2015/11/30	Phebe Su

1. MECHANICAL DIMENSIONS :



RECOMMEND PCB LAYOUT
(TOP VIEW)
DIMENSIONAL TOLERANCE: ±0.05



NOTES:

1. MATERIAL:

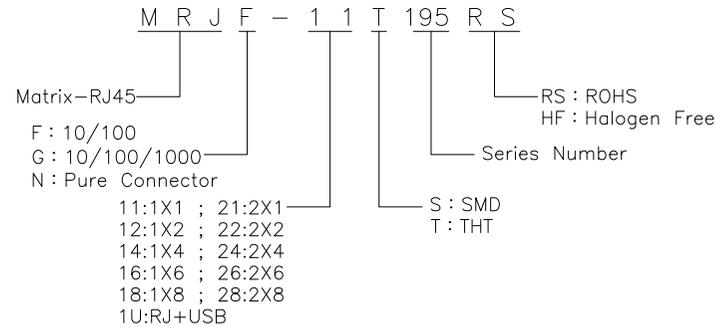
HOUSING: PBT POLYESTER UL94V-0 MIXED GLASS FIBER.
STANDARD COLOR-BLACK.

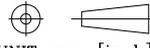
CONTACTS: 0.35mm THICK PHOS-BRONZE PLATED
WITH G/F GOLD AND MATTE TIN IN SOLDER
AREA.

SHIELD : 0.2mm THICK COPPER ALLOY, PLATED WITH NICKEL.

2. CAVITY CONFIRMS TO FCC RULES AND REGULATIONS PART 68,SUBPART F.

ORDER INFORMATION :

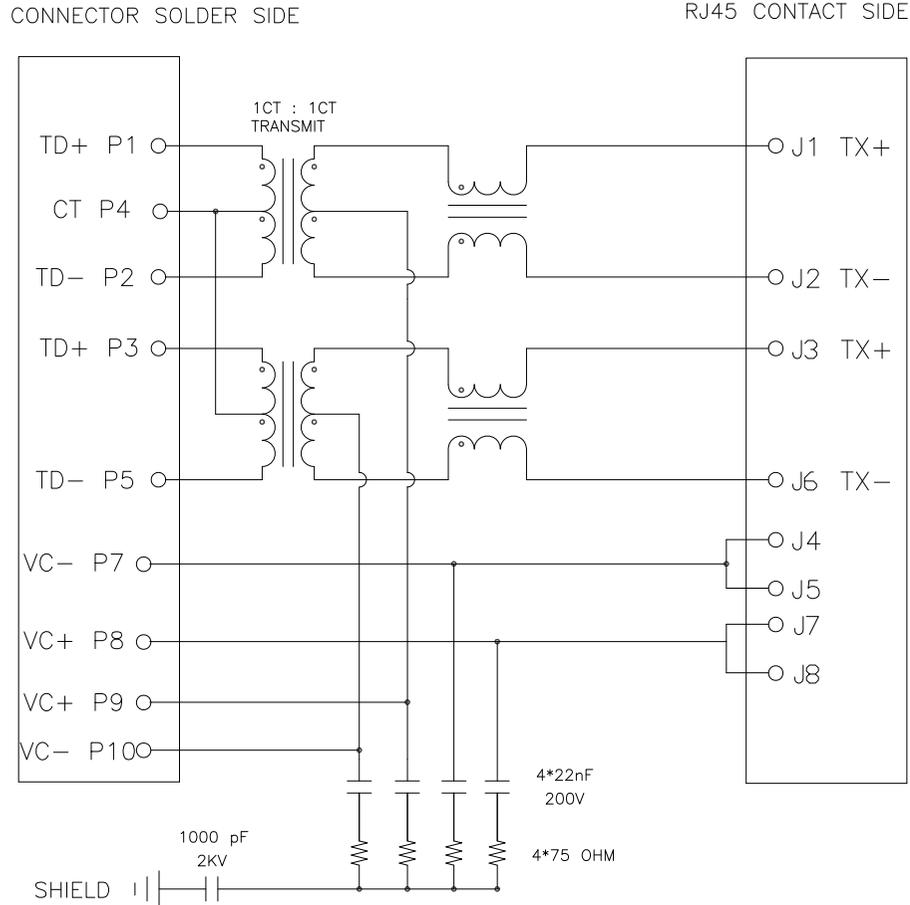


NO	VARIETY	QTY	METERIAL	REMARK
	 Matrix Electronics Co.,Ltd			
TOLERANCE: X.X ±0.38 X.XX ±0.25 X.XXX ±0.13 ANGLE: ±3°		DESIGN BY : Phebe Su CHECKED BY: Vicky Hsieh	DATE : 2015/11/30 DATE : 2015/11/30	PART NAME: RJ45,CONNECTORS,W/LED,10P,8C, POE 10/100MBPS,FILTER PART NO. MRJF-11T195RS
 UNIT: mm [inch]		APPROVED BY1: Richard Hsieh APPROVED BY2: Richard Hsieh	DATE : 2015/11/30 DATE : 2015/11/30	MOLD NO. NA DRAW NO. SHEET NO. 1 OF 3
SCALE:1:1		SIZE:A4		

GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2014/12/10	Phebe Su
A1			Shared molding	2015/11/30	Phebe Su

2. SCHEMATIC:



3. ELECTRICAL CHARACTERISTICS :

TEST NOTES:(25°±5°C)

1. TR:(100KHz,0.1V); 100%

PINS:(P1,P2):(J1,J2)=1:1±3%;(P3,P5):(J3,J6)=1:1±3%

2. LX:(100KHz,100mV,8mA, DC Bias) 100%

PINS:(P1,P2),(P3,P5)=350uH MINIMUM

3. DCR: 100%

PINS:(J1,J2),(J3,J6)=1.2 Ω MAXIMUM

4. HIPOT: 100%

PINS(P1,P2)TO(J1,J2),(P3,P5)TO(J3,J6)=1500VAC FOR 2 SECONDS

5. INSERTION LOSS:

-1.0dB MAXIMUM AT 1MHz TO 100MHz;

6. RETURN LOSS:

-20dB MINIMUM AT 1MHz TO 30MHz;

-15dB MINIMUM AT 30MHz TO 60MHz

-10dB MINIMUM AT 60MHz TO 80MHz

7. CROSS TALK:

-30dB MINIMUM AT 1MHz TO 100MHz

8. COMMON TO COMMON MODE REJECTION:

-30dB MINIMUM AT 1MHz TO 100MHz

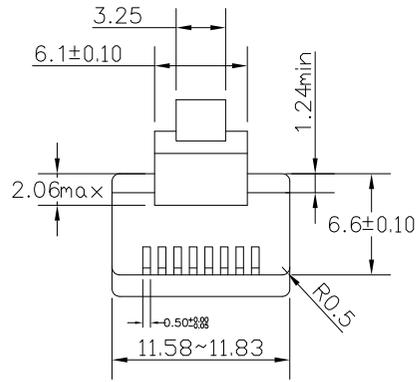
9. Balanced DC LINE CURRENT

350mA MAX@48V DC CONNTINUOUS

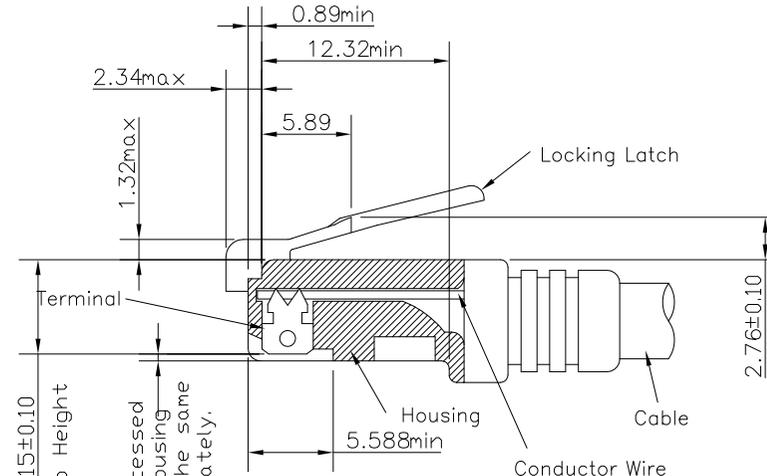
NO	VARIETY	QTY	METERIAL	REMARK
 Matrix Electronics Co.,Ltd				
TOLERANCE: X.X ±0.38 X.XX ±0.25 X.XXX ±0.13 ANGLE: ±3°		DESIGN BY : Phebe Su	DATE : 2015/11/30	PART NAME: RJ45,CONNECTORS,W/LED,10P,8C, POE 10/100MBPS,FILTER
 UNIT: mm [inch]		CHECKED BY: Vicky Hsieh	DATE : 2015/11/30	PART NO. MRJF-11T195RS
SCALE:1:1 SIZE:A4		APPROVED BY1: Richard Hsieh	DATE : 2015/11/30	MOLD NO. NA
		APPROVED BY2: Richard Hsieh	DATE : 2015/11/30	DRAW NO.
				SHEET NO. 2 OF 3

GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2014/12/10	Phebe Su
A1			Shared molding	2015/11/30	Phebe Su



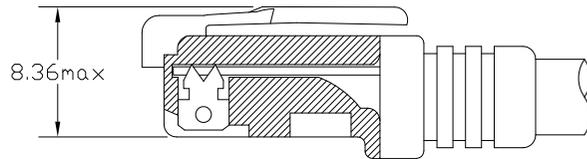
- * There must be no damage to housing or locking latch. There must be no nicks or cuts in cable.
- * Durability : 750 cycles generally



All contacts recessed below top of housing and must be at the same height approximately.

FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F FIGURE 68.500 (C)(2)(i) AND IEC 603-7 FIGURE 23 & 24

STANDARD MODULAR PLUG ASSEMBLY



FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F FIGURE 68.500 (C)(2)(ii)

NO	VARIETY	QTY	MATERIAL	REMARK
 Matrix Electronics Co.,Ltd				
TOLERANCE: X.X ±0.38 X.XX ±0.25 X.XXX ±0.13 ANGLE: ±3°		DESIGN BY : Phebe Su	DATE : 2015/11/30	PART NAME: RJ45,CONNECTORS,W/LED,10P,8C, POE 10/100MBPS,FILTER
 UNIT: mm [inch]		CHECKED BY: Vicky Hsieh	DATE : 2015/11/30	PART NO. MRJF-11T195RS
SCALE:1:1 SIZE:A4		APPROVED BY1: Richard Hsieh	DATE : 2015/11/30	MOLD NO. NA
		APPROVED BY2: Richard Hsieh	DATE : 2015/11/30	DRAW NO. SHEET NO. 3 OF 3