

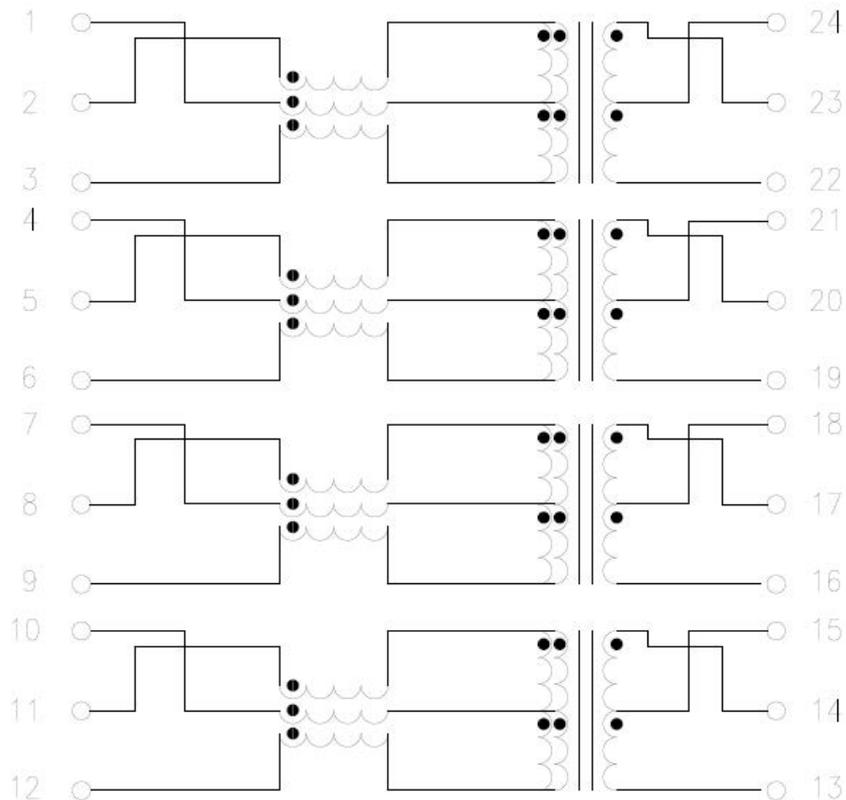


Feature

- Designed to Support 10GBASE-T Transceivers
- Designed to meet IEEE802.3bt type4 POE++ requirement
- OCL:120uH min @100KHz,100mV WITH 17 mA DC BIAS
- Low profile package for PCI Express
- RoHS compliance and peak reflow temperature rating 260°C
- Operating Temperature -40 °C to +100°C
- Storage temperature -50°C to +125°C.
- Meet 1A min. balanced DC current per pair
- Meet POE Compliant 52W Max./Pair, Total 104W Max.

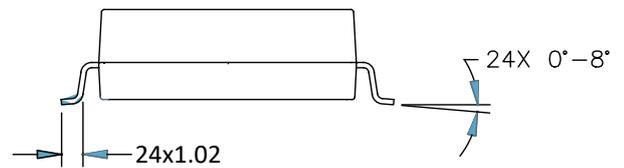
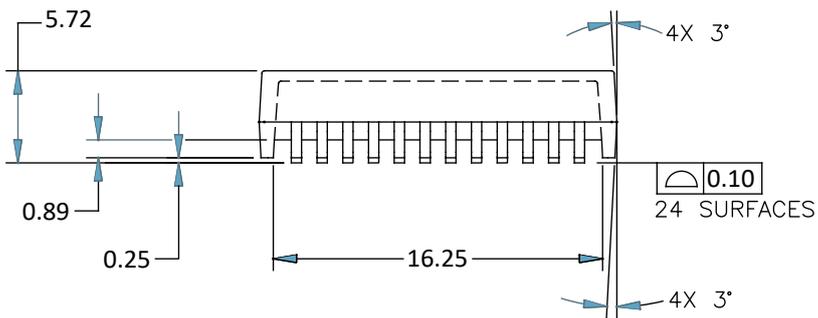
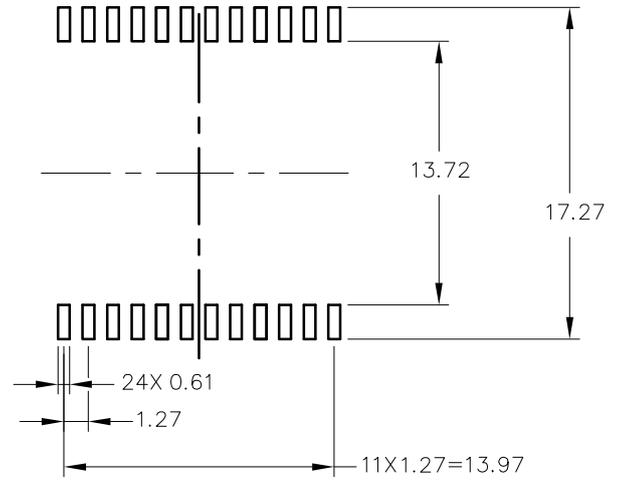
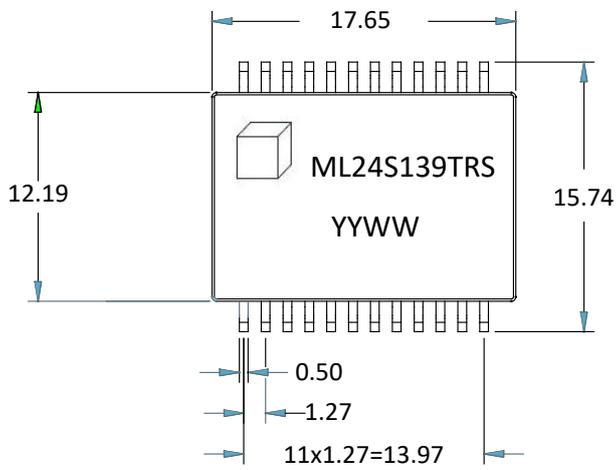
Electrical Specifications@25°C							
Part Number	Insertion Loss(SDD21) dB(max)		PHY SIDE Return Loss(SDD11) (dB Min) @Load 100Ω		PHY SIDE Return Losss(SDD22) (dB Min)@Load 100Ω		
	1-400MHz	400-500MHz	1-100MHz	100-500MHz	1-40MHz	40MHz -400MHz	400MHz-500MHz
ML24S139TRS	-2	-3	-22	-10+20Log(f/400)dB Min	-22	-22+10Log(f/40)dB Min	-12+40Log(f/400)dB Min
Part Number	DCMR (dB Min)			Cross talk Attenuation (dBMin)		HI-POT (Vrms)	
	1-100MHz	100 -300MHz	300-500 MHz	1-100MHz	100MHz-500MHz		
ML24S139TRS	-37	-30	-25	-30	-22	1500	

Schematic





Mechanical



Dimension: mm(inch) Unless otherwise specified, all tolerance are +/-0.25(0.01)