

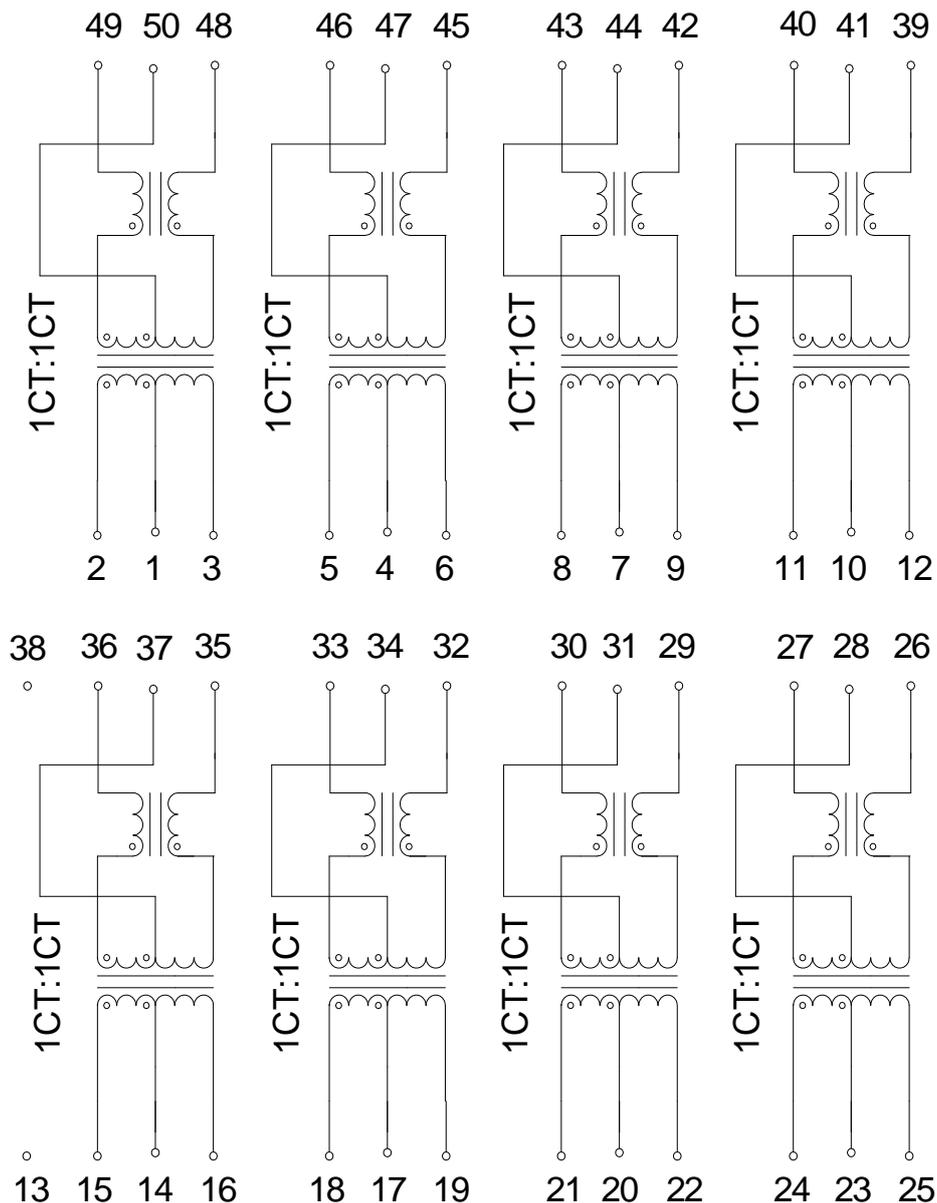


- Feature**
- Designed to meet IEEE802.3bt type3 POE++ requirement
 - Designed to meet IR 260°C peak requirement
 - Primary inductance 350uH min with 8mA DC Bias
 - RoHS Compliant
 - Meet 720mA min. balanced DC current per pair
 - Operating temperature range:-40°C to+85°C
 - Storage temperature range:-25°C to+125°C
 - Meet POE Compliant 36W Max./Pair, Total 72W Max.

Electrical Specifications @ 25°C

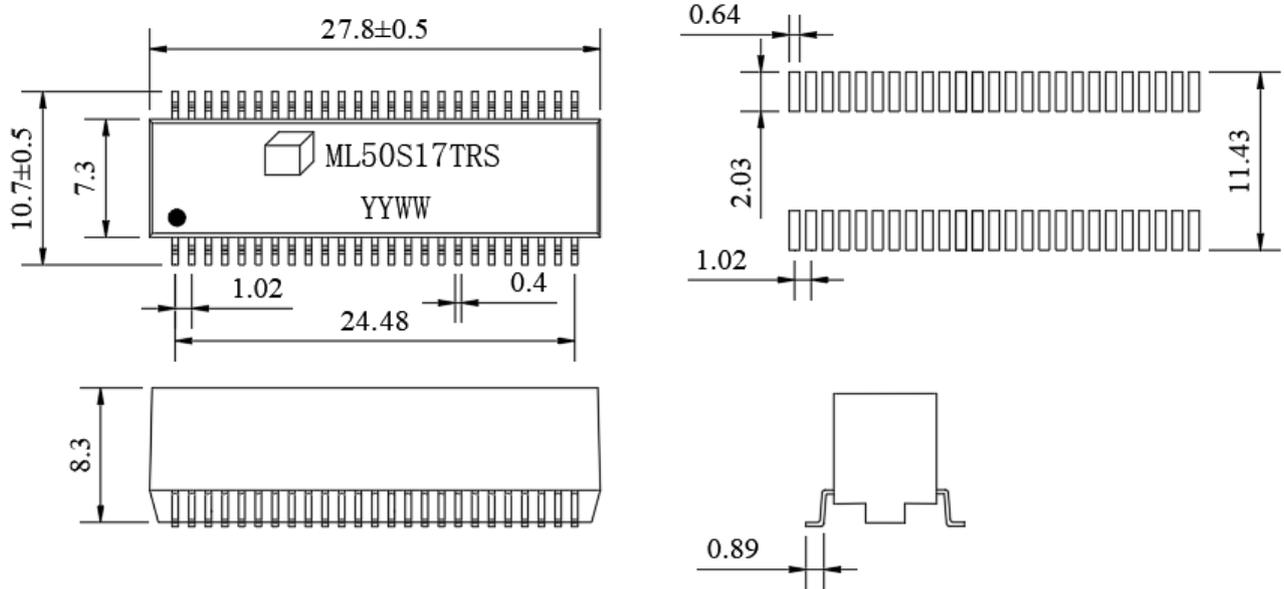
Part Number	Turn Ratio		Insertion Loss 0.5~100MHz (dB Max)	Return Loss (dB Min)			Cross Talk (dB Min)		CMRR (dB Min)	CWW (pF Max)	LK (uH Max)	DCR (Ω Max)	Hi-Pot (Vrms)
	Tx	Rx		0.5-30 MHz	30.1-60 MHz	60.1-80 MHz	0.5-40 MHz	40.1-100 MHz					
	ML50S17TRS	1:1		1:1	-1.0	-16	-14	-12					

Schematic





Mechanical



Dimension: mm(inch), Unless otherwise specified, all tolerance are $\pm 0.25(0.01)$

