



Feature

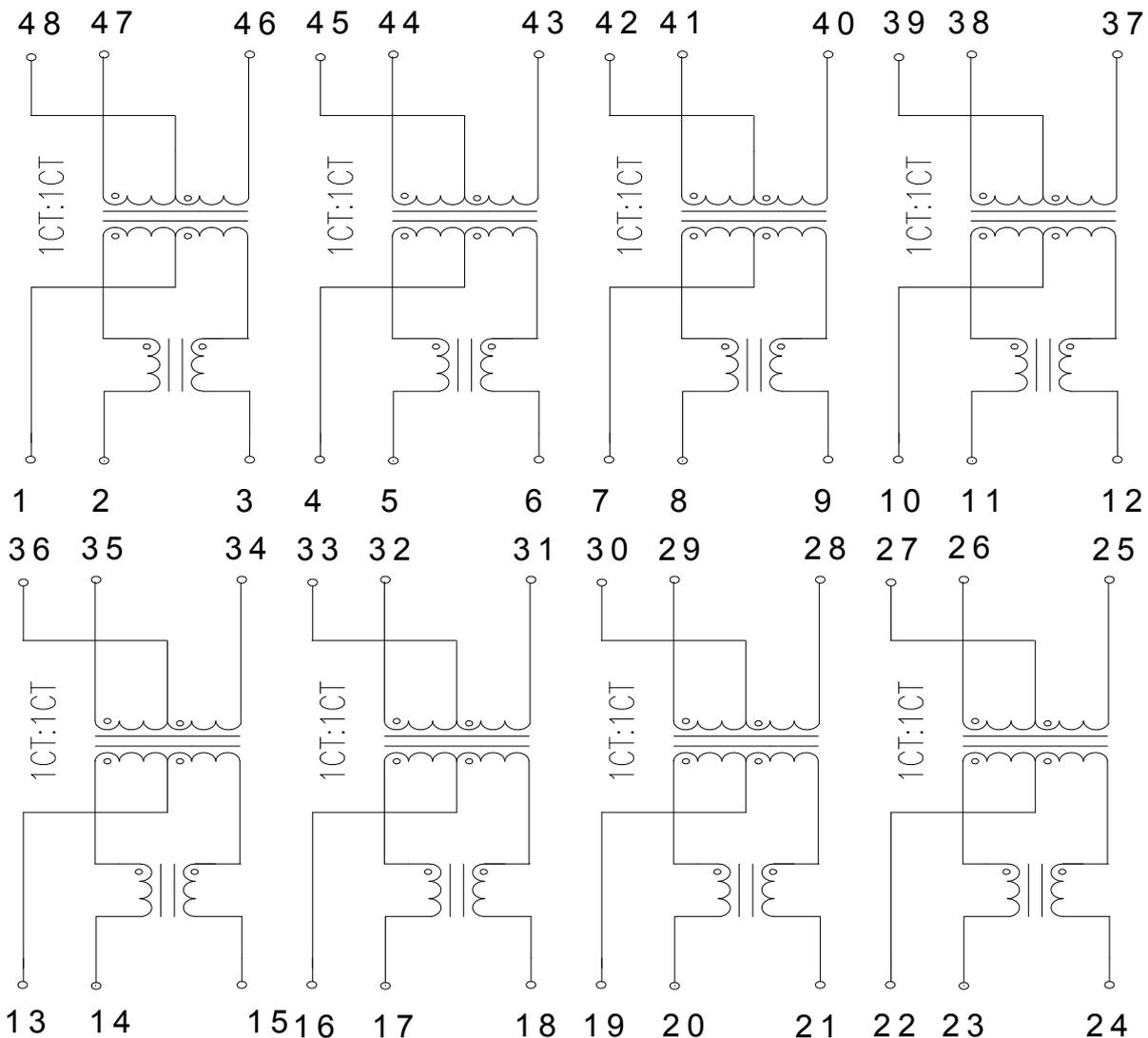


- Designed to meet IEEE802.3bt type3 POE++requirement
- Designed to meet IR 255°C peak requirement
- Primary inductance 350uH min with 8mA DC Bias
- RoHS Compliant
- Meet 720mA min. balanced DC current per pair
- Capacitance between windings (Cww)30pF Max(at 100kHz, 0.1V).
- Leakage Inductance (LL) 0.6uH Max (at 100kHz, 0.1V).DCR 1.2Ω Max.
- Storage temperature -25°C to +125°C.
- Meet POE Compliant 36W Max./Pair, Total 72W Max.

Electrical Specification@ 25°C Operating Temperature -40°C to +85°C

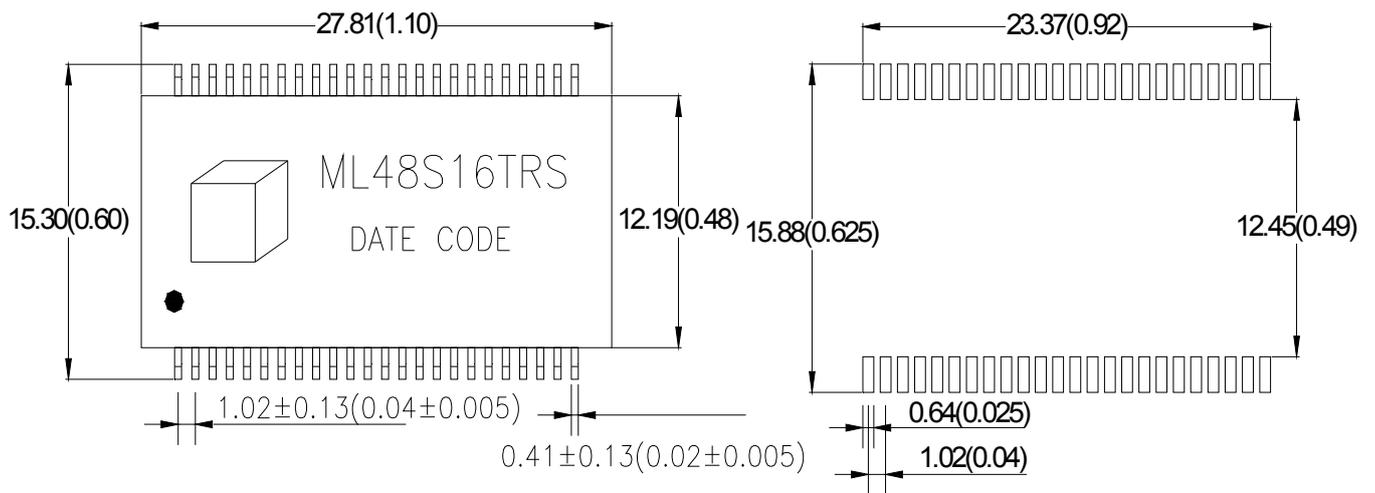
Part No.	Insertion Loss (dB max) 1-100MHz	Return Loss (dB min.) MHz					CMR (dB Min) MHz		Crosstalk (dB Min) MHz			CDMR (dB min) MHz			Isolation Voltage (Vrms)
		2-30MHz	40MHz	50MHz	60-80MHz	100MHz	1-60MHz	60-100MHz	30MHz	60MHz	125MHz	1MHz	60MHz	125MHz	
ML48S16TRS	-1	-18	-17	-16	-12	-10	-37	-25	-45	-40	-35	-45	-40	-35	1500VAC/1mA/2s

Schematic

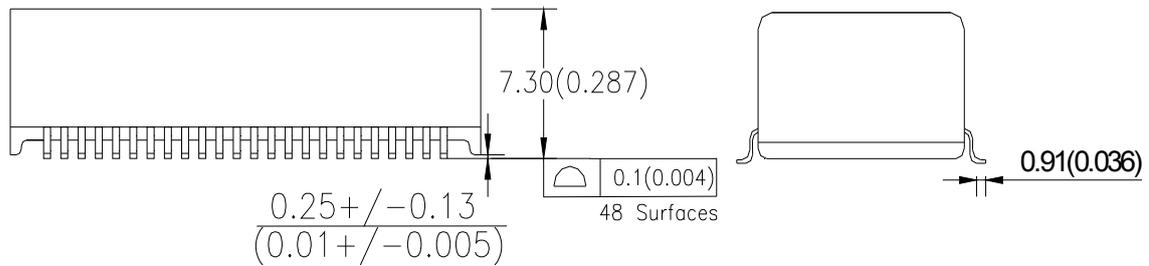




Mechanical



Recommend PAD Layout



Dimension: mm(inch), Unless otherwise specified, all tolerance are ±0.25(0.01)