



Feature



- Designed to meet IEEE802.3bt type3 POE++ requirement
- Designed to meet IR 260°C peak requirement
- Primary inductance 350uH Min with 15mA DC bias.
- Meet 720mA min. balanced DC current per pair
- Capacitance between winding: 25pF Max at 0.1MHz, 1V.
- Leakage inductance 0.5uH Max at 0.1MHz, 0.1V.
- RoHS compliance.
- Operating temperature range:-40°C t o + 8 5 °C
- Storage temperature -40°C to +85°C.
- Meet POE Compliant 36W Max./Pair, Total 72W Max.

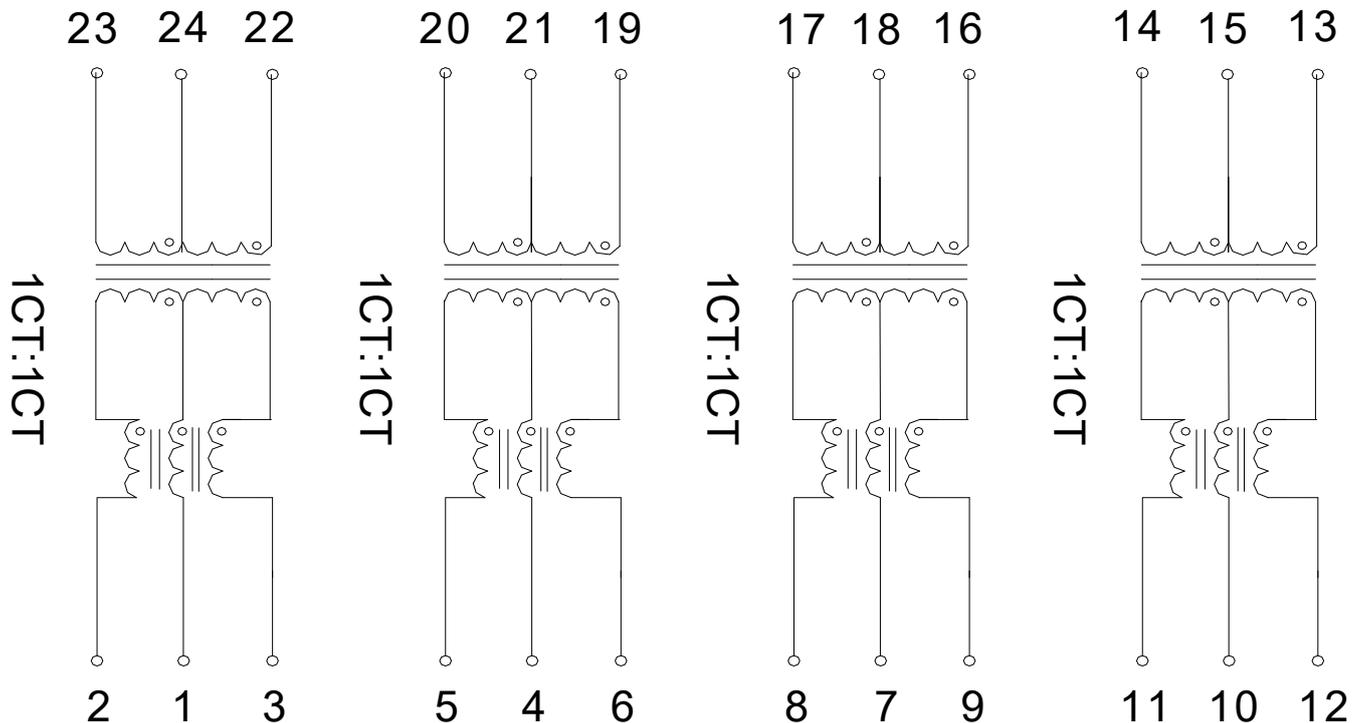
Electrical Specification@25°C

Part NO	Insertion Loss (dB MAX)			Return Loss (dB MIN @ 100Ω)					DC CURRENT/VOLTAGE RATING - PSE PINS
	MHz			MHz					
	0.1 MHz	30-100 MHz	125 MHz	1-30 MHz	40 MHz	50 MHz	60-80 MHz	100 MHz	
ML24S24TRS	-1.1	-1.0	-2.0	-18	-16	-16	-12	-10	720mA @57V (CONTINUOUS)

Continue

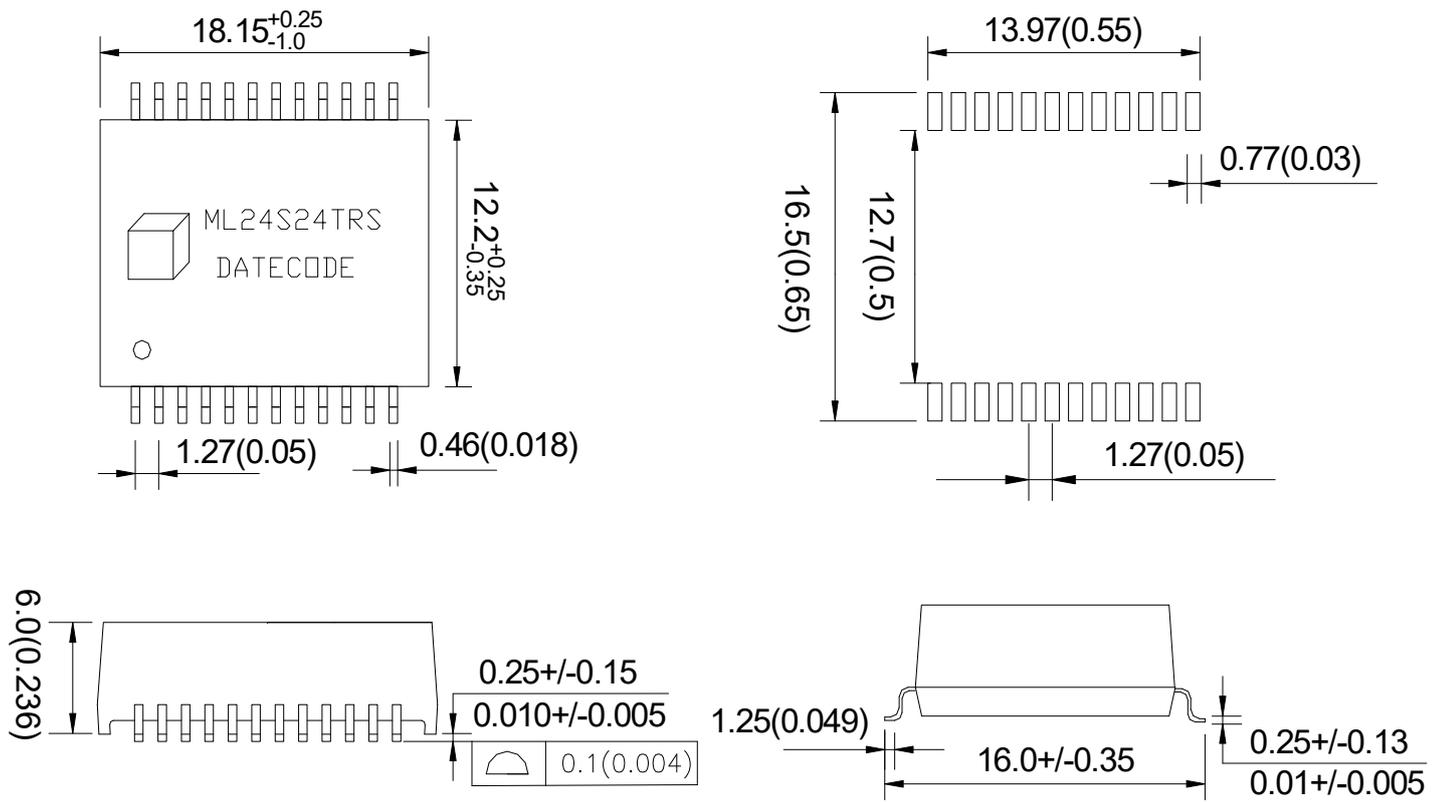
Part NO	Common Mode Rejection Ratio (dB MIN)				Crosstalk (dB MIN)				DCR (Ω Max)	DCR Balance (Ω)	Hipot (Vrms MIN)
	MHz				MHz						
	1 MHz	30-60 MHz	100 MHz	200 MHz	1 MHz	30 MHz	60 MHz	100 MHz			
ML24S24TRS	-45	-35	-30	-25	-50	-43	-37	-33	0.65	±0.05	1500

Schematic





Mechanical



Dimension: mm(inch) Unless otherwise specified , all tolerance are $\pm 0.25(0.01)$