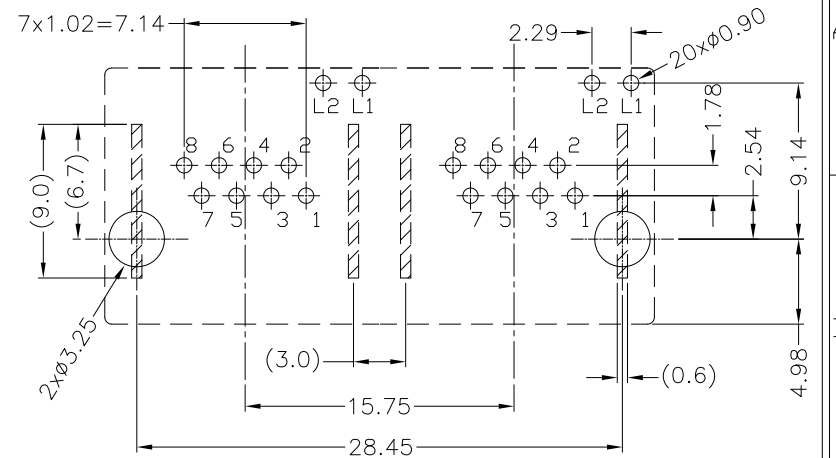
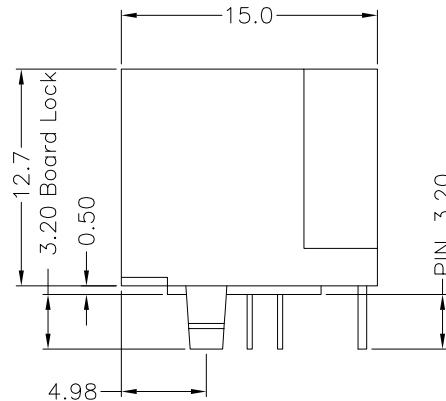
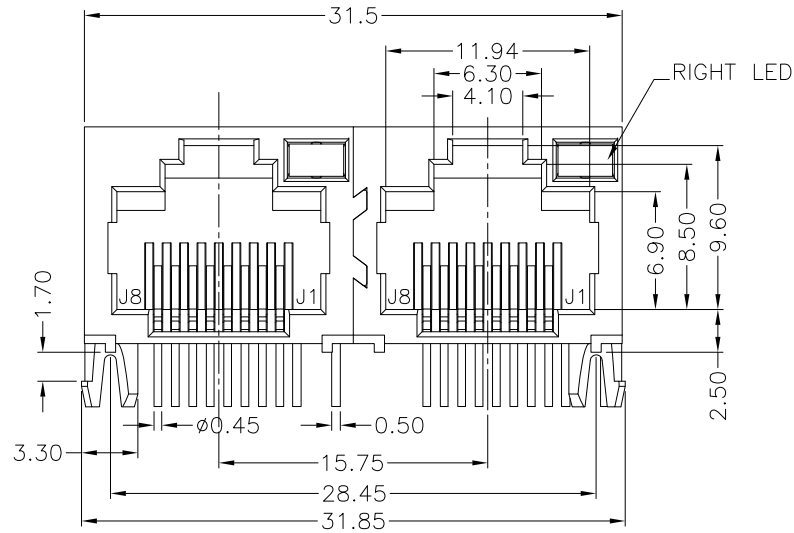


GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2016/12/05	Phebe Su



RECOMMENED PCB LAYOUT
COMPONENT SIDE
TOLERANCE ± 0.05

NOTES:

1.MATERIAL:

HOUSING:HIGH TEMPERATURE THERMO PLASTIC (UL94V-0), PBT White
CONTACT:PHOSPHOR BRONZE

2.FINISHED:

CONTACT:50u"MIN. NICKEL UNDERPLATING ALL AREA,
G/F PLATING ON CONTACT AREA.

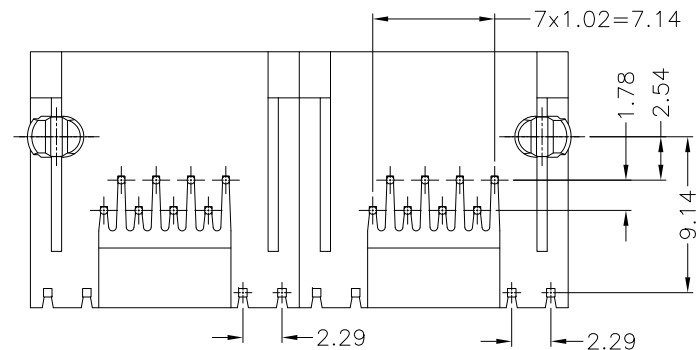
3.JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68,.500 (D) (2)

MATRIX PART NO:

Matrix-RJ45 M R J N - 1 2 599 T R S

RS:ROHS
HF,Halogen Free
Industrial temperature
Series Number
S,SMD
T,THT
114X1 ; 21:2X1
12:1X2 ; 22:2X2
14:1X4 ; 24:2X4
16:1X6 ; 26:2X6
18:1X8 ; 28:2X8
1U:RJ+USB

F : 10/100
G : 10/100/1000
N : Pure Connector

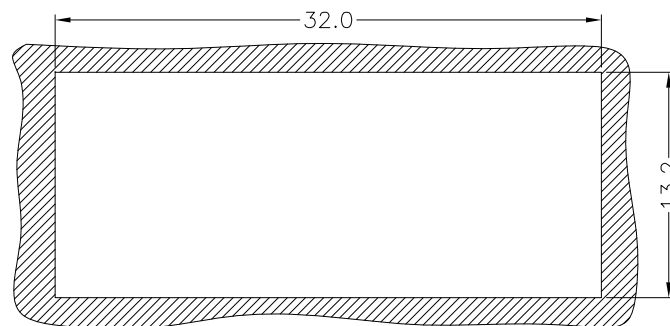


LEFT LED:

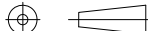
W/O LED

RIGHT LED:

GREEN

$$L2 + \text{---} \triangle \text{---} - L1$$


RECOMMENDED
PANEL CUTOUT DIMENSION

NO	VARIETY	QTY	MATERIAL	REMARK	
 Matrix Electronics Co.,Ltd					
TOLERANCE: X.X ±0.38 X.XX ±0.25 X.XXX ±0.13 ANGLE: ±1°		DESIGN BY : Phebe Su	DATE : 2016/12/05	PART NAME: RJ45 PCB JACK SHELL&W/LED	
 UNIT: mm [inch]		CHECKED BY: Joyce Cho	DATE : 2016/12/05	PART NO.	MRJN-12T599TRS
		APPROVED BY1: Richard Hsieh	DATE : 2016/12/05	MOLD NO.	NA
		APPROVED BY2: Richard Hsieh	DATE : 2016/12/05	DRAW NO.	
SCALE:1:1	SIZE:A4			SHEET NO.	1 of 1