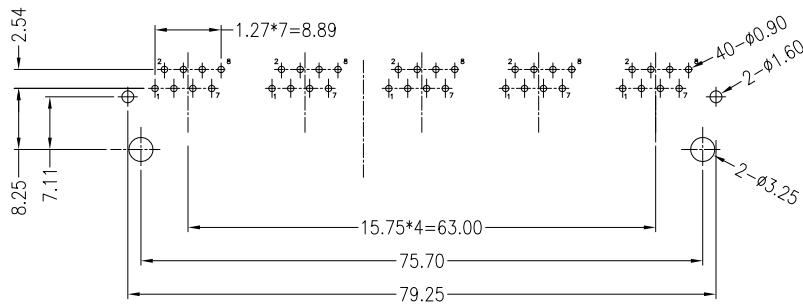
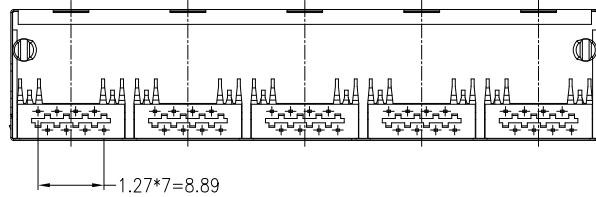
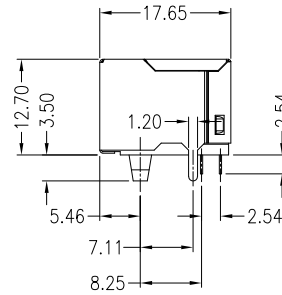
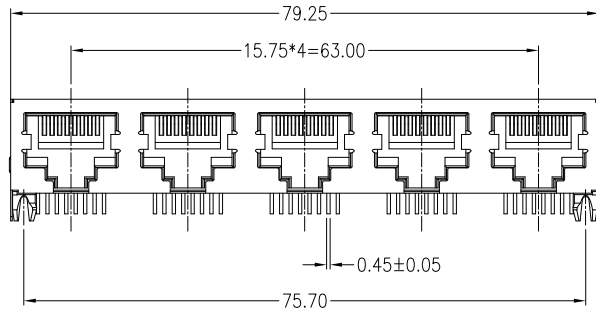


GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2018/05/30	Phebe Su
A1			Modify drawing	2018/06/30	Phebe Su



RECOMMEND PCB LAYOUT (COMPONENT SIDE)
DIMENSIONAL TOLERANCE: ± 0.05

NOTES:

1. MATERIALS:

HOUSING: PBT, UL 94V-0, BLACK.
CONTACTS: T=0.30 mm PHOSPHOR BRONZE,
50u" NICKEL UNDERPLATE
100u" MATTE TIN ON SOLDER TAIL
GOLD 15u" ON CONTACT AREA.

SHELL: STAINLESS T=0.20mm, NICKEL 30u" MIN. OVERALL

2. OPERATING TEMPERATURE: $-40^{\circ}\text{C} \sim +85^{\circ}\text{C}$

3. JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68, SUBPART F

MATRIX PART NO:

Matrix-RJ45

F : 10/100
G : 10/100/1000
N : Pure Connector

11:1X1 ; 21:2X1
12:1X2 ; 22:2X2
14:1X4 ; 24:2X4
16:1X6 ; 26:2X6
18:1X8 ; 28:2X8
1U:RJ+USB

RS : ROHS
HF : Halogen Free

Industrial temperature

Series Number

S : SMD
T : THT



Matrix Electronics Co., Ltd

TOLERANCE: X: X X.X: ± 0.38 X.XX: ± 0.25 X.XXX: ± 0.13 ANGLE: $\pm 1^{\circ}$	DESIGN BY : Phebe Su	DATE : 2018/06/30	PART NAME: RJ45 1X5 W/SHELL W/O LED	
	CHECKED BY: Vicky Hsieh	DATE : 2018/06/30	PART NO.	MRJN-15T863TRS
	APPROVED BY1: Richard Hsieh	DATE : 2018/06/30	MOLD NO.	NA
	APPROVED BY2: Richard Hsieh	DATE : 2018/06/30	DRAW NO.	
UNIT: mm [inch] SCALE: 1:1 SIZE: A4			SHEET NO.	1 OF 1