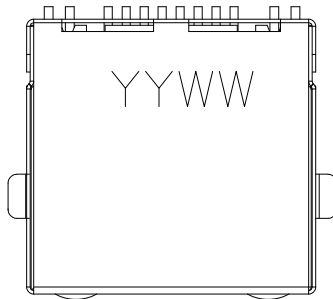
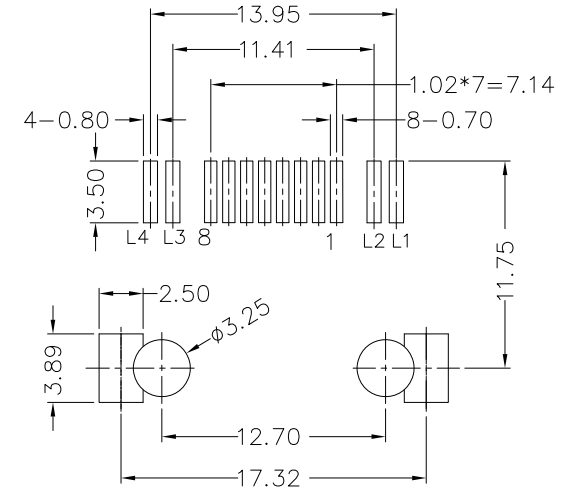
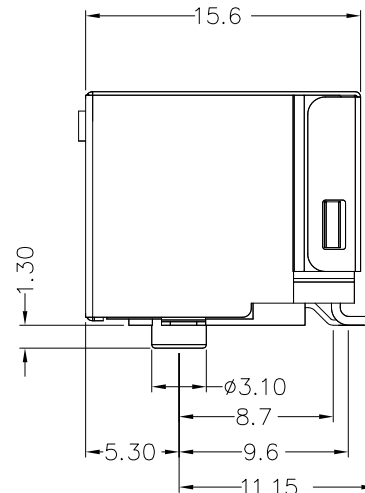
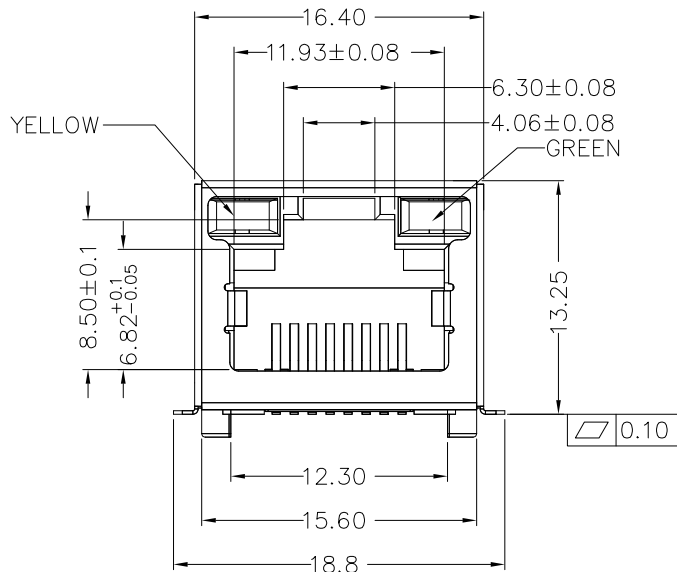


GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2018/02/07	Phebe Su
A1			Change	2019/02/22	Phebe Su



Yellow Green
L4 → L3 L2 → L1



RECOMMENED PCB LAYOUT (COMPONENT SIDE)
TOLERANCE ± 0.05

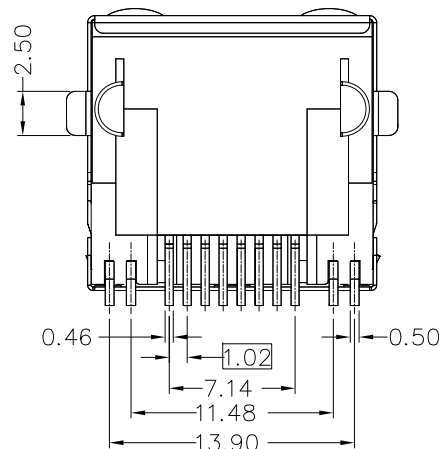
NOTES:

1. MATERIALS:

HOUSING: LCP, UL 94V-0, BLACK.

CONTACTS: T=0.30 mm PHOSPHOR BRONZE,
GOLD FLASH ON CONTACT AREA.
100u" MATTE TIN ON SOLDER TAIL
50u" NICKEL UNDERPLATE

SHELL: BRASS, T=0.20mm

2. OPERATING TEMPERATURE: $-40^{\circ}\text{C} \sim +85^{\circ}\text{C}$ 3. JACK CAVITY CONFORMS TO FCC RULES AND
REGULATIONS PART 68, SUBPART F

ORDER INFORMATION :

Matrix-RJ45
F : 10/100
G : 10/100/1000
N : Pure Connector
11: 1X1 ; 21: 2X1
12: 1X2 ; 22: 2X2
14: 1X4 ; 24: 2X4
16: 1X6 ; 26: 2X6
18: 1X8 ; 28: 2X8
1U: RJ+USB
M R J N - 1 1 S 993 R S
RS: ROHS
HF: Halogen Free
Series Number
S: SMD
T: THT



Matrix Electronics Co., Ltd

TOLERANCE: X.X ± 0.38 X.XX ± 0.25 X.XXX ± 0.13 ANGLE: $\pm 3^{\circ}$	DESIGN BY : Phebe Su	DATE : 2019/02/22	PART NAME: RJ45 1X1 PCB JACK WITH LED SMT TYPE	
	CHECKED BY: Janice Liu	DATE : 2019/02/22	PART NO.	MRJN-11S993RS
 UNIT: mm [inch] SCALE: 1:1 SIZE: A4	APPROVED BY1: Richard Hsieh	DATE : 2019/02/22	MOLD NO.	NA
	APPROVED BY2: Richard Hsieh	DATE : 2019/02/22	DRAW NO.	
			SHEET NO.	1 OF 1