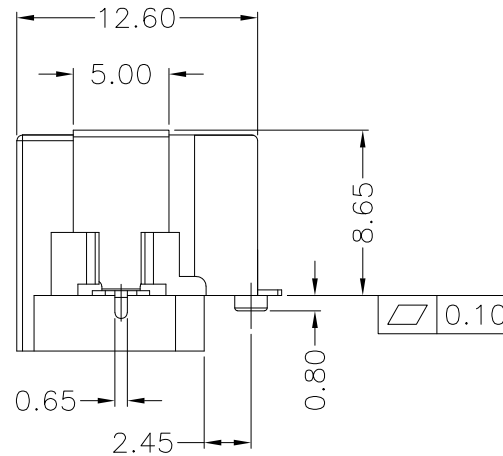
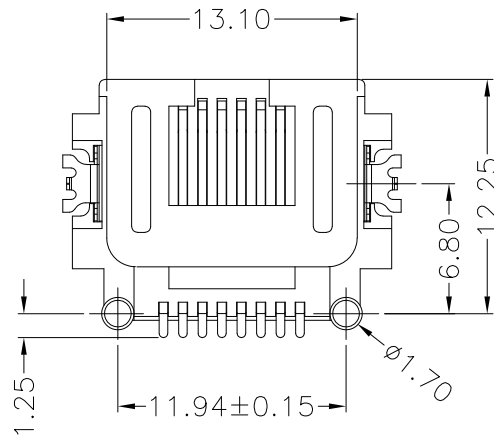
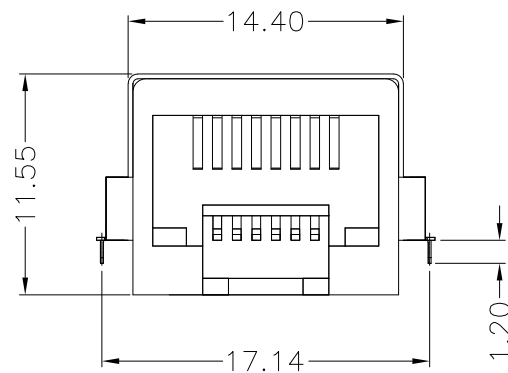
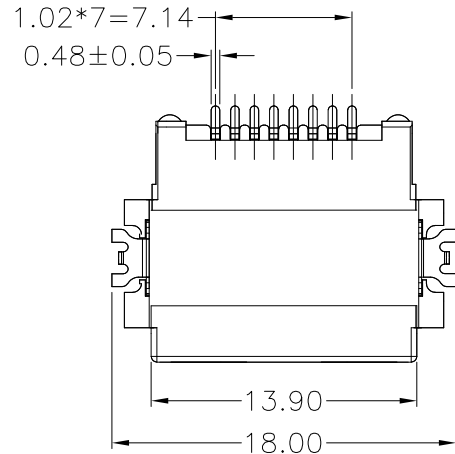


GP Component

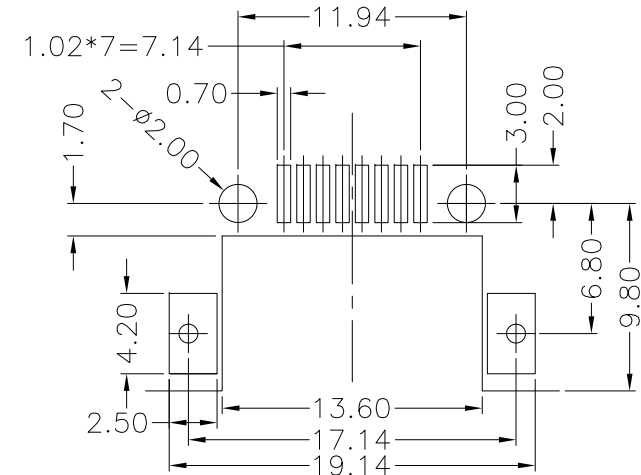
REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2017/08/22	Phebe Su
A1			Change	2017/08/23	Phebe Su
A2			Change housing material	2018/12/28	Phebe Su
A3			Optimized structure	2019/04/01	Phebe Su



MATRIX PART NO:

M R J N - 1 1 S 724 T R S

Matrix-RJ45

F : 10/100
G : 10/100/1000
N : Pure Connector11:1X1 ; 21:2X1
12:1X2 ; 22:2X2
14:1X4 ; 24:2X4
16:1X6 ; 26:2X6
18:1X8 ; 28:2X8
1U:RJ+USBS: SMD
T: THTRS: ROHS
HF: Halogen Free
Industrial temperature
Series Number

RECOMMEND PCB LAYOUT (TOP VIEW)

DIMENSIONAL TOLERANCE: ± 0.05

NOTES:

- MATERIALS:
HOUSING: PA9T, BLACK UL94V-0,
CONTACTS: PHOSPHOR BRONZE T=0.30,
GOLD FLASH ON CONTACT AREA,
MATTE TIN 80u" MIN ON SOLDER TAIL
SHIELD: COPPER ALLOY, 30u" NICKEL PLATING
OVERALL
- OPERATING TEMPERATURE: $-40^{\circ}\text{C} \sim +85^{\circ}\text{C}$
- WITHDRAWAL FORCE: 7.0kgf Min.
- JACK CAVITY CONFORMS TO FCC RULES AND
REGULATIONS PART 68, SUBPART F



Matrix Electronics Co.,Ltd

TOLERANCE: X:X X:X X:XX X:XXX ANGLE: $\pm 1^{\circ}$	DESIGN BY : Phebe Su	DATE : 2019/04/01	PART NAME: RJ45 1x1 W/O LED SHEN
UNIT: mm [inch]	CHECKED BY: Vicky Hsieh	DATE : 2019/04/01	PART NO. MRJN-11S724TRS
SCALE:1:1	APPROVED BY1: Richard Hsieh	DATE : 2019/04/01	MOLD NO. NA
SIZE:A4	APPROVED BY2: Richard Hsieh	DATE : 2019/04/01	DRAW NO. SHEET NO. 1 OF 1