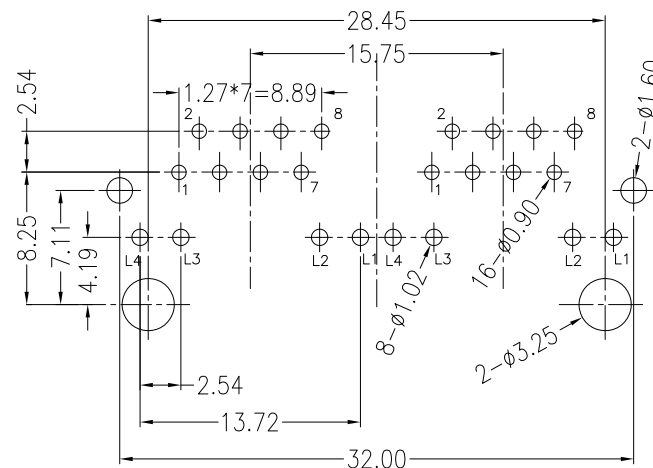
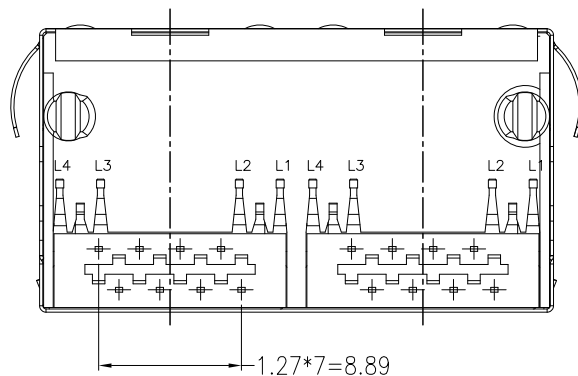


GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
AO			Initial	2018/09/27	Phebe Su



RECOMMEND PCB LAYOUT(TOP VIEW)

DIMENSIONAL TOLERANCE: ± 0.05

NOTES:

1. MATERIALS:

HOUSING: PBT BLACK(UL94V-0)

CONTACTS: PHOSPHOR BRONZE T:0.30MM

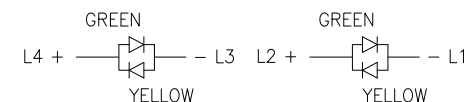
2. FINISH:

CONTACT: 50u" NICKEL LUNDERPLAT ON ALL AREA,

100u"MIN. TIN ON SOLDERTAILS,

GOLD 6u" PLATED ON CONTACT AREA.

SHIELD: STAINLESS T:0.20MM PLATED WITH NICKEL

3. OPERATING TEMPERATURE: $-40^{\circ}\text{C} \sim +85^{\circ}\text{C}$ 4. JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS
PART 68, SUBPART F

ORDER INFORMATION :

MRJN-12T944RS

Matrix-RJ45

F: 10/100

G: 10/100/1000

N: Pure Connector

11:1X1 ; 21:2X1

12:1X2 ; 22:2X2

14:1X4 ; 24:2X4

16:1X6 ; 26:2X6

18:1X8 ; 28:2X8

1U:RJ+USB

RS: ROHS

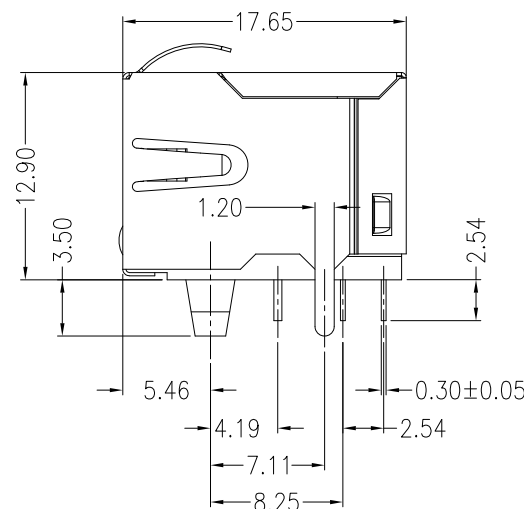
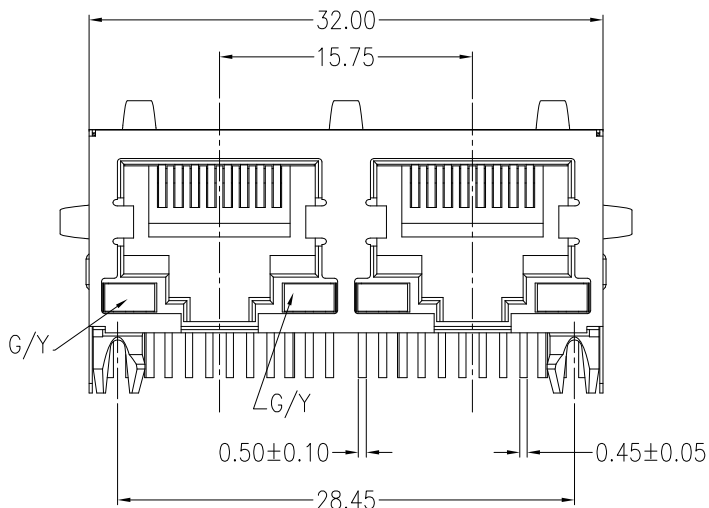
HF: Halogen Free

Industrial temperature

Series Number

S: SMD

T: THT



Matrix Electronics Co.,Ltd

TOLERANCE: X:X X:XX X:XXX ANGLE: $\pm 3^{\circ}$	DESIGN BY : Phebe Su	DATE : 2018/09/27	PART NAME: RJ45 1x2 JACK W/ LED & SHELL T/H	
	CHECKED BY: Vicky Hsieh	DATE : 2018/09/27	PART NO.	MRJN-12T944RS
 UNIT: mm [inch]	APPROVED BY1: Richard Hsieh	DATE : 2018/09/27	MOLD NO.	NA
	APPROVED BY2: Richard Hsieh	DATE : 2018/09/27	DRAW NO.	
SCALE:1:1	SIZE:A4		SHEET NO.	1 OF 1