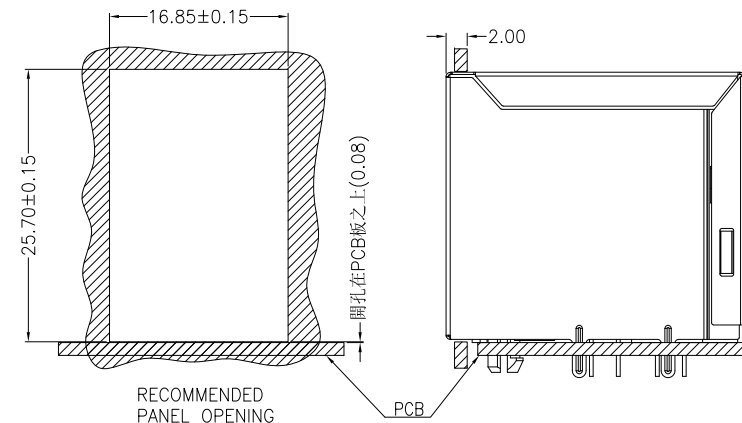
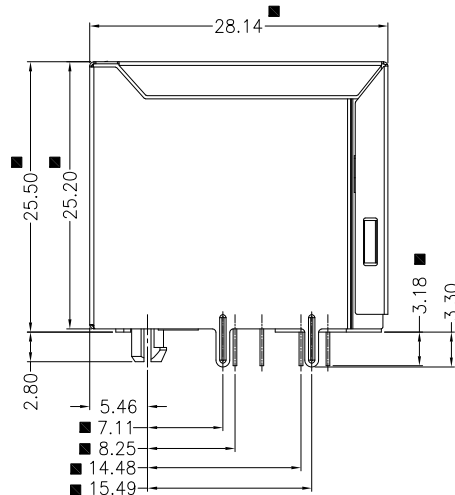
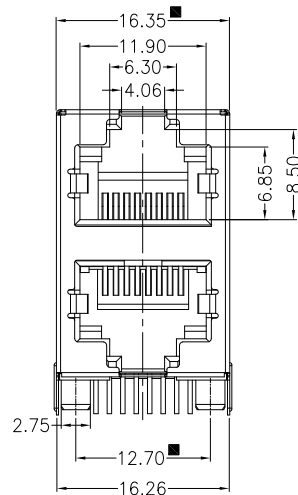


GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2016/03/06	Phebe Su
A1			Optimize the bottom of the molded case	2019/02/20	Phebe Su



NOTES:

1.MATERIAL:

HOUSING:BLACK,PBT UL 94V-0

CONTACT:PHOSPHOR BORNZE,T=0.35mm

SHELL:BRASS, T=0.20mm

2.FINISH:

CONTACT:50u"MIN. NICKEL UNDERPLATING ALL AREA, 100u"MIN. TIN ON SOLDERTAILS, G/F PLATING ON CONTACT .

SHELL:30u"MIN. NICKEL UNDERPLATING ALL AREA

3.OPERATING TEMPERATURE:-40°C~+85°C.

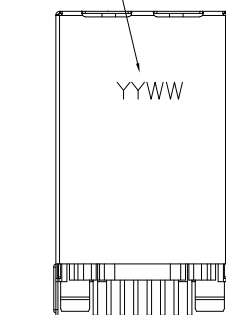
4.JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68, SUBPART F

ORDER INFORMATION :

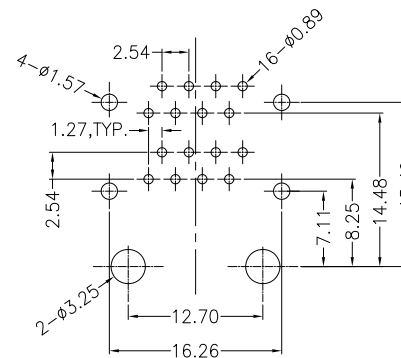
Matrix-RJ45	M	R	J	N	-	2	1	T	6	4	1	T	R	S
F: 10/100														
G: 10/100/1000														
N: Pure Connector														
11:1X1 ; 21:2X1														
12:1X2 ; 22:2X2														
14:1X4 ; 24:2X4														
16:1X6 ; 26:2X6														
18:1X8 ; 28:2X8														
1U:RJ+USB														
RS:ROHS														
HF:Halogen Free														
Industrial temperature														
Series Number														
S:SMD														
T:THT														

A1 增加銅口破挫力,
將下層三個窗口堵上

噴印週期



BACK VIEW

PCB LAYOUT TOLERANCE:±0.05
TOP VIEW

Matrix Electronics Co.,Ltd

TOLERANCE: X.X ±0.38 X.XX ±0.25 X.XXX ±0.13 ANGLE: ±3°	DESIGN BY : Phebe Su	DATE : 2019/02/20	PART NAME: RJ45 CONNECTOR 2X1 PORT W/O LED, 8P8C, Shielded
APPROVED BY1: Richard Hsieh	CHECKED BY: Joyce Cho	DATE : 2019/02/20	PART NO. MRJN-21T641TRS
APPROVED BY2: Richard Hsieh	DATE : 2019/02/20	MOLD NO. NA	
UNIT: mm [inch] SCALE:1:1 SIZE:A4	DATE : 2019/02/20	DRAW NO. SHEET NO.	1 OF 1