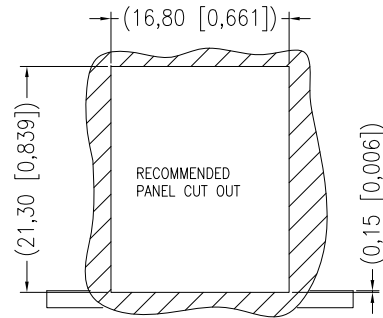
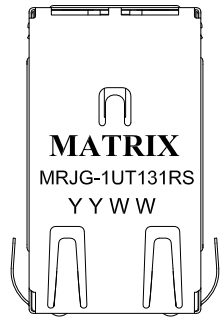
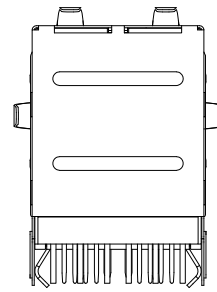
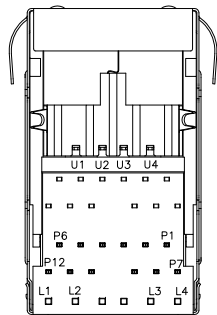
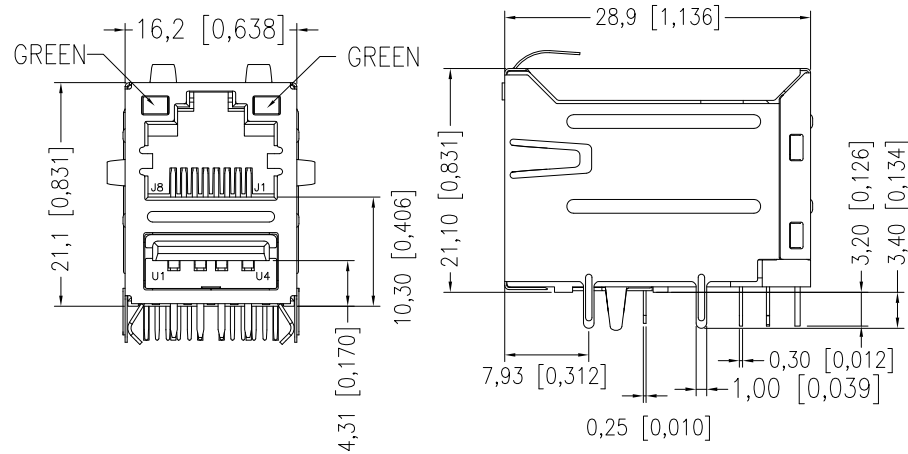
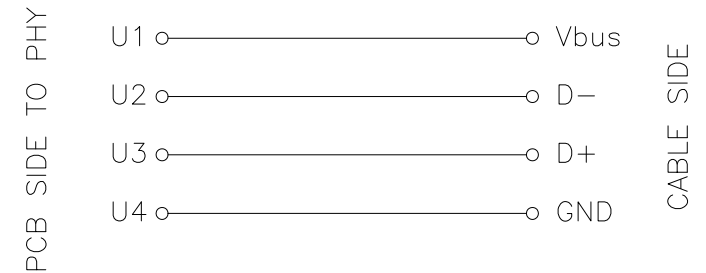


GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
AO			Initial	2014/04/19	Phebe Su



SCHEMATIC FOR USB:



BACK VIEW

SPECIFICATIONS :

1. MATERIAL

- HOUSING : THERMOPLASTIC, UL 94V-0, BLACK
- MODULE CASE : HIGH TEMPERATURE THERMOPLASTIC, UL 94V-0, BLACK
- RJ CONTACTS : PHOSPHOR BRONZE
- PLATING : G/F ON CONTACT AREA, 100U" MIN TIN PLATING ON SOLDER TAILS, OVER 50~100U" NICKEL OVERALL
- SOLDER TAIL TERMINAL : BRASS, PLATING : 100U" TIN MIN OVER 50U" NICKEL OVERALL
- SHIELD : STAINLESSS, THICKNESS, PRE-SOLDERING

2. ELECTRICAL

- INSULATION RESISTANCE : 500 MOHMS MIN
- DIELECTRIC WITHSTANDING VOLTAGE : 1000 VAC FOR 1 MINUTE

3. DURABILITY TEST RATING

- INSERTION FORCE WITH THE LATCH DEPRESSED :22N MAX
- REMOVAL FORCE WITH THE LATCH DEPRESSED :44N MAX
- DURABILITY :750 CYCLES

4. OPERATING AND STORAGE TEMPERATURE

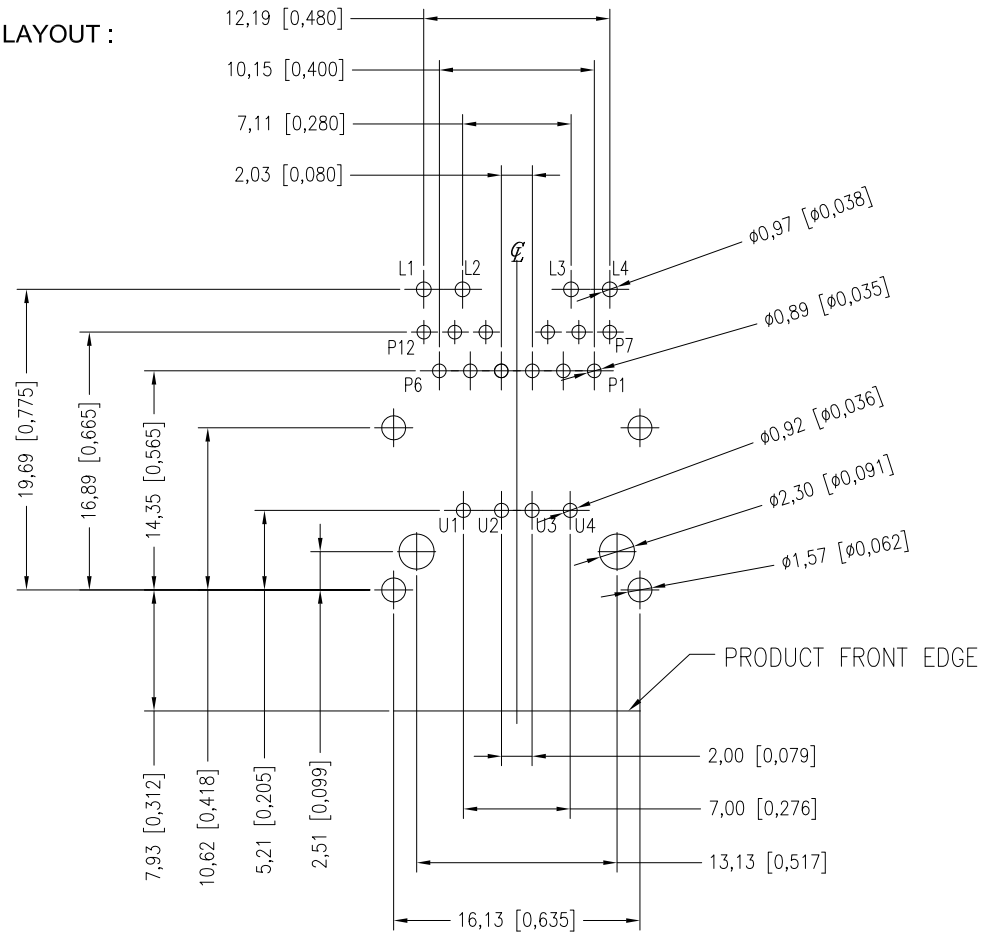
- OPERATING TEMPERATURE : 0°C TO +70°C
- STORAGE TEMPERATURE : -40°C TO +85°C

NO	VARIETY	QTY	METERIAL	REMARK
 Matrix Electronics Co.,Ltd				
TOLERANCE: X:X ±0.38 X:XX ±0.25 X:XXX ±0.13 ANGLE: ±3°		DESIGN BY : Phebe Su	DATE : 2014/04/19	PART NAME: RJ45+USB2.0 W/LED&SHELL 10/100/1000 FILTERMBPS,FILTER
UNIT: mm [inch]		CHECKED BY: Vicky Hsieh	DATE : 2014/04/19	PART NO. MRJG-1UT131RS
SCALE:1:1 SIZE:A4		APPROVED BY1: Richard Hsieh	DATE : 2014/04/19	MOLD NO. NA
		APPROVED BY2: Richard Hsieh	DATE : 2014/04/19	DRAW NO.
				SHEET NO. 1 OF 3

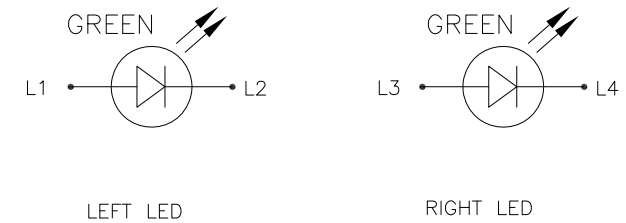
GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
AO			Initial	2014/04/19	Phebe Su

2. PCB LAYOUT :

RECOMMENDED PCB LAYOUT
(COMPONENT SIDE VIEW)

3. LED CONFIGURATION :



Emitting Color	λ_p (nm)	V_f @ $I_f=20mA$	I_r @ $V_r=5V$
Green	565	1.7V~2.6V	10 μA max.

NO	VARIETY	QTY	MATERIAL	REMARK
 Matrix Electronics Co.,Ltd				
TOLERANCE: X.X ± 0.38 X.XX ± 0.25 X.XXX ± 0.13 ANGLE: $\pm 3^\circ$		DESIGN BY : Phebe Su	DATE : 2014/04/19	PART NAME: RJ45+USB2.0 W/LED&SHELL 10/100/1000 FILTERMBPS,FILTER
		CHECKED BY: Vicky Hsieh	DATE : 2014/04/19	PART NO. MRJG-1UT131RS
		APPROVED BY1: Richard Hsieh	DATE : 2014/04/19	MOLD NO. NA
		APPROVED BY2: Richard Hsieh	DATE : 2014/04/19	DRAW NO.
UNIT: mm [inch] SCALE:1:1 SIZE:A4				SHEET NO. 2 OF 3

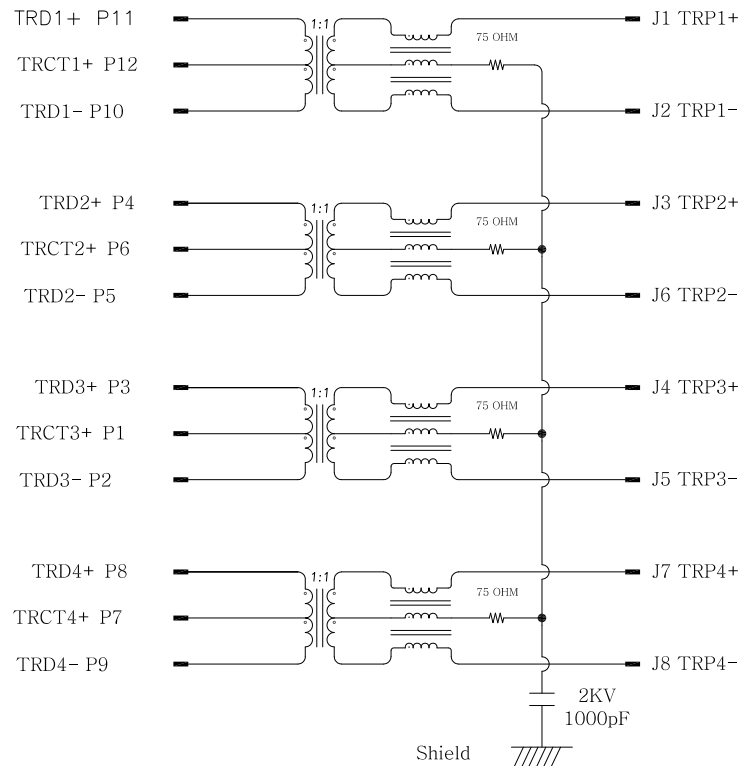
GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
AO			Initial	2014/04/19	Phebe Su

4. SCHEMATIC:

Board side

Cable side



MATRIX PART NO:

Matrix-RJ45

M R J G - 1 U T 131 R S

RS: ROHS
HF: Halogen Free

F: 10/100
G: 10/100/1000
N: Pure Connector

Series Number

S: SMD
T: THT

11:1X1 ; 21:2X1
12:1X2 ; 22:2X2
14:1X4 ; 24:2X4
16:1X6 ; 26:2X6
18:1X8 ; 28:2X8
1U:RJ+USB

5. ELECTRICAL CHARACTERISTICS :

- INSERTION LOSS :
 - 1~100 MHz -1.0dB MAX.
- RETURN LOSS(LOAD 100Ω) :
 - 1~30 MHz -18dB MIN
 - 30~60 MHz -16dB MIN
 - 60~80 MHz -12dB MIN
 - 80~100 MHz -10dB MIN
- COMMON MODE REJECTION
 - 1~100 MHz -30dB MIN
- CROSS TALK
 - 1~100 MHz -30dB MIN
- INDUCTANCE @ 100KHz, 0.1V, 8mA DC BIAS
 - (P11-P10),(P4-P5),(P3-P2),(P8-P9) : 350 uH MIN
- HI-POT TEST
 - PCB SIDE TO CABLE SIDE : 1500VAC 60S OR 2250VDC 60S
- TURNS RATION 100KHz
 - (P11-P10) : (J1-J2) = 1:1 ±5%
 - (P4-P5) : (J3-J6) = 1:1 ±5%
 - (P3-P2) : (J3-J6) = 1:1 ±5%
 - (P8-P9) : (J3-J6) = 1:1 ±5%

NO	VARIETY	QTY	METERIAL	REMARK
 Matrix Electronics Co.,Ltd				
TOLERANCE: X:X ±0.38 X:XX ±0.25 X:XXX ±0.13 ANGLE: ±3°		DESIGN BY : Phebe Su	DATE : 2014/04/19	PART NAME: RJ45+USB2.0 W/LED&SHELL 10/100/1000 FILTERMBPS,FILTER
UNIT: mm [inch]		CHECKED BY: Vicky Hsieh	DATE : 2014/04/19	PART NO. MRJG-1UT131RS
SCALE:1:1 SIZE:A4		APPROVED BY1: Richard Hsieh	DATE : 2014/04/19	MOLD NO. NA
		APPROVED BY2: Richard Hsieh	DATE : 2014/04/19	DRAW NO.
				SHEET NO. 3 OF 3