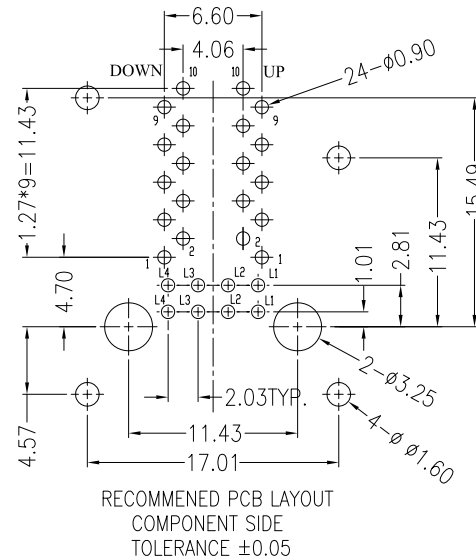
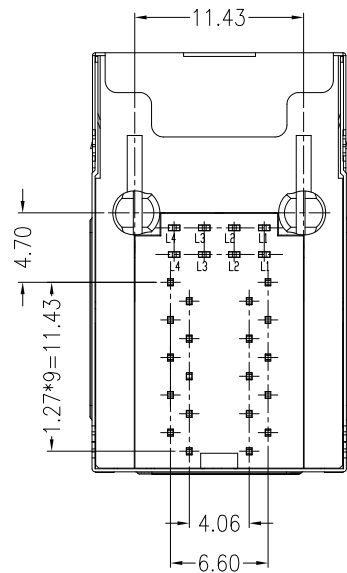


GP Component

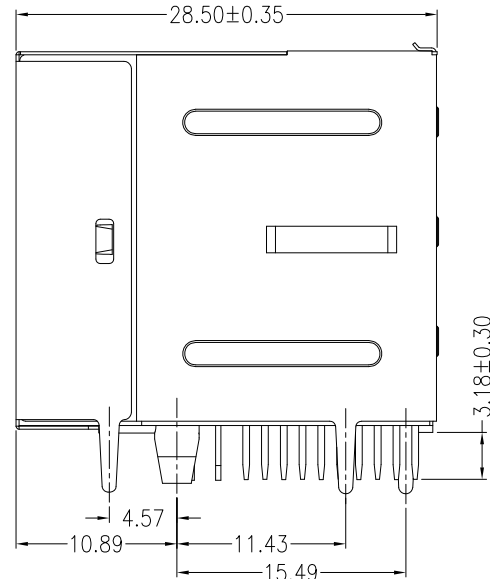
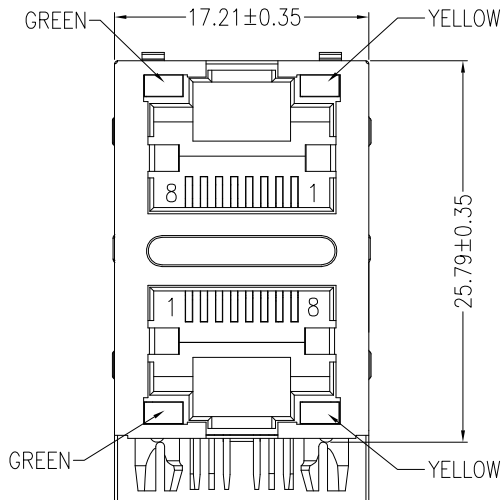
REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2018/02/23	Phebe Su
A1			Change PBT to PA66	2018/04/03	Phebe Su

1. MECHANICAL DIMENSIONS :



噴印内容:

MATRIX
MRJF-21T812RS
YYWW



NOTES:

1.MATERIAL:

HOUSING: PA66 BLACK UL94V-0

CONTACTS:PHOSPHOR BRONZE

GOLD FLASH ON CONTACT AREA, TIN PLATING ON SOLERTAIL

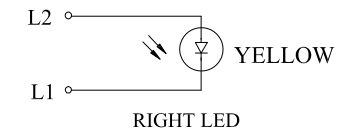
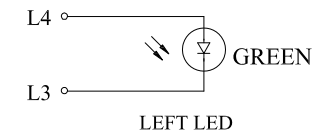
SHIELD : COPPER ALLOY

NICKEL PLATING OVERALL

2. CAVITY CONFIRMS TO FCC RULES AND REGULATIONS

PART 68,SUBPART F.

3. OPERATING TEMPERATURE:0°C TO 70°C



ORDER INFORMATION :

M R J F - 2 1 T 8 1 2 R S

Matrix—RJ45 —

F : 10/100

G : 10/100/1000

N : Pure Connector

11:1X1 ; 21:2X1

12:1X2 ; 22:2X2

14:1X4 ; 24:2X4

16:1X6 ; 26:2X6

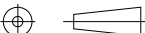
18:1X8 ; 28:2X8

1U:RJ+USB

RS : ROHS
—HF : Halogen Free

Number

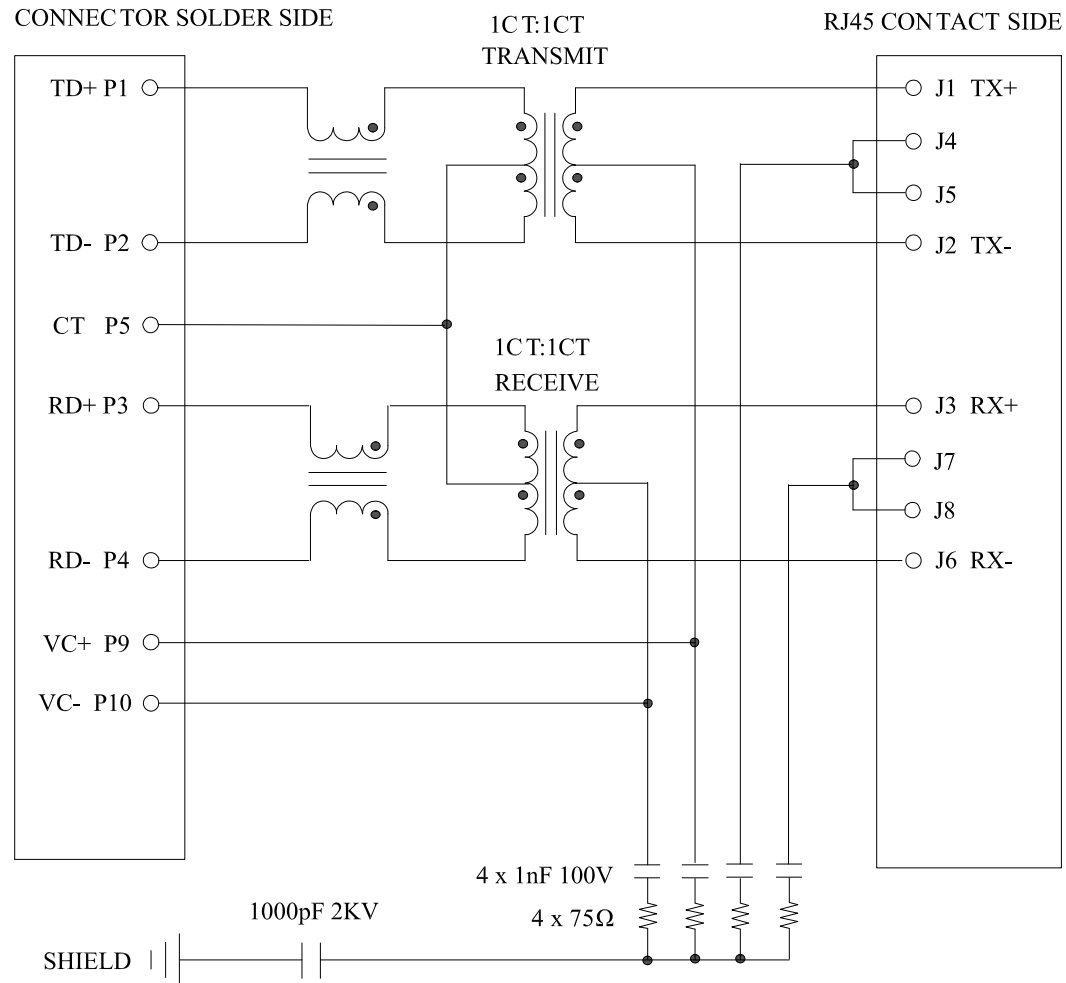
— S : SMD
T : THT

NO	VARIETY	QTY	MATERIAL	REMARK	
 Matrix Electronics Co.,Ltd					
TOLERANCE: X: X.X X.XX X.XXX ANGLE:		DESIGN BY : Phebe Su CHECKED BY: Vicky Hsieh		DATE : 2018/04/03 DATE : 2018/04/03	
±0.38 ±0.25 ±0.13 ±3°		APPROVED BY1: Richard Hsieh APPROVED BY2: Richard Hsieh		DATE : 2018/04/03 DATE : 2018/04/03	
		MOLD NO. NA		UNIT: mm [inch] DRAW NO.	
SCALE:1:1		SIZE:A4		SHEET NO. 1 OF 2	

GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2018/02/23	Phebe Su
A1			Change PBT to PA66	2018/04/03	Phebe Su

2. SCHEMATIC :



3. ELECTRICAL CHARACTERISTICS :

TEST NOTES:(25°±5°C)

- TR:(100KHz,0.1V); 100%
PINS:(P1,P2):(J1,J2)=1:1±3%;(P3,P4):(J3,J6)=1:1±3%
- LX:(100KHz,100mV,8mA,DC Bias) 100%
PINS:(J1,J2)=350uH MINIMUM
PINS:(J3,J6)=350uH MINIMUM
- DCR: 100%
PINS:(J1,J2),(J3,J6)=1.2Ω MAXIMUM
- HIPOT: 100%
PINS(P1,P2)TO(J1,J2),(P3,P4)TO(J3,J6)=1500VAC
FOR 60 SECONDS
- INSERTION LOSS:
-1.0dB MAXIMUM AT 1MHz TO 100MHz;
- RETURN LOSS:
-18dB MINIMUM AT 1MHz TO 30MHz;
-16dB MINIMUM AT 30MHz TO 60MHz
-12dB MINIMUM AT 60MHz TO 80MHz
- CROSS TALK:
-30dB MINIMUM AT 1MHz TO 100MHz
- COMMON TO COMMON MODE REJECTION:
-30dB MINIMUM AT 1MHz TO 100MHz

NO	VARIETY	QTY	METERIAL	REMARK
 Matrix Electronics Co.,Ltd				
TOLERANCE: X:X ±0.38 X:XX ±0.25 X:XXX ±0.13 ANGLE: ±3°		DESIGN BY : Phebe Su	DATE : 2018/04/03	PART NAME: RJ45 2X1 10/100 POE
CHECKED BY: Vicky Hsieh		DATE : 2018/04/03	PART NO.	MRJF-21T812RS
APPROVED BY1: Richard Hsieh		DATE : 2018/04/03	MOLD NO.	NA
APPROVED BY2: Richard Hsieh		DATE : 2018/04/03	DRAW NO.	
SCALE:1:1 SIZE:A4			SHEET NO.	2 OF 2