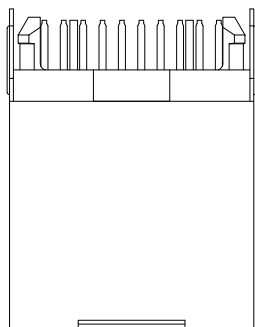


GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2016/02/18	Phebe Su

1. MECHANICAL DIMENSIONS :

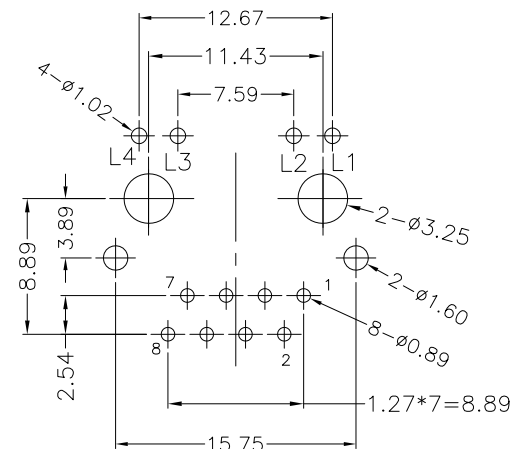


GREEN

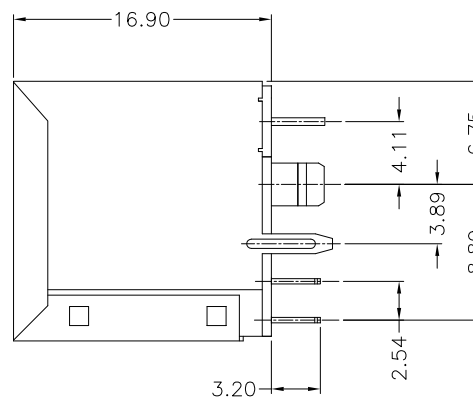
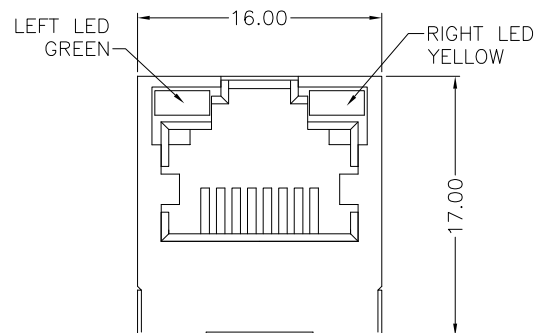
YELLOW

L4 —> L3

L2 —> L1



RECOMMENED PCB LAYOUT
COMPONENT SIDE
TOLERANCE ± 0.05



NOTES:

1. MATERIALS:

HOUSING: PBT, UL94V-0, BLACK
CONTACTS: PHOSPHOR BRONZE T=0.30
GOLD FLASH ON CONTACT AREA
80u" TIN ON SOLDER TAIL
SHELL: COPPER ALLOY T=0.20
NICKEL PLATING OVERALL

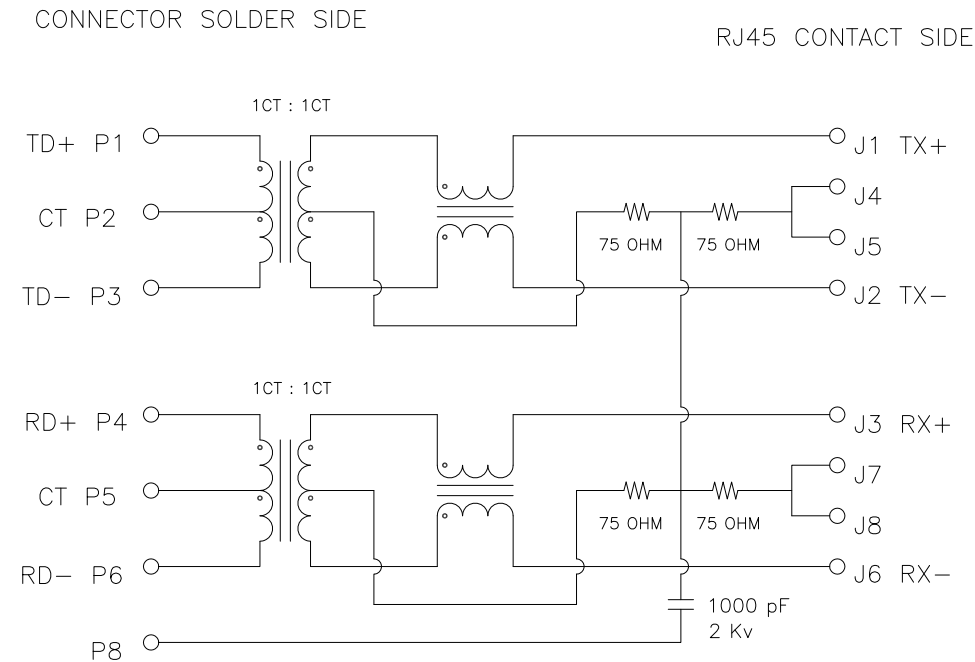
2. OPERATING TEMPERATURE: $-40^{\circ}\text{C} \sim +85^{\circ}\text{C}$
3. JACK CAVITY CONFORMS TO FCC RULES AND
REGULATIONS PART 68, SUBPART F

NO	VARIETY	QTY	MATERIAL	REMARK
 Matrix Electronics Co.,Ltd				
TOLERANCE: X.X ± 0.38 X.XX ± 0.25 X.XXX ± 0.13 ANGLE: $\pm 3^{\circ}$		DESIGN BY : Phebe Su	DATE : 2016/02/18	PART NAME: RJ45,CONNECTORS,W/LED THRU,HOLE10/100Mbps,FILTER
		CHECKED BY: Vicky Hsieh	DATE : 2016/02/18	PART NO. MRJF-11T440TRS
UNIT: mm [inch]		APPROVED BY1: Richard Hsieh	DATE : 2016/02/18	MOLD NO. NA
SCALE: 1:1 SIZE: A4		APPROVED BY2: Richard Hsieh	DATE : 2016/02/18	DRAW NO.
				SHEET NO. 1 OF 3

GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2016/02/18	Phebe Su

2. SCHEMATIC:



MATRIX PART NO:

Matrix-RJ45

M R J F - 1 1 T 440 T R S

RS: ROHS
HF: Halogen Free
Industrial Temperature

Series Number

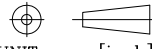
F: 10/100
G: 10/100/1000
N: Pure Connector

11:1X1 ; 21:2X1
12:1X2 ; 22:2X2
14:1X4 ; 24:2X4
16:1X6 ; 26:2X6
18:1X8 ; 28:2X8
1U:RJ+USB

S: SMD
T: THT

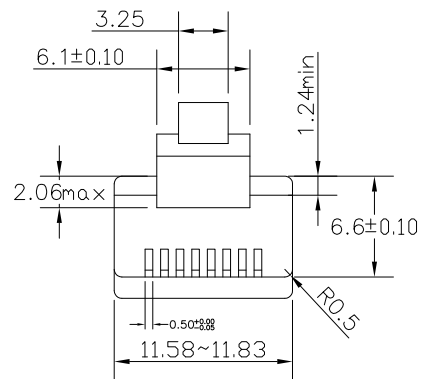
3. ELECTRICAL CHARACTERISTICS(25±5°C) :

- TR:(100KHz,0.1V); 100%
PINS:(P1,P3):(J1,J2)=1:1±3%;
PINS:(P4,P6):(J4,J5)=1:1±3%;
- LX:(100KHz,100mV,8mA, DC Bias) 100%
PINS:(P1,P3)=350uH MINIMUM
PINS:(P4,P6)=350uH MINIMUM
- DCR: 100%
PINS:(J1,J2),(J3,J6)=1.2 Ω MAXIMUM
- HIPOT: 100%
PINS(P1,P3)TO(J1,J2),(P4,P6)TO(J3,J6)=1500VAC FOR 60 SECONDS
- INSERTION LOSS:
-1.0dB MAXIMUM AT 1MHz TO 100MHz;
- RETURN LOSS:
-18dB MINIMUM AT 1MHz TO 30MHz;
-16dB MINIMUM AT 30MHz TO 60MHz
-12dB MINIMUM AT 60MHz TO 80MHz
- CROSS TALK:
-30dB MINIMUM AT 1MHz TO 100MHz
- COMMON TO COMMON MODE REJECTION:
-30dB MINIMUM AT 1MHz TO 100MHz

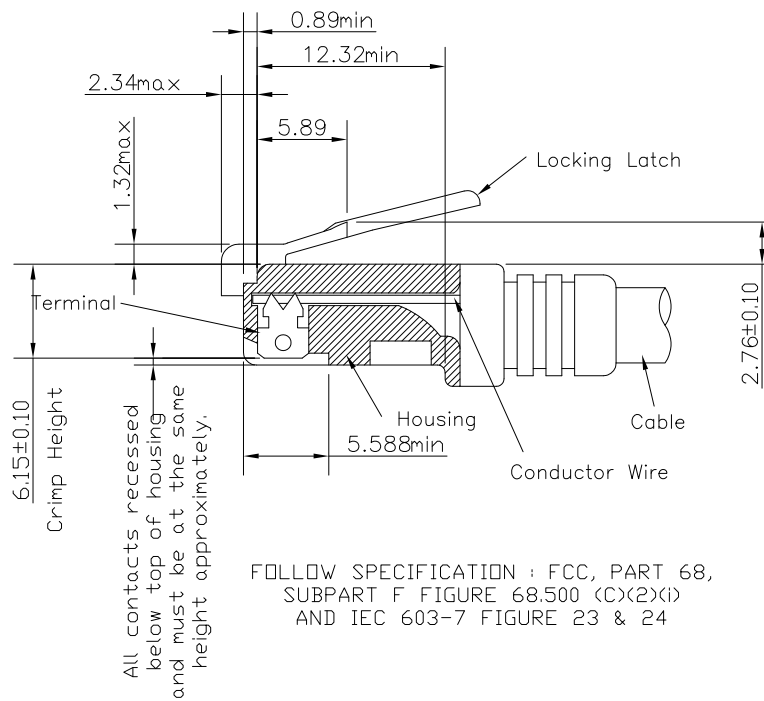
NO	VARIETY	QTY	METERIAL	REMARK
 Matrix Electronics Co.,Ltd				
TOLERANCE: X:X ±0.38 X:XX ±0.25 X:XXX ±0.13 ANGLE: ±3°		DESIGN BY : Phebe Su	DATE : 2016/02/18	PART NAME: RJ45,CONNECTORS,W/LED THRU,HOLE10/100MBPS,FILTER
 UNIT: mm [inch]		CHECKED BY: Vicky Hsieh	DATE : 2016/02/18	PART NO. MRJF-11T440TRS
SCALE:1:1		APPROVED BY1: Richard Hsieh	DATE : 2016/02/18	MOLD NO. NA
SIZE:A4		APPROVED BY2: Richard Hsieh	DATE : 2016/02/18	DRAW NO.
				SHEET NO. 2 OF 3

GP Component

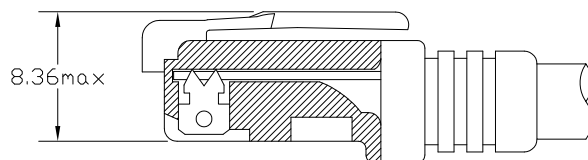
REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
AO			Initial	2016/02/18	Phebe Su



- * There must be no damage to housing or locking latch. There must be no nicks or cuts in cable.
- * Durability : 750 cycles generally



FOLLOW SPECIFICATION : FCC, PART 68,
SUBPART F FIGURE 68.500 (C)(2)(i)
AND IEC 603-7 FIGURE 23 & 24



FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F
FIGURE 68.500 (C)(2)(ii)

STANDARD MODULAR PLUG ASSEMBLY

NO	VARIETY	QTY	METERIAL	REMARK
 Matrix Electronics Co.,Ltd				
TOLERANCE: X:X ±0.38 X:XX ±0.25 X:XXX ±0.13 ANGLE: ±3°		DESIGN BY : Phebe Su	DATE : 2016/02/18	PART NAME: RJ45,CONNECTORS,W/LED THRU,HOLE10/100MBPS,FILTER
UNIT: mm [inch]		CHECKED BY: Vicky Hsieh	DATE : 2016/02/18	PART NO. MRJF-11T440TRS
SCALE:1:1 SIZE:A4		APPROVED BY1: Richard Hsieh	DATE : 2016/02/18	MOLD NO. NA
		APPROVED BY2: Richard Hsieh	DATE : 2016/02/18	DRAW NO.
				SHEET NO. 3 OF 3