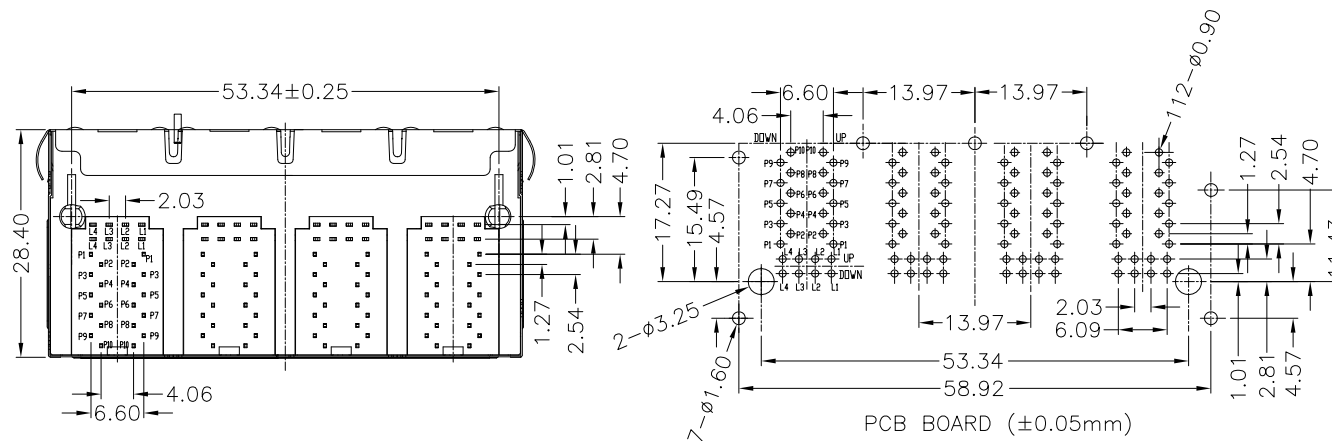


GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2014/12/29	Phebe Su

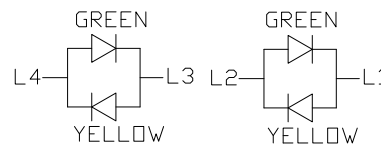
1. MECHANICAL DIMENSIONS :

PCB BOARD (± 0.05 mm)

LAYOUT

LEFT LED:

RIGHT LED:



NOTES:

1. MATERIALS:

HOUSING: PBT POLYESTER UL94V-0 MIXED GLASS FIBER. STANDARD COLOR-BLACK.

CONTACTS: 0.35mm THICK PHOS-BRONZE PLATED WITH G/F GOLD AND MATTE TIN IN SOLDER AREA.

SHIELD: 0.2mm THICK COPPER ALLOY, PLATED WITH NICKEL.

2. CAVITY CONFIRMS TO FCC RULES AND REGULATIONS PART 68,SUBPART F.

MATRIX PART NO:

Matrix-RJ45

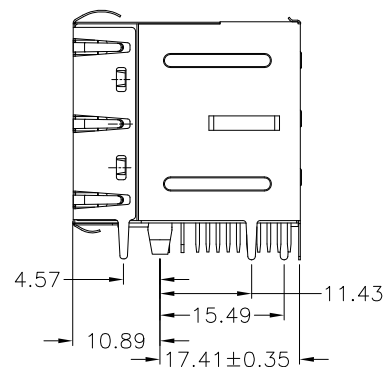
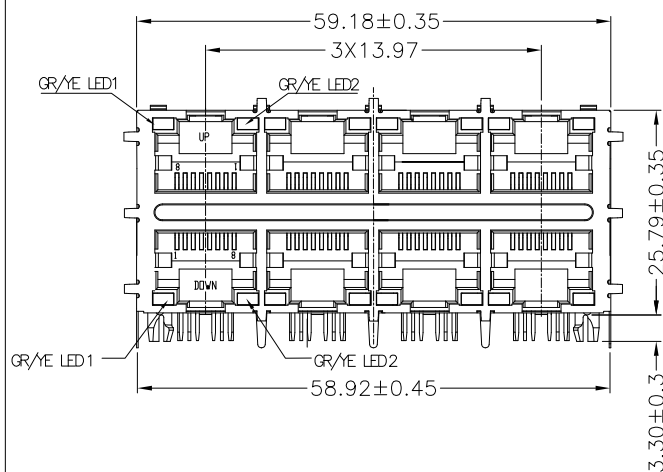
F : 10/100
G : 10/100/1000
N : Pure Connector

11:1X1 ; 21:2X1
12:1X2 ; 22:2X2
14:1X4 ; 24:2X4
16:1X6 ; 26:2X6
18:1X8 ; 28:2X8
1U:RJ+USB

RS : ROHS
HF : Halogen Free

Series Number

S : SMD
T : THT



NO	VARIETY	QTY	METERIAL	REMARK
	Matrix Electronics Co.,Ltd			
TOLERANCE: X: X X.X: ± 0.38 X.XX: ± 0.25 X.XXX: ± 0.13 ANGLE: $\pm 3^\circ$		DESIGN BY : Phebe Su	DATE : 2014/12/29	PART NAME: RJ45 2X4 CONN.W/LED 10/100/1000,FILTER
APPROVED BY1: Richard Hsieh		DATE : 2014/12/29	PART NO.	MRJG-24T206RS
APPROVED BY2: Richard Hsieh		DATE : 2014/12/29	MOLD NO.	NA
SCALE:1:1		SIZE:A4	DRAW NO.	
			SHEET NO.	1 OF 3

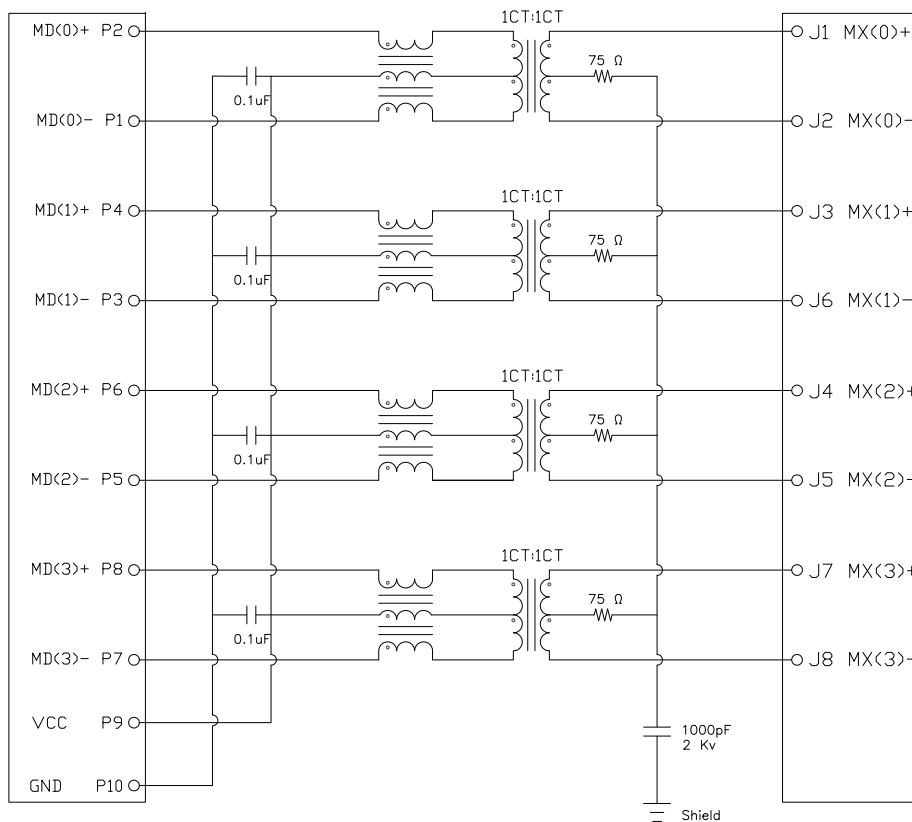
GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2014/12/29	Phebe Su

2. SCHEMATIC:

CONNECTOR SOLDER SIDE

RJ45 CONTACT SIDE



3. ELECTRICAL CHARACTERISTICS :

TEST NOTES:(25°±5°C)

1. TR:(100KHz,0.1V); 100%

PINS:(P2,P1):(J1,J2)=1:1±3%;(P4,P3):(J3,J6)=1:1±3%

PINS:(P6,P5):(J4,J5)=1:1±3%;(P8,P7):(J7,J8)=1:1±3%

2. LX:(100KHz,100mV,8mA, DC Bias) 100%

PINS:(P2,P1),(P4,P3),(P6,P5),(P8,P7)=350uH MINIMUM

3. DCR: 100%

PINS:(J1,J2),(J3,J6),(J4,J5),(J7,J8)=1.0 ohm MAXIMUM

4. HIPOT: 100%

PINS(P2,P1)TO(J1,J2),(P4,P3)TO(J3,J6)=1500VAC OR 2250VDC FOR 2 SECONDS

PINS(P6,P5)TO(J4,J5),(P8,P7)TO(J7,J8)=1500VAC OR 2250VDC FOR 2 SECONDS

5. INSERTION LOSS:

-1.0dB MAXIMUM AT 1MHz TO 100MHz;

6.RETURN LOSS:

-20dB MINIMUM AT 1MHz TO 30MHz;

-15dB MINIMUM AT 30MHz TO 60MHz

-10dB MINIMUM AT 60MHz TO 80MHz

7.CROSS TALK:

-30dB MINIMUM AT 1MHz TO 100MHz

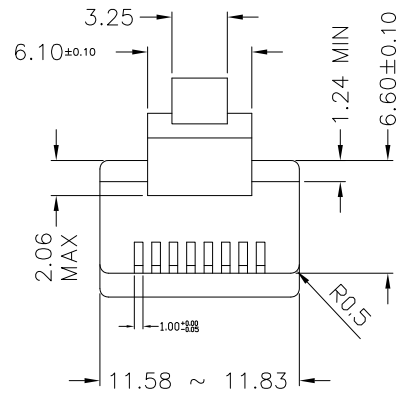
8.COMMON TO COMMON MODE REJECTION:

-30dB MINIMUM AT 1MHz TO 100MHz

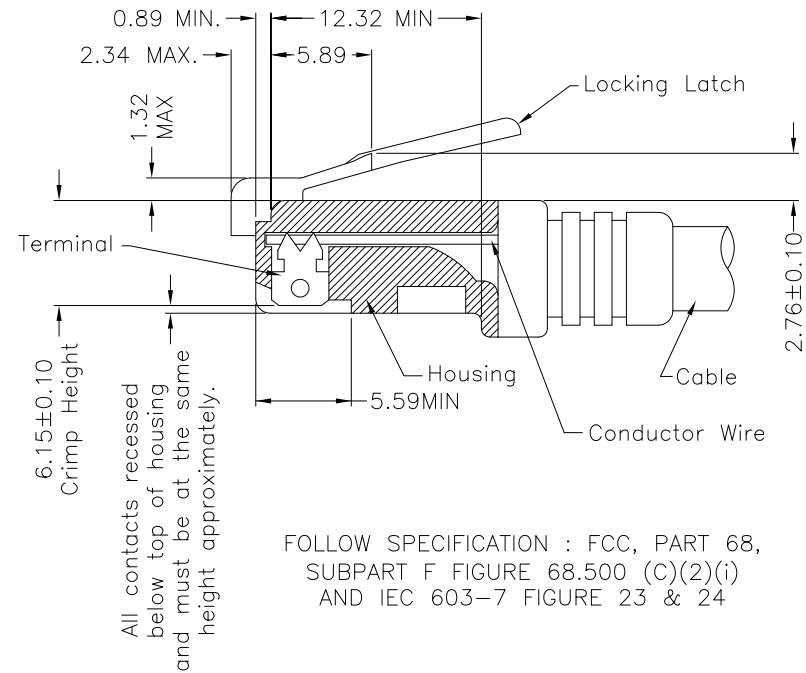
NO	VARIETY	QTY	METERIAL	REMARK
 Matrix Electronics Co.,Ltd				
TOLERANCE: X:X ±0.38 X:XX ±0.25 X:XXX ±0.13 ANGLE: ±3°		DESIGN BY : Phebe Su	DATE : 2014/12/29	PART NAME: RJ45 2X4 CONN.W/LED 10/100/1000.FILTER
CHECKED BY: Vicky Hsieh		DATE : 2014/12/29	PART NO.	MRJG-24T206RS
APPROVED BY1: Richard Hsieh		DATE : 2014/12/29	MOLD NO.	NA
APPROVED BY2: Richard Hsieh		DATE : 2014/12/29	DRAW NO.	
SCALE:1:1 SIZE:A4			SHEET NO.	2 OF 3

GP Component

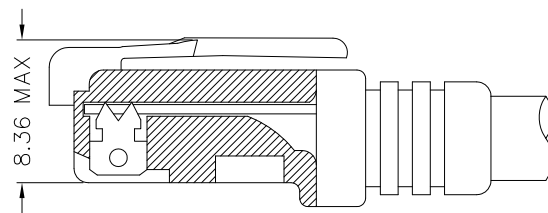
REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2014/12/29	Phebe Su



- * There must be no damage to housing or locking latch. There must be no nicks or cuts in cable.
- * Durability : 750 cycles generally



FOLLOW SPECIFICATION : FCC, PART 68,
SUBPART F FIGURE 68.500 (C)(2)(i)
AND IEC 603-7 FIGURE 23 & 24



FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F
FIGURE 68.500 (C)(2)(ii)

STANDARD MODULAR PLUG ASSEMBLY

NO	VARIETY	QTY	METERIAL	REMARK
 Matrix Electronics Co.,Ltd				
TOLERANCE: X.X ±0.38 X.XX ±0.25 X.XXX ±0.13 ANGLE: ±3°		DESIGN BY : Phebe Su	DATE : 2014/12/29	PART NAME: RJ45 2X4 CONN.W/LED 10/100/1000,FILTER
UNIT: mm [inch]		CHECKED BY: Vicky Hsieh	DATE : 2014/12/29	PART NO. MRJG-24T206RS
SCALE:1:1 SIZE:A4		APPROVED BY1: Richard Hsieh	DATE : 2014/12/29	MOLD NO. NA
		APPROVED BY2: Richard Hsieh	DATE : 2014/12/29	DRAW NO.
				SHEET NO. 3 OF 3