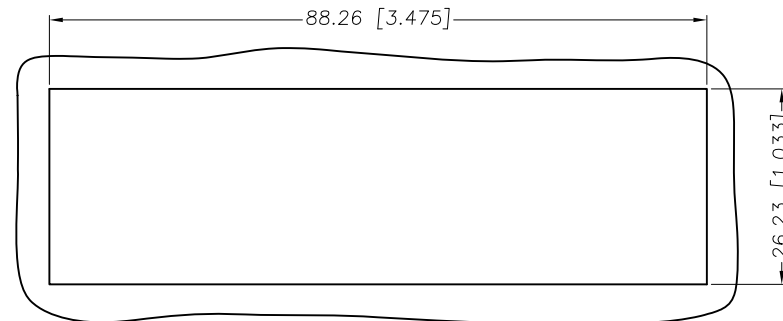
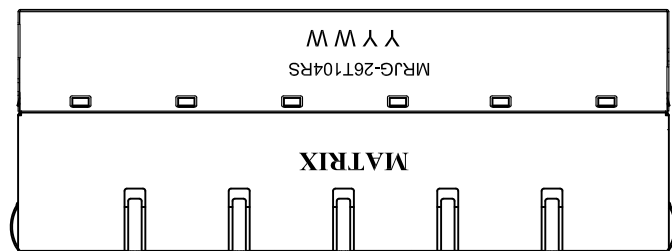
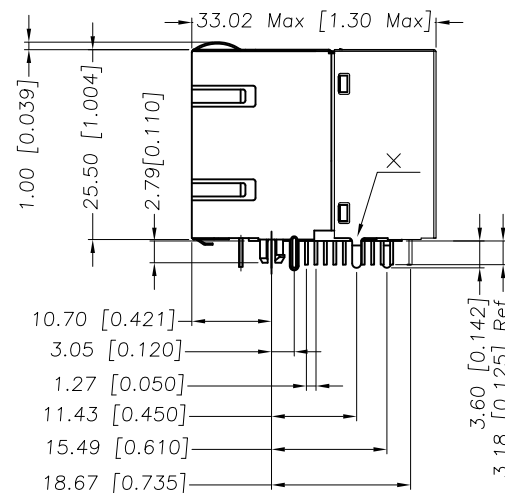
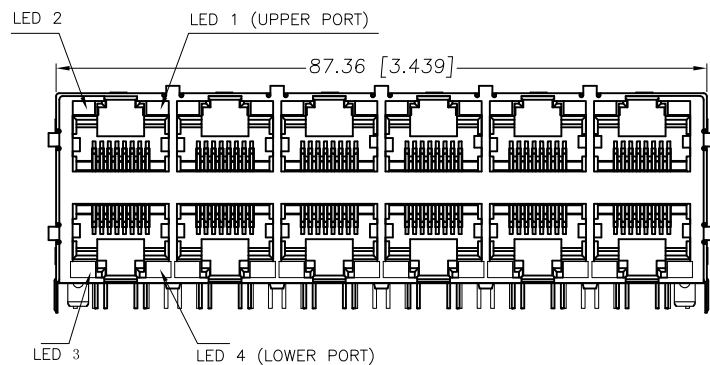
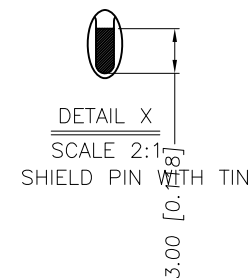


GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
AO			Initial	2013/12/02	Jamie Zie

1. MECHANICAL DIMENSIONS :

RECOMMENDED
PANEL CUTOUT DIMENSION

NOTES:

1. MATERIAL:

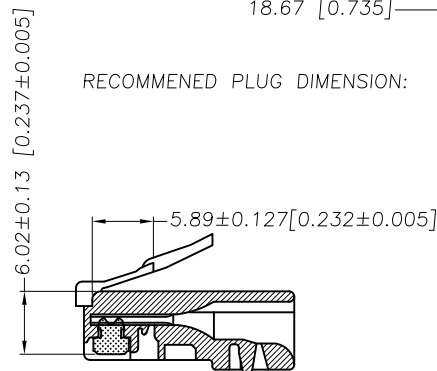
HOUSING-PBT POLYESTER UL-94V-0
MIXED GLASS FIBER.
STANDARD COLOR-BLACK

CONTACTS-0.35mm THICK PHOS-BRONZE PLATED
WITH 15u" GOLD AND TIN IN
SOLDER AREA, AND 50u" NICKEL
PLATING IN ALL AREA.

SHIELD - SUS301 T=0.20 mm WITH TIN ON
TERMINATION END.

2. CAVITY CONFIRMS TO FCC RULES AND REGULATIONS
PART 68,SUBPART F.

RECOMMENED PLUG DIMENSION:

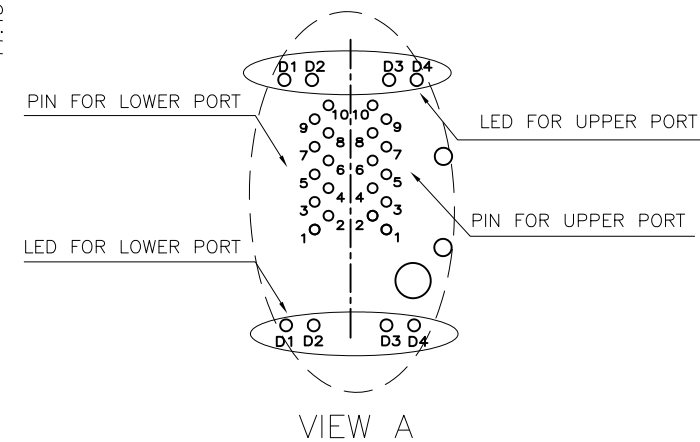
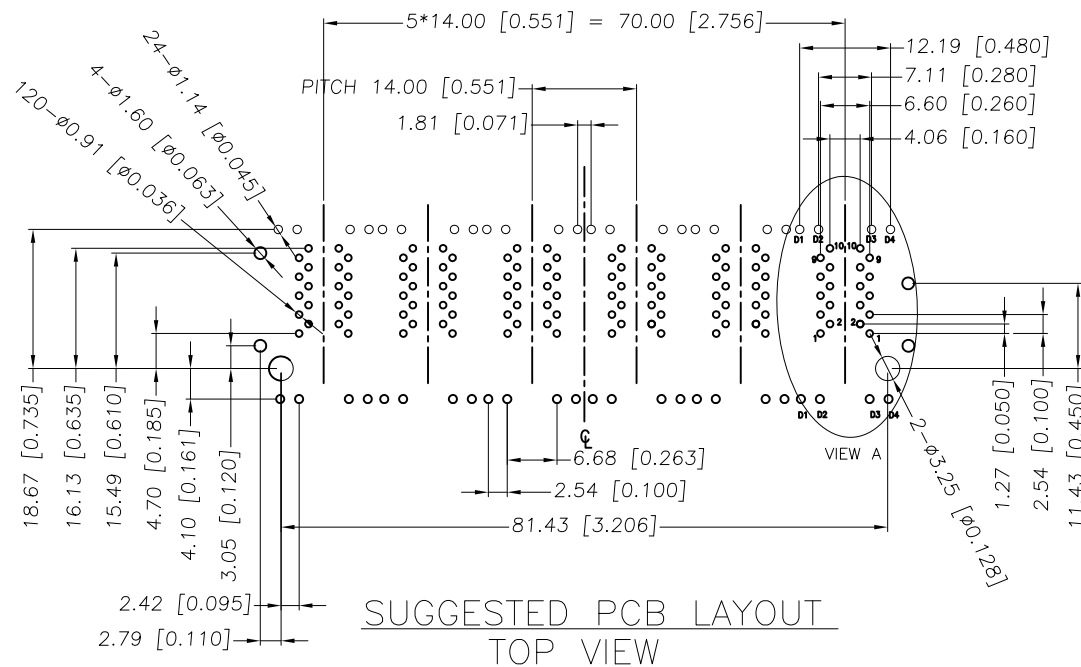


NO	VARIETY	QTY	METERIAL	REMARK
 Matrix Electronics Co.,Ltd				
TOLERANCE: X.XX ±0.38 X.XX ±0.25 X.XX ±0.13 ANGLE: ±5°		DESIGN BY : Jamie Zie	DATE : 2013/12/02	PART NAME: RJ45, W/LED, with 10/100/1000 Mbps LAN Magnetics10P,8C, Shielded, Thru Hole.
UNIT: mm [inch]		CHECKED BY: Vicky Hsieh	DATE : 2013/12/02	PART NO. MRJG-26T104RS
SCALE:1:1 SIZE:A4		APPROVED BY1: Richard Hsieh	DATE : 2013/12/02	MOLD NO. NA
		APPROVED BY2: Richard Hsieh	DATE : 2013/12/02	DRAW NO. SHEET NO.
				1 OF 3

GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
AO			Initial	2013/12/02	Jamie Zie

2. PCB LAYOUT :



3. LED CONFIGURATION :



Color	Lens Color	Vf(v) @20mA		Iv(mcd) @10mA		Viewing angle
		Min	Max	Min	Typ	
Green	White Diffused	1.7	2.6	3.0	7.0	152
YELLOW		1.7	2.6	1.8	4.0	152

NO	VARIETY	QTY	MATERIAL	REMARK
 Matrix Electronics Co.,Ltd				
TOLERANCE: X.XX ±0.38 X.XX ±0.25 X.XXX ±0.13 ANGLE: ±5°		DESIGN BY : Jamie Zie	DATE : 2013/12/02	PART NAME: RJ45, W/LED, with 10/100/1000 Mbps LAN Magnetics10P,8C, Shielded, Thru Hole.
CHECKED BY: Vicky Hsieh		DATE : 2013/12/02	PART NO. MRJG-26T104RS	
APPROVED BY1: Richard Hsieh		DATE : 2013/12/02	MOLD NO. NA	
APPROVED BY2: Richard Hsieh		DATE : 2013/12/02	DRAW NO.	
SCALE:1:1 SIZE:A4			SHEET NO.	2 OF 3

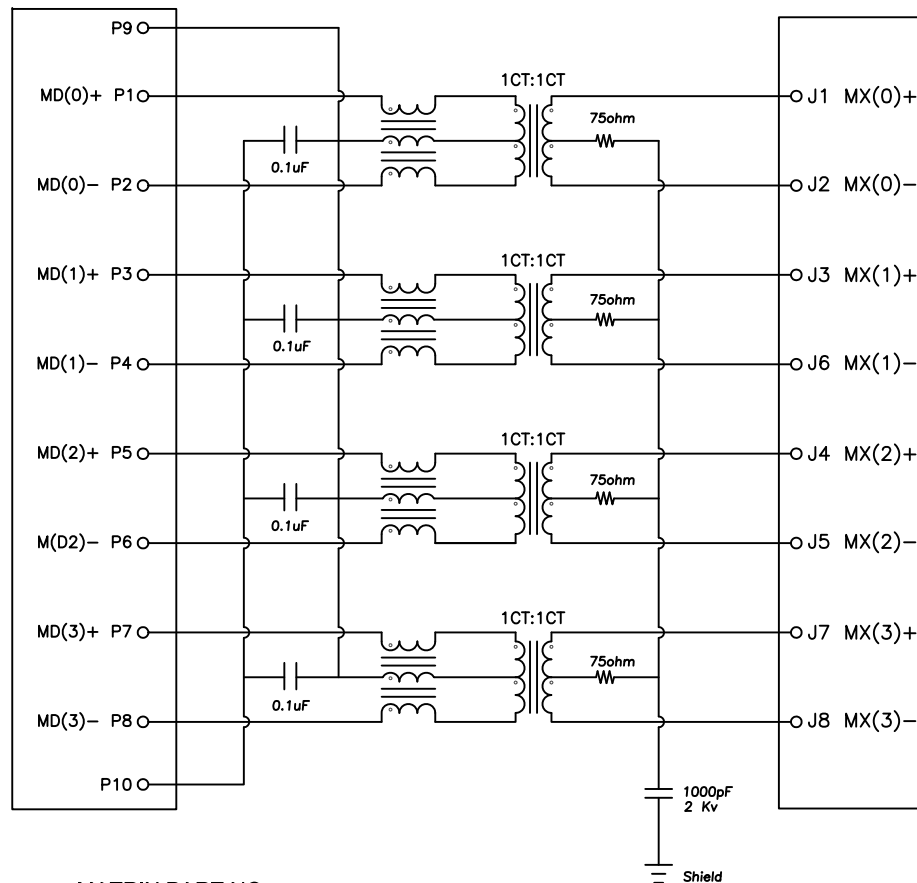
GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2013/12/02	Jamie Zie

4. SCHEMATIC:

CONNECTOR SOLDER SIDE

RJ45 CONTACT SIDE



MATRIX PART NO:

M R J G - 2 6 T 104 R S

Matrix-RJ45

F: 10/100
G: 10/100/1000
N: Pure Connector

RS: ROHS
HF: Halogen Free

Series Number

S: SMD
T: THT

11:1X1 ; 21:2X1
12:1X2 ; 22:2X2
14:1X4 ; 24:2X4
16:1X6 ; 26:2X6
18:1X8 ; 28:2X8
1U:RJ+USB

5. ELECTRICAL CHARACTERISTICS :

TEST NOTES:(25°±5°C)

- TR:(100KHz,0.1V); 100%
PINS:(P1,P2):(J1,J2)=1:1±3%;(P3,P4):(J3,J6)=1:1±3%
PINS:(P5,P6):(J4,J5)=1:1±3%;(P7,P8):(J7,J8)=1:1±3%
- LX:(100KHz,100mV,8mA, DC Bias) 100%
PINS:(P1,P2),(P3,P4),(P5,P6),(P7,P8)=350uH MINIMUM
- DCR: 100%
PINS:(J1,J2),(J3,J6),(J4,J5),(J7,J8)=1.0 MAXIMUM
- HIPOT: 100%
PINS(P1,P2)TO(J1,J2),(P3,P4)TO(J3,J6)=1500VAC FOR 2 SECONDS
PINS(P5,P6)TO(J4,J5),(P7,P8)TO(J7,J8)=1500VAC FOR 2 SECONDS
- INSERTION LOSS:
-1.0dB MAXIMUM AT 1MHz TO 100MHz;
- RETURN LOSS:
-20dB MINIMUM AT 1MHz TO 30MHz;
-15dB MINIMUM AT 30MHz TO 60MHz
-10dB MINIMUM AT 60MHz TO 80MHz
- CROSS TALK:
-30dB MINIMUM AT 1MHz TO 100MHz
- COMMON TO COMMON MODE REJECTION:
-30dB MINIMUM AT 1MHz TO 100MHz

NO	VARIETY	QTY	METERIAL	REMARK
 Matrix Electronics Co.,Ltd				
TOLERANCE: X.X X.X X.XX X.XXX ANGLE: ±3°		DESIGN BY : Jamie Zie	DATE : 2013/12/02	PART NAME: RJ45, W/LED, with 10/100/1000 Mbps LAN Magnetics10P,8C, Shielded, Thru Hole.
CHECKED BY: Vicky Hsieh		DATE : 2013/12/02	PART NO.	MRJG-26T104RS
APPROVED BY1: Richard Hsieh		DATE : 2013/12/02	MOLD NO.	NA
APPROVED BY2: Richard Hsieh		DATE : 2013/12/02	DRAW NO.	
SCALE:1:1 SIZE:A4			SHEET NO.	3 OF 3