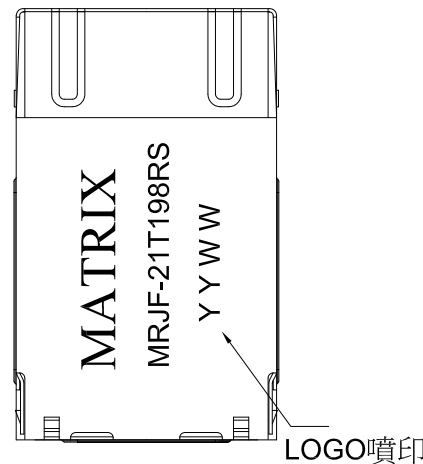
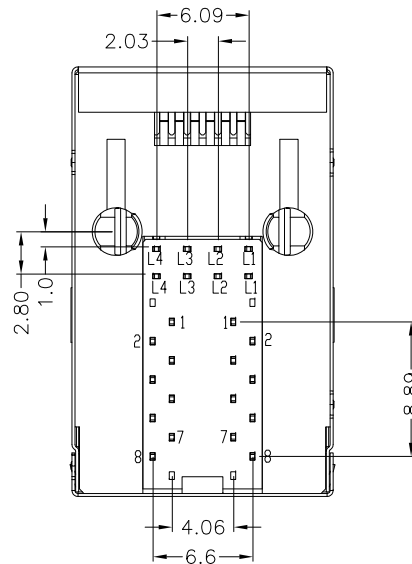
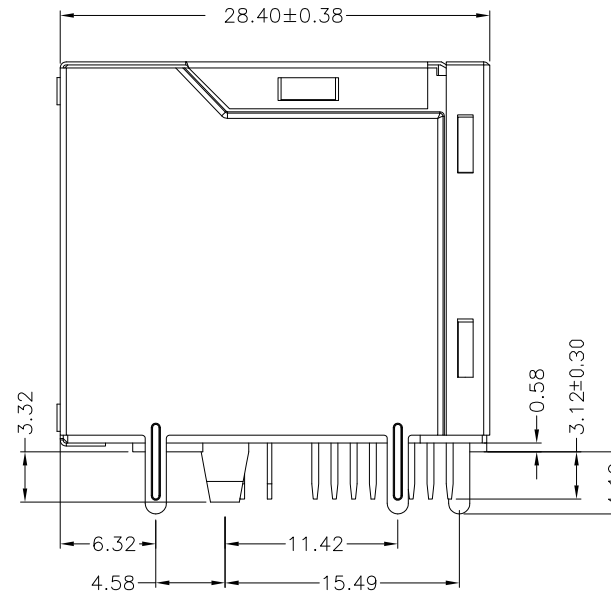
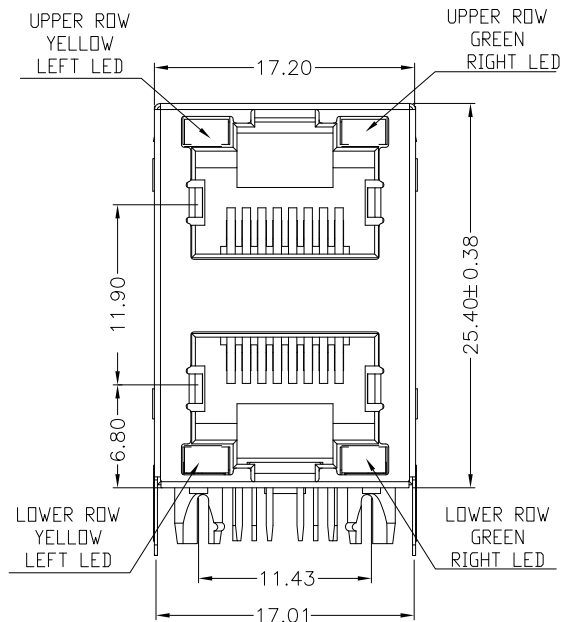


GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2014/12/17	Phebe Su

1. Mechanical:



NOTES:

1.Material:

- 1.1>.HOUSING: PBT,UL94V-0,COLOR:BLACK;
- 1.2>.SHELL: Stainless T=0.20mm;
- 1.3>.RJ45 TERMINAL: PHOSPHOR BRONZE T=0.30mm
PLATING: G/F PLATED ON CONTACT AREA,
100u" TIN PLATED ON SOLDER AREA,
50u"~80u" NI UNDERPLATED OVERALL;
- 1.4>.TAIL TERMINAL: BRASS T=0.30mm
PLATING: 80u"~160u" TIN PLATED OVER,
50u"~100u" NI UNDERPLATED OVERALL.

2.OVER:

All materials must meet Greenconn requirement for environment protection.

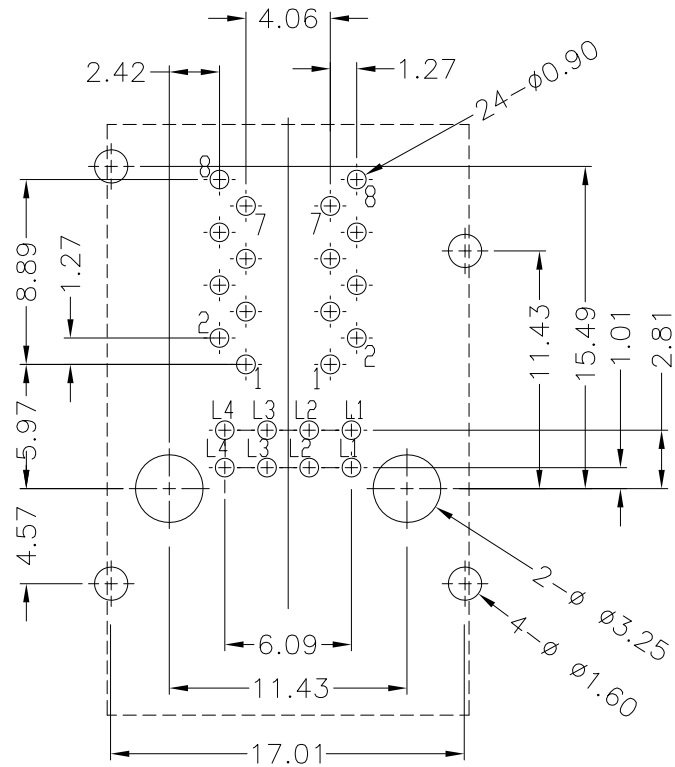
ORDER INFORMATION

MRJ	F	-	21	T	198	RS	
							RS : ROHS HF : Halogen Free
							Series Number
							S : SMD T : THT
Matrix-RJ45							
F : 10/100 G : 10/100/1000 N : Pure Connector							
11:1X1 ; 21:2X1 12:1X2 ; 22:2X2 14:1X4 ; 24:2X4 16:1X6 ; 26:2X6 18:1X8 ; 28:2X8 1U:RJ+USB							

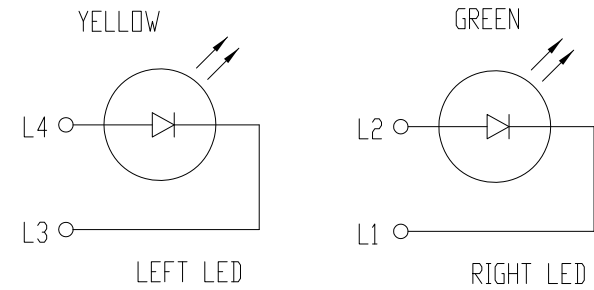
NO	VARIETY	QTY	MATERIAL	REMARK
 Matrix Electronics Co.,Ltd				
TOLERANCE: X.X ±0.38 X.XX ±0.25 X.XXX ±0.13 X.X*±2° X.X*±1°		DESIGN BY : Phebe Su	DATE : 2014/12/17	PART NAME: RJ45 2X1 CONN,W/LED 10/100,FILTER
CHECKED BY: Vicky Hsieh		DATE : 2014/12/17	PART NO.	MRJF-21T198RS
APPROVED BY1: Richard Hsieh		DATE : 2014/12/17	MOLD NO.	NA
APPROVED BY2: Richard Hsieh		DATE : 2014/12/17	DRAW NO.	
SCALE:1:1 SIZE:A4			SHEET NO.	1 OF 4

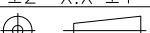
REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2014/12/17	Phebe Su

2. PCB LAYOUT:

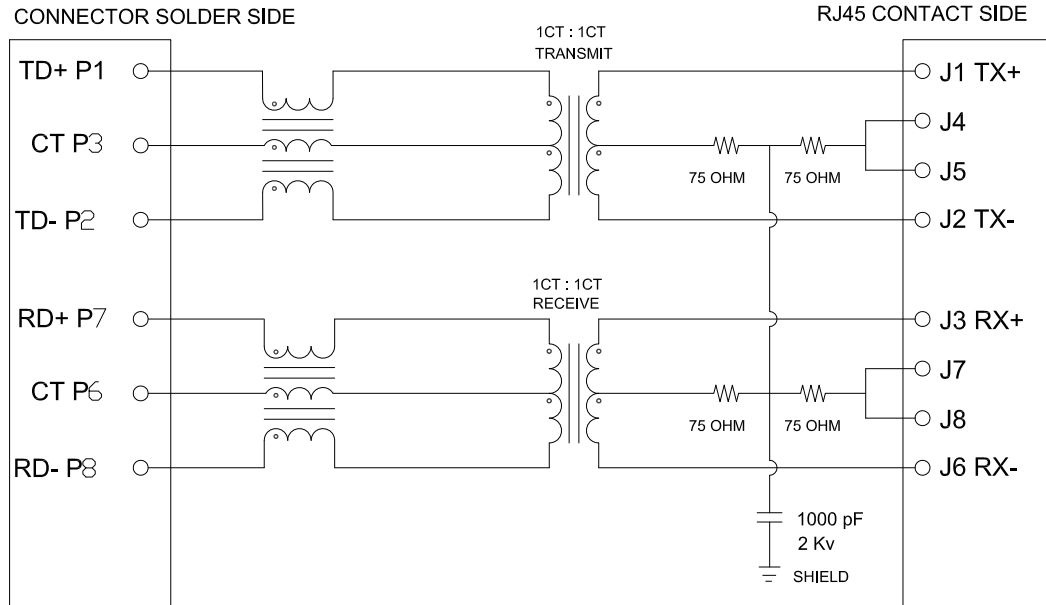


3. LED:



NO	VARIETY	QTY	MATERIAL	REMARK
 Matrix Electronics Co.,Ltd				
TOLERANCE: X.X X.XX ±0.38 X.XX ±0.25 X.XXX ±0.13 X.* ±2° X.X* ±1°		DESIGN BY : Phebe Su	DATE : 2014/12/17	
		CHECKED BY: Vicky Hsieh	DATE : 2014/12/17	PART NAME: RJ45 2X1 CONN,W/LED 10/100,FILTER
 UNIT: mm [inch]		APPROVED BY1: Richard Hsieh	DATE : 2014/12/17	PART NO. MRJF-21T198RS
		APPROVED BY2: Richard Hsieh	DATE : 2014/12/17	MOLD NO. NA
SCALE:1:1		SIZE:A4	DRAW NO	
			SHEET NO.	2 OF 4

4. SCHEMATIC:



5. ELECTRICAL CHARACTERISTICS :

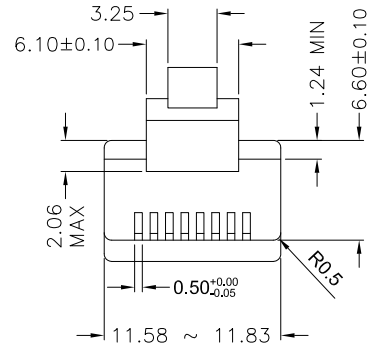
TEST NOTES:(25°±5°C)

- TR:(100KHz,0.1V); 100%
PINS:(P1,P2):(J1,J2)=1:1±3%;(P7,P8):(J3,J6)=1:1±3%
- LX:(100KHz,100mV,8mA, DC Bias) 100%
PINS:(P1,P2),(P7,P8)=350uH MINIMUM
- DCR: 100%
PINS:(J1,J2),(J3,J6)=1.2 OHM MAXIMUM
- HIPOT: 100%
PINS(P1,P2)TO(J1,J2),(P7,P8)TO(J3,J6)=1500VAC OR 2250VDC FOR 2 SECONDS
- INSERTION LOSS:
-1.0dB MAXIMUM AT 1MHz TO 100MHz;
- RETURN LOSS:
-20dB MINIMUM AT 1MHz TO 30MHz;
-15dB MINIMUM AT 30MHz TO 60MHz
-10dB MINIMUM AT 60MHz TO 80MHz
- CROSS TALK:
-30dB MINIMUM AT 1MHz TO 100MHz
- COMMON TO COMMON MODE REJECTION:
-30dB MINIMUM AT 1MHz TO 100MHz

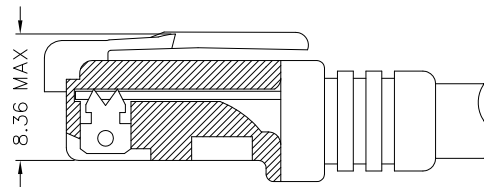
NO	VARIETY	QTY	METERIAL	REMARK
 Matrix Electronics Co.,Ltd				
TOLERANCE: X:X X.X ±0.38 X.XX ±0.25 X.XXX ±0.13 X.* ±2° X.X* ±1°		DESIGN BY : Phebe Su	DATE : 2014/12/17	PART NAME: RJ45 2X1 CONN,W/LED 10/100,FILTER
		CHECKED BY: Vicky Hsieh	DATE : 2014/12/17	PART NO. MRJF-21T198RS
		APPROVED BY1: Richard Hsieh	DATE : 2014/12/17	MOLD NO. NA
UNIT: mm [inch]		APPROVED BY2: Richard Hsieh	DATE : 2014/12/17	DRAW NO.
SCALE:1:1	SIZE:A4			SHEET NO. 3 OF 4

GP Component

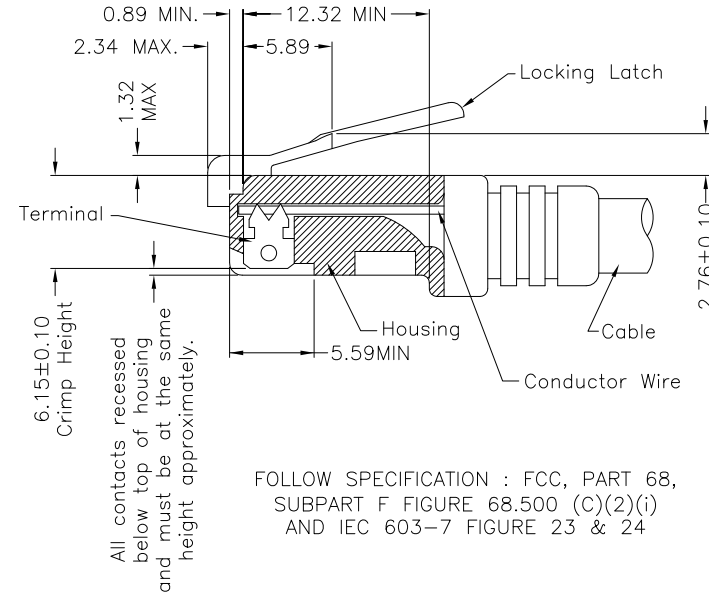
REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2014/12/17	Phebe Su



- * There must be no damage to housing or locking latch. There must be no nicks or cuts in cable.
- * Durability : 750 cycles generally



FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F
FIGURE 68.500 (C)(2)(ii)



FOLLOW SPECIFICATION : FCC, PART 68,
SUBPART F FIGURE 68.500 (C)(2)(i)
AND IEC 603-7 FIGURE 23 & 24

STANDARD MODULAR PLUG ASSEMBLY

NO	VARIETY	QTY	METERIAL	REMARK
 Matrix Electronics Co.,Ltd				
TOLERANCE: X:X X.X X.XX X.XXX X.X*±2°		DESIGN BY : Phebe Su CHECKED BY: Vicky Hsieh APPROVED BY1: Richard Hsieh UNIT: mm [inch] SCALE: 1:1 SIZE: A4		DATE : 2014/12/17 DATE : 2014/12/17 DATE : 2014/12/17 DATE : 2014/12/17
				PART NAME: RJ45 2X1 CONN./W/LED 10/100/FILTER PART NO. MRJF-21T198RS MOLD NO. NA DRAW NO. SHEET NO. 4 OF 4