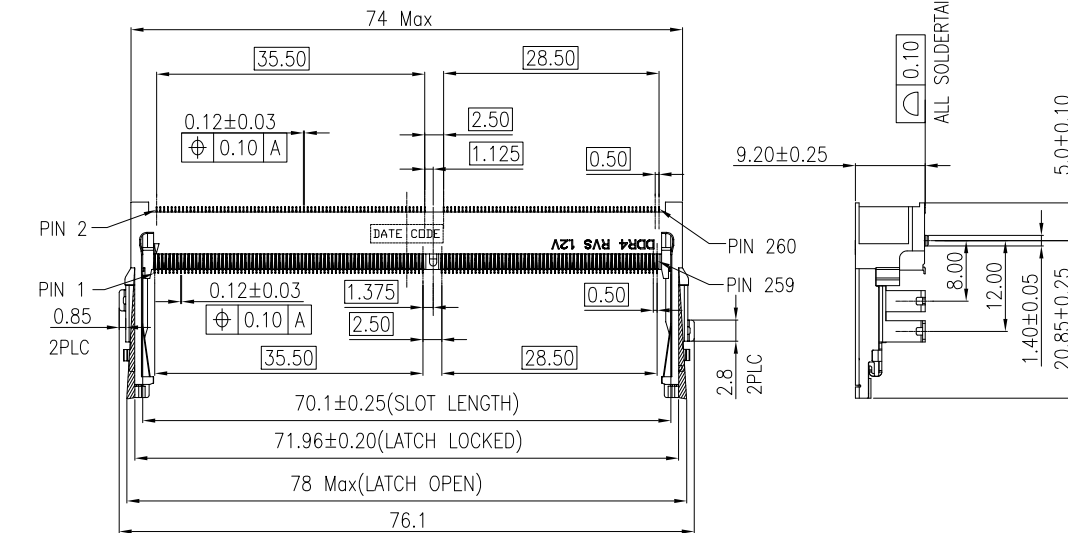
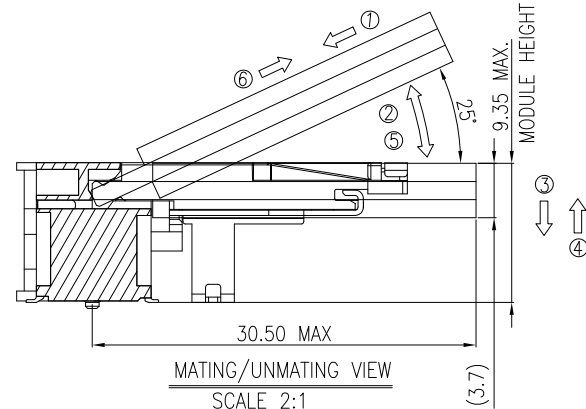
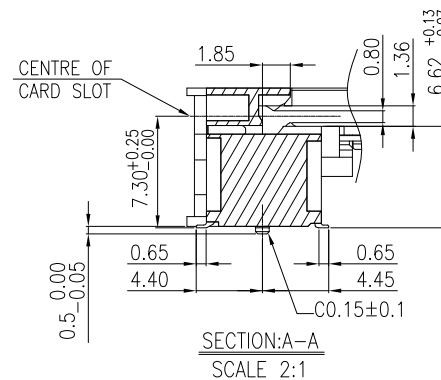
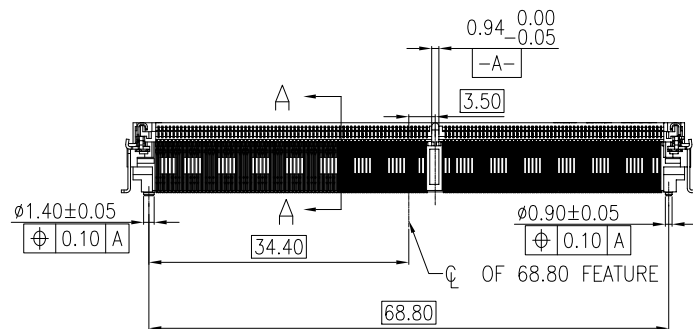


GP Component

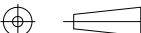
REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2015/11/02	Phebe Su



— PIN NUMBER CODE: 260 PINS

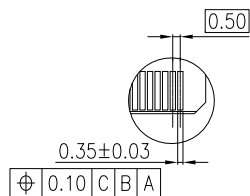
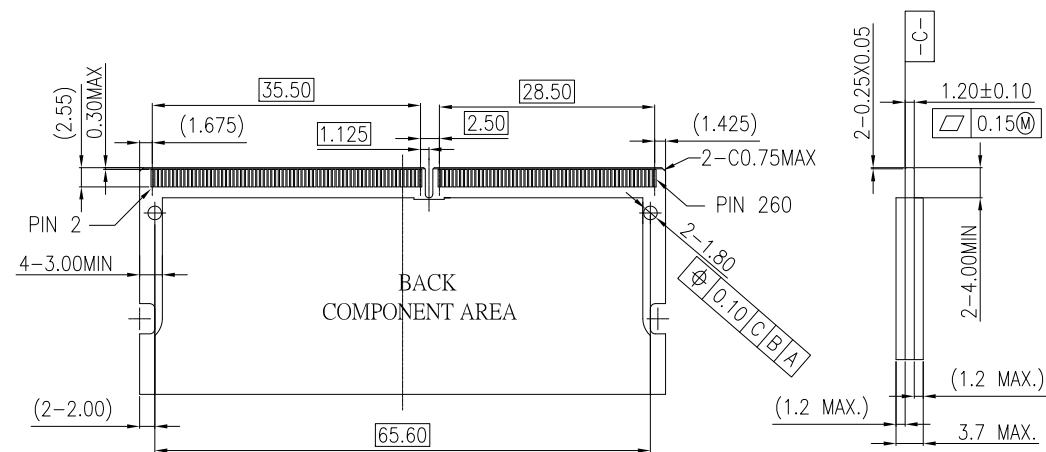
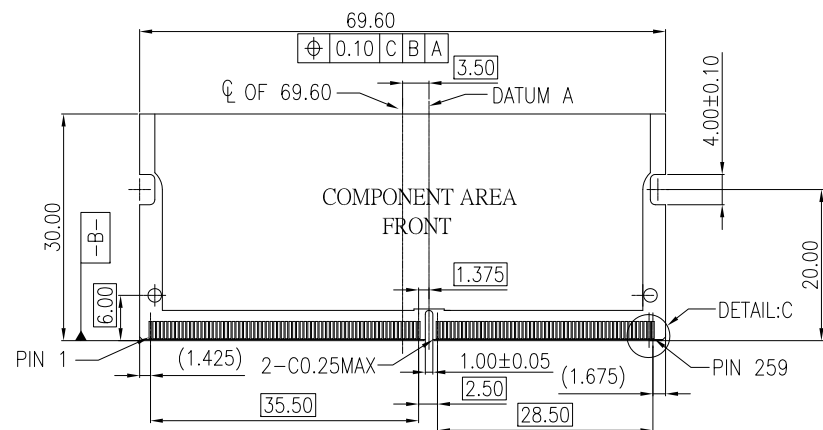


MATING: ① INSERTING(30') → ② ROTATING → ③ FIXED
UNMATING: ④ UNFIXED → ⑤ ROTATING → ⑥ WITHDRAWING

NO	VARIETY	QTY	MATERIAL	REMARK
 Matrix Electronics Co.,Ltd				
TOLERANCE: X.X ±0.25 X.XX ±0.15 X.XXX ±0.10 ANGLE: ±3°		DESIGN BY : Phebe Su	DATE : 2015/11/02	PART NAME: DDR4 SO DIMM 9.2H RVS
		CHECKED BY: Vicky Hsieh	DATE : 2015/11/02	PART NO. MDDR4-260X21
		APPROVED BY1: Richard Hsieh	DATE : 2015/11/02	MOLD NO. NA
UNIT: mm [inch]		APPROVED BY2: Richard Hsieh	DATE :	DRAW NO.
SCALE:1:1	SIZE:A4	Richard Hsieh	2015/11/02	SHEET NO. 1 OF 2

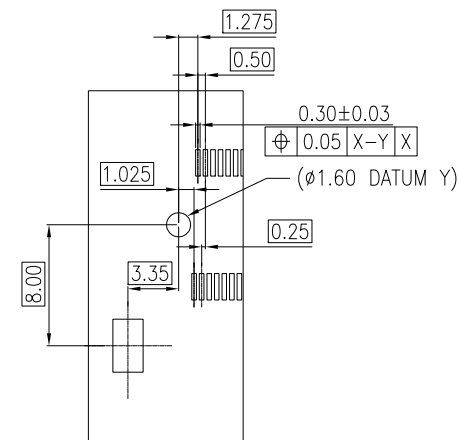
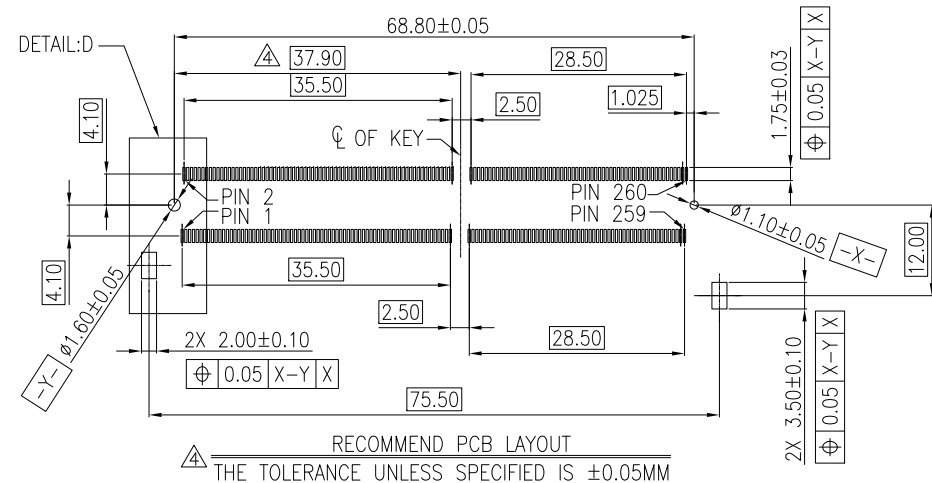
GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2015/11/02	Phebe Su

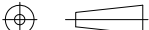


DETAIL: C

SCALE 2:1



DETAIL:D
SCALE 2:1

NO	VARIETY	QTY	MATERIAL	REMARK	
 Matrix Electronics Co.,Ltd					
TOLERANCE: X. ±0.25 X.X ±0.15 X.XX ±0.10 X.XXX ±0.10 ANGLE: ±3°		DESIGN BY : Phebe Su	DATE : 2015/11/02	PART NAME: DDR4 SO DIMM 9.2H RVS	
		CHECKED BY: Vicky Hsieh	DATE : 2015/11/02	PART NO.	MDDR4-260X21
		APPROVED BY1: Richard Hsieh	DATE : 2015/11/02	MOLD NO.	NA
UNIT: mm [inch]		APPROVED BY2: Richard Hsieh	DATE : 2015/11/02	DRAW NO.	
SCALE:1:1	SIZE:A4			SHEET NO.	2 OF 2