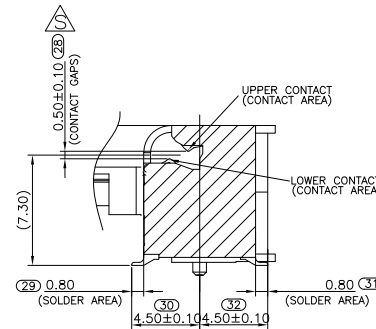
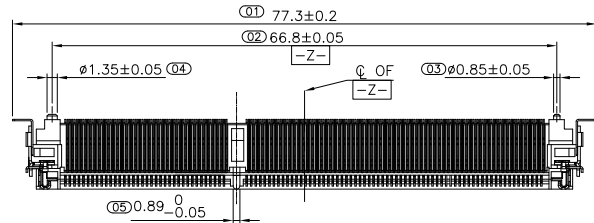


GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
AO			Initial	2015/11/02	Phebe Su

SECTIONAL A-A
SCALE 2:1

NOTES:

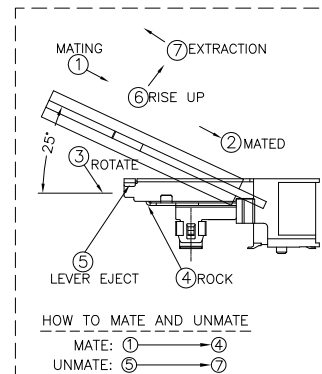
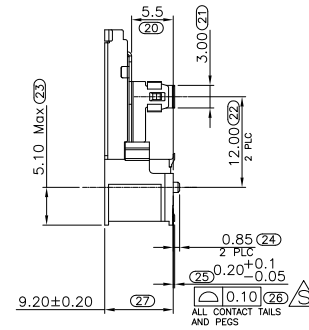
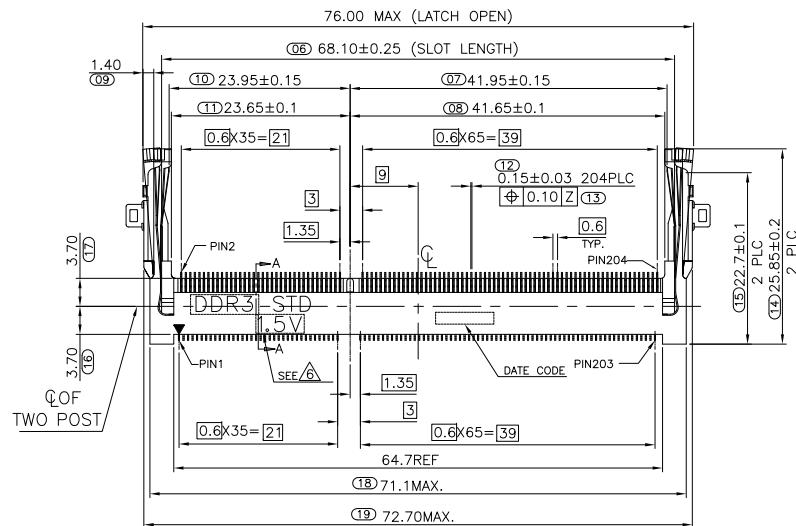
- MATERIAL:
 - HOUSING: LCP, UL94V-0; COLOR: BLACK.
 - CONTACTS: PHOSPHOR BRONZE
 - LATCH: STAINLESS STEEL
 - NUT: STAINLESS STEEL
 - FLOATING PEG: BRASS
- FINISH:
 - CONTACT:
GOLD (SEE TABLE) PLATED ON CONTACT AREA AND GOLD 2u" SOLDER AREA. 50u" MIN. NICKEL UNDERPLATING.
 - FLOATING PEG:
100u" MIN. MATTE-TIN. 50u" MIN. NICKEL UNDERPLATING.
- REFLOW SOLDER CAPABLE TO 260°C.
- SHOULD BE MEASURED BY CPK
- MAKED "Ø" DIMENSIONS NEED BE CONTROLLED IN FAI.

PART NUMBER:
MDDR3-204XX12

Series number

PLATING CODE:

- GOLD FLASH 4: Au15u"
 - Au5u" 5: Au20u"
 - Au10u" 6: Au30u"
- MARK & HSG COLOR:
- DDR3 STD 1.5V & BLACK
 - DDR3 STD & BLACK 2: STD & BLACK
 - DDR3 STD 1.5V & IVORY
 - DDR3 STD & IVORY 5: STD & IVORY
- PIN NUMBER CODE:
204: 204 PINS

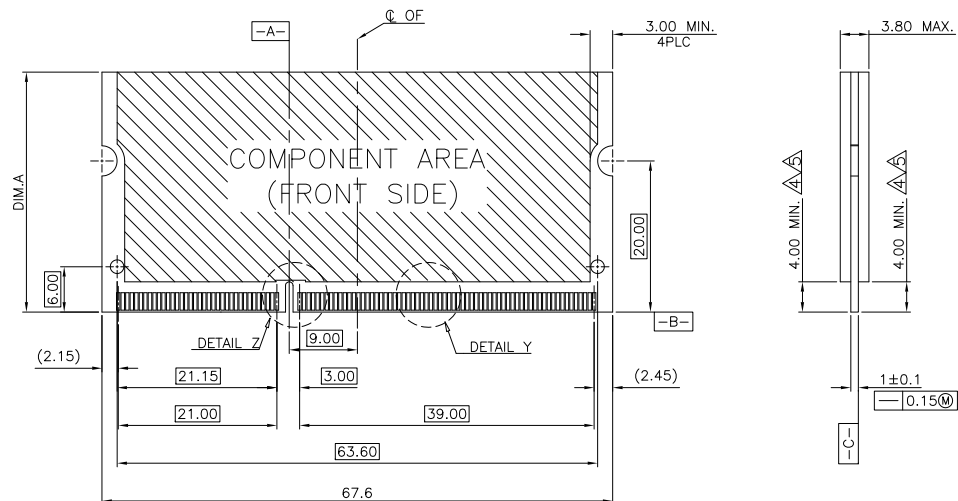
HOW TO MATE AND UNMATE
MATE: ① → ④
UNMATE: ⑤ → ⑦

RECOMMENDED PCB LAYOUT

NO	VARIETY	QTY	METERIAL	REMARK
Matrix Electronics Co.,Ltd				
TOLERANCE: X:X ±0.25 X:XX ±0.15 X:XXX ±0.10 ANGLE: ±3°		DESIGN BY : Phebe Su	DATE : 2015/11/02	PART NAME: DDR3 SO DIMM 9.2H STD ASSY
UNIT: mm [inch]		CHECKED BY: Vicky Hsieh	DATE : 2015/11/02	PART NO. MDDR3-204XX12
SCALE:1:1 SIZE:A4		APPROVED BY1: Richard Hsieh	DATE : 2015/11/02	MOLD NO. NA
		APPROVED BY2: Richard Hsieh	DATE : 2015/11/02	DRAW NO. SHEET NO. 1 OF 2

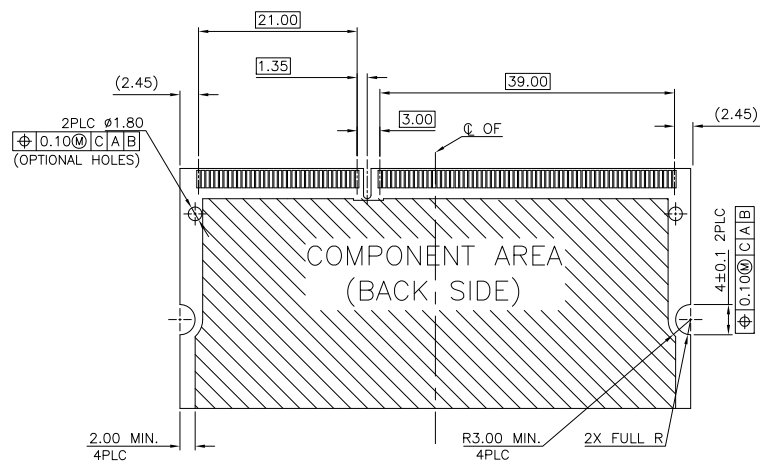
GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
AO			Initial	2015/11/02	Phebe Su

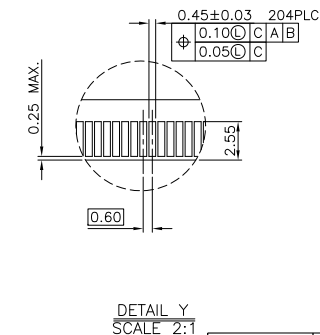
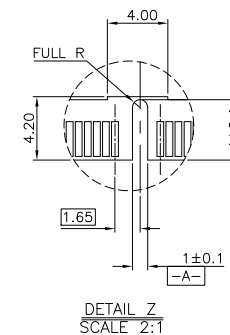


NOTE:

1. TOLERANCES ON ALL DIMENSIONS ± 0.15 UNLESS OTHERWISE SPECIFIED.
2. P.C.BOARD THICKNESS APPLIES ACROSS TABS AND INCLUDES PLATING AND/OR MENTAIZATION.
3. FINISH OF PAD:GOLD PLATING 0.00076MIN OVER Ni PLATING 0.002MIN.
4. DIMENSIONS APPLICABLE WHEN COMPONENTS MOUNTED ON BOTH SIDE. PCB THICKNESS NOT TO BE EXCEEDED OUTSIDE OF COMPONENT AREA.
5. BORDER OF COMPONENT AREA.
6. REFER TO JEDEC SPEC. MO-268 FOR COMPLETE MODULE.



RECOMMENDED MATING P.C.B CONFIGURATION



30.00	CA
31.75	BA
25.40	AA
DIM. A	SDRAM VARIATIONS

NO	VARIETY	QTY	METERIAL	REMARK
 Matrix Electronics Co.,Ltd				
TOLERANCE:		DESIGN BY :	DATE :	PART NAME:
X.X ±0.25		Phebe Su	2015/11/02	DDR3 SO DIMM 9.2H STD ASSY
X.XX ±0.15		CHECKED BY:	DATE :	PART NO.
X.XXX ±0.10		Vicky Hsieh	2015/11/02	MDDR3-204XX12
ANGLE: ±3°		APPROVED BY1:	DATE :	MOLD NO.
UNIT: mm [inch]		Richard Hsieh	2015/11/02	NA
SCALE:1:1 SIZE:A4		APPROVED BY2:	DATE :	DRAW NO.
		Richard Hsieh	2015/11/02	SHEET NO.
				2 OF 2