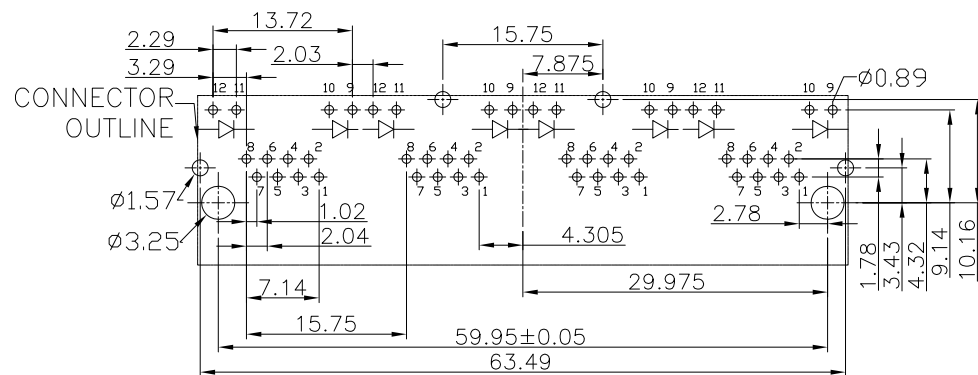
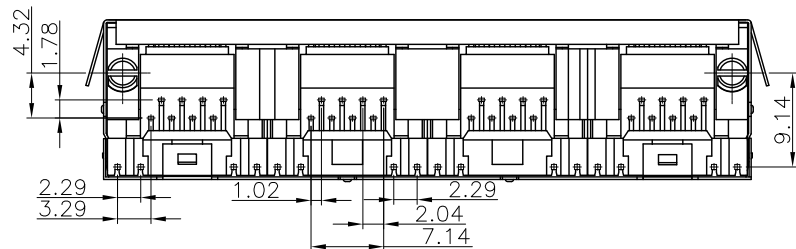
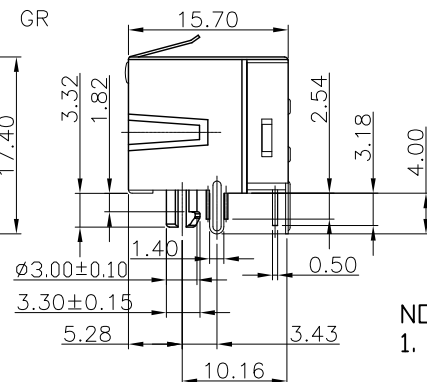
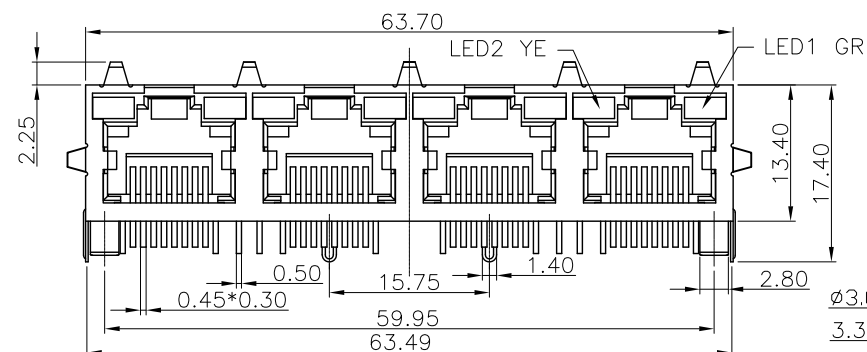
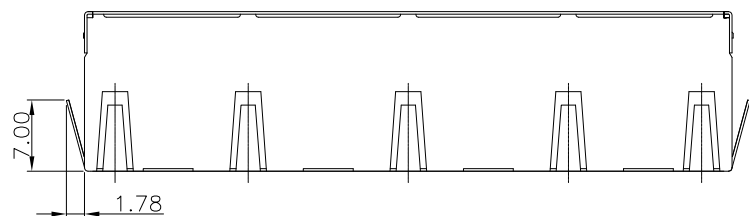


GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0			Initial	2013/6/24	Jamie Zie



PCB LAYOUT
TOLERANCE: ± 0.05

ORDER INFORMATION :

Matrix-RJ45

F : 10/100
G : 10/100/1000
N : Pure Connector

RS : R0HS
HF : Halogen Free

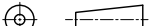
Series Number

S : SMD
T : THT

11:1X1 ; 21:2X1
12:1X2 ; 22:2X2
14:1X4 ; 24:2X4
16:1X6 ; 26:2X6
18:1X8 ; 28:2X8
1U:RJ+USB

NOTES:

1. MATERIALS:
 - HOUSING: HIGH TEMP. THERMOPLASTIC, PBT, UL 94V-0, BLACK.
 - CONTACTS: T=0.35 mm PHOSPHOR BRONZE, 50u" NICKEL UNDERPLATE ON ALL AREA, 100u" TIN AND G/F ON CONTACT AREA.
 - SHIELD: T=0.20 mm COPPER ZINC ALLOY PLATED
4. 標"*"為重點管控尺寸 WITH NICKEL
2. OPERATING TEMPERATURE: $-40^{\circ}\text{C} \sim +85^{\circ}\text{C}$
3. JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS
PART 68, SUBPART F

NO	VARIETY	QTY	MATERIAL	REMARK	
 Matrix Electronics Co.,Ltd					
TOLERANCE: X: X X.X ±0.38 X.XX ±0.25 X.XXX ±0.13 ANGLE: ±3°		DESIGN BY : Jamie Zie	DATE : 2013/6/24	PART NAME: RJ45 CONNECTOR JT 1*4 PORT	
		CHECKED BY: Sam Kuo	DATE : 2013/6/24	PART NO.	MRJN-14T70RS
		APPROVED BY1: Richard Hsieh	DATE : 2013/6/24	MOLD NO.	NA
UNIT: mm [inch]		APPROVED BY2: Richard Hsieh	DATE :	DRAW NO.	
SCALE:1:1	SIZE:A4	2013/6/24	SHEET NO.	1 of 1	