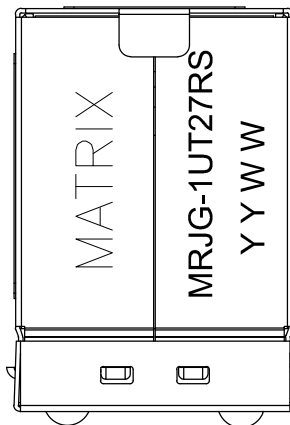
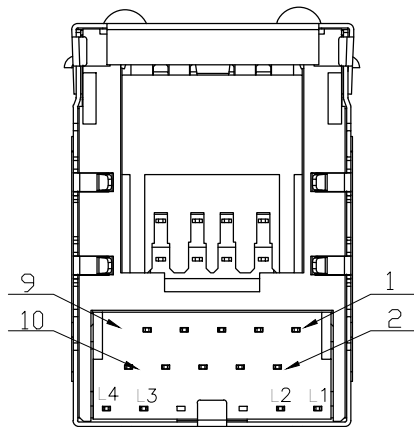
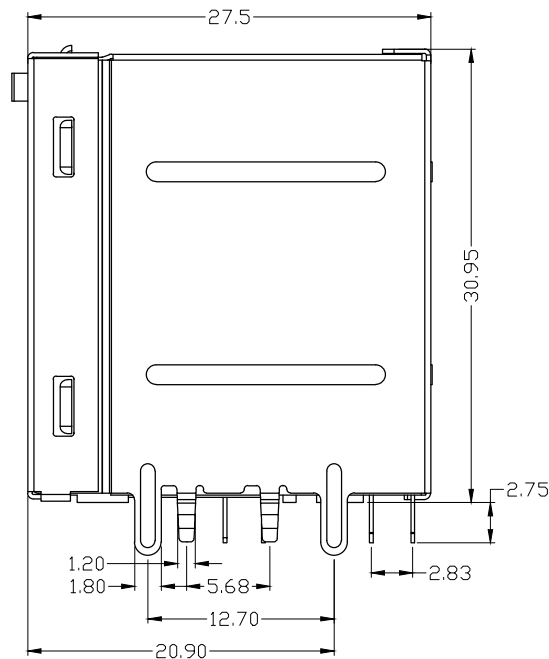
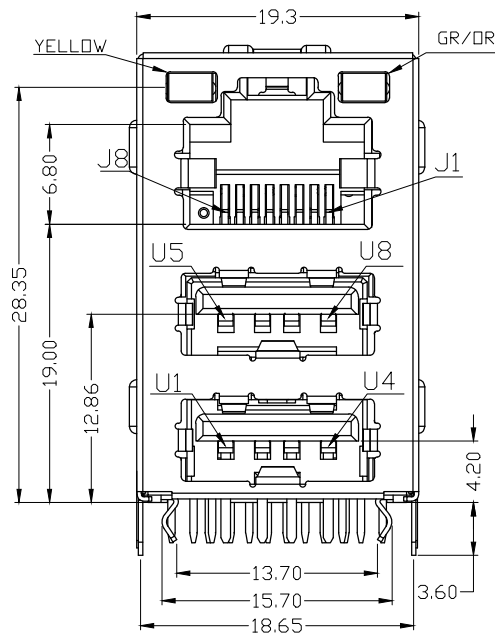


GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0				2012/9/28	Allen Chu
A1			Change LED color and OP temp. to -40~+85	2012/11/05	Allen Chu

1.機構尺寸:



TOP VIEW

NOTE:

1. MATERIDL:

RJ45

HOUSING: THERMOPLASTICS,UL94V-0;

SUPPORT: THERMOPLASTICS,UL94V-0;

COVER: THERMOPLASTICS,UL94V-0;

CONTACT: G/FGOLD PLATED ON CONTACT AREA

FRONT SHELL:STAINLESS THICKNESS=0.20mm

BACK SHELL: STAINLESS IRON

THICKNESS=0.20mm

USB

HOUSING:THERMOPLASTICS,UL94V-0

CONTACT:G/FGOLD PLATED ON CONTACT AREA

2.FINISH:

USB

CONTACT:G/FGOLD PLATED ON CONTACT AREA,

100u" MIN TIN PLATED ON SOLDER TALLS

AREA

50u"MIN,NICKEL UNDERPLATING OVER ALL,

RJ45

CONTACT:G/FGOLD PLATED ON CONTACT AREA,

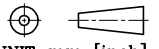
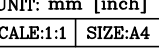
100U" MIN TIN PALTED ON SOLDER TAILS

AREA,

50U' MIN,NICKEL UNDERPLATING OVER ALL,

3. CAVITY CONFIRMS TO FCC RULES AND

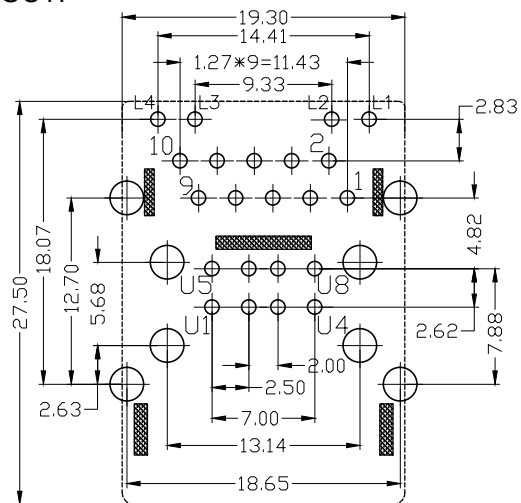
REGULATIONS PART 68,SUBPART F,

NO	VARIETY	QTY	METERIAL	REMARK
 Matrix Electronics Co.,Ltd				
TOLERANCE: X:X ±0.38 X:XX ±0.25 X:XXX ±0.13 ANGLE: ±1°		DESIGN BY : Allen Chu	DATE : 2012/9/28	PART NAME: RJ+USB2.0,W/LED Giga LAN
 UNIT: mm [inch]		CHECKED BY: David Chou	DATE : 2012/9/28	PART NO. MRJG-1UT27RS
 SCALE:1:1 SIZE:A4		APPROVED BY1: Richard Hsieh	DATE : 2012/10/01	MOLD NO. NA
		APPROVED BY2: Richard Hsieh	DATE : 2012/10/01	DRAW NO.
				SHEET NO. 1 OF 4

GP Component

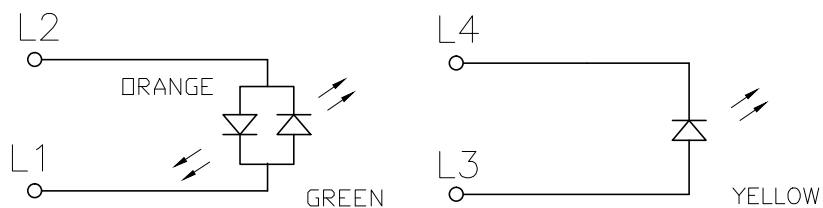
REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0				2012/9/28	Allen Chu
A1			Change LED color and DP temp. to -40~+85	2012/11/05	Allen Chu

2. PCB LAYOUT:



COMPONENT SIDE SHOWN

RECOMMENDED P.C.B. PATTERN LAYOUT



EmittingColor	λ_p (nm)	$V_f@I_f=20mA$		$I_R@V_r=5V$
YELLOW	565	1.7~2.6V		10 μA max
GREEN	565	1.7	2.6	10 μA max
ORANGE	610	1.7	2.6	10 μA max

ORDER INFORMATION

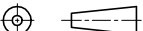
MRJG-1UT27RS

RS : ROHS
HF : Halogen Free

Series Number

Matrix-RJ45

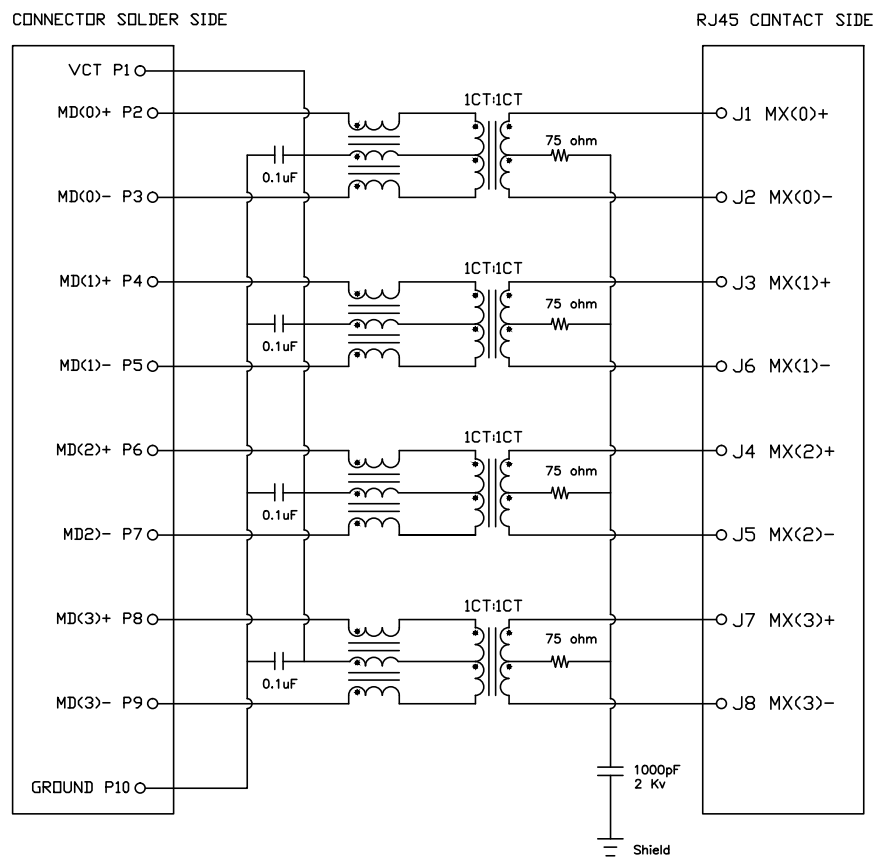
F : 10/100
G : 10/100/1000
N : Pure ConnectorS : SMD
T : THT11:1X1 ; 21:2X1
12:1X2 ; 22:2X2
14:1X4 ; 24:2X4
16:1X6 ; 26:2X6
18:1X8 ; 28:2X8
1U:RJ+USB

NO	VARIETY	QTY	MATERIAL	REMARK	
 Matrix Electronics Co.,Ltd					
TOLERANCE: X.X ±0.38 X.XX ±0.25 X.XXX ±0.13 ANGLE: ±1°		DESIGN BY : Allen Chu	DATE : 2012/9/28	PART NAME: RJ+USB2.0,W/LED Giga LAN	
 UNIT: mm [inch]		CHECKED BY: David Chou	DATE : 2012/9/28	PART NO.	MRJG-1UT27RS
		APPROVED BY1: Richard Hsieh	DATE : 2012/10/01	MOLD NO.	NA
SCALE:1:1		APPROVED BY2: Richard Hsieh	DATE : 2012/10/01	DRAW NO.	
SIZE:A4				SHEET NO.	2 OF 4

GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0				2012/9/28	Allen Chu
A1			Change LED color and OP temp. to -40~+85	2012/11/05	Allen Chu

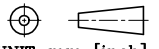
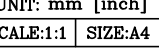
3 SCHEMATIC:



4. ELECTRICAL CHARACTERISTICS :

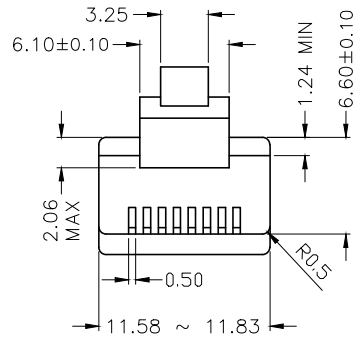
TEST NOTES:(25°±5°C)

- TR:(100KHz,0.1V); 100%
PINS:(P2,P3):(J1,J2)=1:1±3%;(P4,P5):(J3,J6)=1:1±3%
PINS:(P6,P7):(J4,J5)=1:1±3%;(P8,P9):(J7,J8)=1:1±3%
- LX:(100KHz,100mV,8mA, DC Bias) 100%
PINS:(P2,P3),(P4,P5),(P6,P7),(P8,P9)=350uH MINIMUM
- DCR: 100%
PINS:(J1,J2),(J3,J6),(J4,J5),(J7,J8)=1.2 Ω MAXIMUM
- HIPOT: 100%
PINS(P2,P3)TO(J1,J2),(P4,P5)TO(J3,J6)=1500VAC OR 2250VDC FOR 2 SECONDS
PINS(P6,P7)TO(J4,J5),(P8,P9)TO(J7,J8)=1500VAC OR 2250VDC FOR 2 SECONDS
- INSERTION LOSS:
-1.0dB MAXIMUM AT 1MHz TO 100MHz;
- RETURN LOSS:
-20dB MINIMUM AT 1MHz TO 30MHz;
-15dB MINIMUM AT 30MHz TO 60MHz
-10dB MINIMUM AT 60MHz TO 80MHz
- CROSS TALK:
-30dB MINIMUM AT 1MHz TO 100MHz
- COMMON TO COMMON MODE REJECTION:
-30dB MINIMUM AT 1MHz TO 100MHz

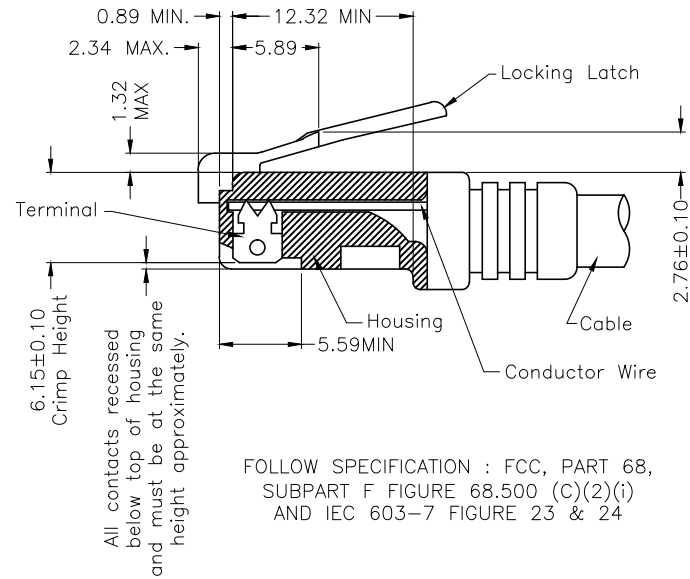
NO	VARIETY	QTY	MATERIAL	REMARK
 Matrix Electronics Co.,Ltd				
TOLERANCE: X:X ±0.38 X:XX ±0.25 X:XXX ±0.13 ANGLE: ±1°		DESIGN BY : Allen Chu	DATE : 2012/9/28	PART NAME: RJ+USB2.0,W/LED Giga LAN
 UNIT: mm [inch]		CHECKED BY: David Chou	DATE : 2012/9/28	PART NO. MRJG-1UT27RS
 SCALE:1:1 SIZE:A4		APPROVED BY1: Richard Hsieh	DATE : 2012/10/01	MOLD NO. NA
		APPROVED BY2: Richard Hsieh	DATE : 2012/10/01	DRAW NO.
				SHEET NO. 3 OF 4

GP Component

REV.	ECN NO.	LOCATIONS	DESCRIPTION	DATE	DESIGN
A0				2012/9/28	Allen Chu
A1			Change LED color and DP temp. to -40~+85	2012/11/05	Allen Chu

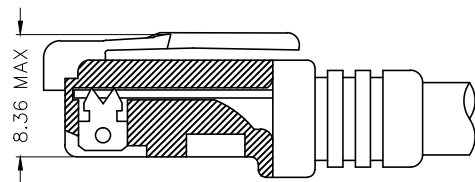


- * There must be no damage to housing or locking latch. There must be no nicks or cuts in cable.
- * Durability : 750 cycles generally

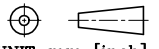
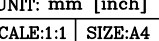


FOLLOW SPECIFICATION : FCC, PART 68,
SUBPART F FIGURE 68.500 (C)(2)(i)
AND IEC 603-7 FIGURE 23 & 24

STANDARD MODULAR PLUG ASSEMBLY



FOLLOW SPECIFICATION : FCC, PART 68, SUBPART F
FIGURE 68.500 (C)(2)(ii)

NO	VARIETY	QTY	METERIAL	REMARK
 Matrix Electronics Co.,Ltd				
TOLERANCE: X:X ±0.38 X:XX ±0.25 X:XXX ±0.13 ANGLE: ±1°		DESIGN BY : Allen Chu	DATE : 2012/9/28	PART NAME: RJ+USB2.0,W/LED Giga LAN
 UNIT: mm [inch]		CHECKED BY: David Chou	DATE : 2012/9/28	PART NO. MRJG-1UT27RS
 SCALE:1:1 SIZE:A4		APPROVED BY1: Richard Hsieh	DATE : 2012/10/01	MOLD NO. NA
		APPROVED BY2: Richard Hsieh	DATE : 2012/10/01	DRAW NO. SHEET NO. 4 OF 4